

Worcester Polytechnic Institute Electrical and Computer Engineering Department Methodologies for System Level Design and Modeling

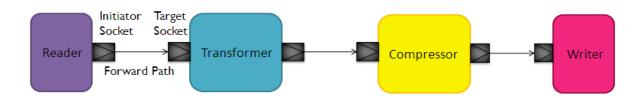
Online Offering – Fall 2010 Homework 7 – TLM2.0 Basic Concepts

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In this homework you are to design a data compression system. Processing elements in this system are to be written in C/C++ and the communications are to be handled with TLM 2.0 interfaces. The system has four processing elements for reading a block of data, transforming the data, compressing it, and writing the compressed block of data.

Problem Description:

In this design, the *reader* reads 32, 8-bit words from "input.txt" (this is considered a block of data). The *transformer* module uses BWT algorithm to transform data words to the form that is easier and more efficient to compress. The *compressor* uses run length encoding to compress the sorted (transformed) data. The compressed block is written to "output.txt" using writer. Implement this design using <u>TLM blocking transport interface</u>. You should write the description for the *reader*, *transformer*, *compressor* and writer in C/C++. You may have to define some internal memory buffers for the operation modules. To sort the table of BWT you can use any sorting algorithm you wish. The C/C++ code of run-length coding and the pseudo code of BWT are made available to you. Diagram below shows the structure of the processing elements and communications of the system you are designing.



Example:

Here is an example for transforming and compressing four 4-bit words. This shows the functionality of your system.

The *reader* reads the block shown below from "*input.txt*" and stores to its local memory. This block is transferred to *transformer*.

1010			
1111			
0101			
1010			

The steps of BWT algorithm performed by this module are shown in the following table. As shown, all possible rotations of the input data are obtained first, and they are sorted in ascending order. Output bits from left to right are obtained by taking the right-most bits of the sorted data. The output block will be transferred to the *compression* module.

BWT Transform				
Input	All rotations	Sort rows	Output	
1010111101011010	1010111101011010	0101010111101011	1111111010000110	
	0101011110101101	0101011110101101		
	1010101111010110	0101101010101111		
	0101010111101011	0101111010110101		
	1010101011110101	0110101010111101		
	11010101011111010	0111101011010101		
	0110101010111101	101010101111010 <mark>1</mark>		
	10110101010111110	1010101111010110		
	01011010101011111	1010110101010111		
	10101101010101111	1010111101011010		
	1101011010101011	1011010101011111 <mark>0</mark>		
	1110101101010101	1011110101101010		
	1111010110101010	11010101011111010		
	0111101011010101	1101011010101011		
	1011110101101010	1110101101010101		
	0101111010110101	111101011010101 <mark>0</mark>		

The output of the *compression* module will be:

711011402110 which translates to: 111100100011100001010010 in binary, assuming length 3 for each run.

The *writer* module receives the above block from the *compression* module and writes it to "output.txt".

The C source code of run length encoding and the pseudo code of BWT are shown below:

C source code of run length coding

```
function BWT (string s)
create a table, rows are all possible rotations of s
sort rows in ascending order
return (last column of the table)
```