## Programação de Sistemas Operativos de Tempo-Real Departamento de Engenharia Informática - Instituto Superior de Engenharia do Porto

## Exame Época Setembro - Duração: 60 minutos

Notas:		
<ul> <li>Em cada ur</li> </ul>	na das perguntas e	xiste apenas <b>uma</b> resposta correta
		nulam uma resposta correta
<ul> <li>Nota mínin</li> </ul>	na de 7.5 valores	•
Nº:	Nome:	
solving user prob	lems easier; iii) r v) scheduling res only only only	ctions of the operating system: i) execute user programs; ii) makin make the hardware convenient to use; iv) using the hardware in a ources among users.
2) The kernel kee (a) Process Contro (b) User Space Control (c) Task Control B (d) Memory page	<mark>l Block</mark> ntrol Block	ate of each task by using a structure called
	stem, device drivenel	l containing virtually the complete operating system, including ers, and memory management.
4) In process school CPU.  (a) Long-term school (b) Medium-term school (c) Short-term school (d) None of the about	eduler echeduler e <mark>duler</mark>	determines which ready process will be executed next by th
process has comp (a) Mutual exclusion (b) Hold and wait (c) Circular waits (d) No preemption	leted its task.	an be released only voluntarily by the process holding it, after that
6) The following of i) Mutual exclusion ii) Hold and wait (a) i, ii, and iii only (b) ii, iii, and iv on (c) i, iii, and iv only (d) All of these contributions	on iii) M iv) C / ly y	e present for a deadlock to be possible: No preemption Circular wait

7) refers to a situation in which a process is ready to execute but it is continuously denied access to the CPU in deference of other processes.  (a) Synchronization (b) Mutual exclusion (c) Deadlock (d) Starvation
8) State whether the following statement is true: i) it takes less time to terminate a thread than a process; ii) threads enhance efficiency in communication between different executing programs.  (a) i) is true, ii) is true  (b) i) is true, ii) is false  (c) i) is false, ii) is true  (d) i) is false, iii) is false
9) Which of the following statements about FCFS scheduling are true: i) it tends to favor CPU bound processes over I/O bound processes; ii) it may result inefficient use of both the processor and I/O devices; iii) it is an attractive alternative on its own for a single processor system.  (a) i and ii only (b) ii and iii only (c) i and iii only (d) All of these statements
10) establishes a set of scheduling queues and allocates processes to queues based on execution history and other criteria.  (a) Round robin  (b) Shortest Job First  (c) Shortest Remaining Time First  (d) Multilevel Feedback Scheduling
<ul><li>11) What is the blocked state of a process?</li><li>(a) When the process is able to make progress but not using the CPU</li><li>(b) When the process is unable to make progress until some task has been completed</li><li>(c) When the process is using the CPU</li><li>(d) None of the above</li></ul>
12) An optimal scheduling algorithm in terms of minimizing the average waiting time of a given set of processes is  (a) First come, first served  (b) Round robin  (c) Shortest job first  (d) Any preemptive priority scheduling algorithm
<ul> <li>13) If a set of processes cannot be scheduled by rate monotonic, then:</li> <li>(a) They can be scheduled by EDF</li> <li>(b) They cannot be scheduled by EDF, but they may be by other scheduling algorithm</li> <li>(c) They cannot be scheduled by any other known algorithm</li> <li>(d) It is impossible for a set of processes not to be scheduled by rate monotonic</li> </ul>
14) Interrupts are provided primarily to: (a) Improve processor utilization (b) Improve processor control (c) Improve processor speed (d) Improve processor execution

- 15) From the following, the most effective technique to keep dispatch latency low is to:
- (a) Provide a non-preemptive kernel
- (b) Provide a preemptive kernel
- (c) Switch processes in user space
- (d) Allow fewer processes in the run queue
- 16) Consider one is implementing a plane ticketing system where each plane flight is represented by a data structure which has a lock and a list of reservations on that flight. To reserve a plane ticket for a user-supplied list of multiple flights, the system iterates through the list and acquires the lock for each flight, then iterates through the reservation list and verifies seats are available on each flight, eventually modifying the list of reservations for each flight, then releases the locks for all the flights. When might this strategy result in a deadlock:
- (a) If two or more users are simultaneously trying to reserve the same sequence of flights and they specify the flights in the exact same order
- (b) If two or more users are simultaneously trying to reserve the same sequence of flights, but they specify the flights in different orders
- (c) If two users are simultaneously trying to reserve two different sequences of flights, and those sequences share the same last flight (but all the other flights are different)
- (d) None of the above
- 17) In the many-to-one threading model, if a thread makes a blocking system call:
- (a) The entire process will be blocked
- (b) A part of the process will stay blocked, while the rest is able to run
- (c) The entire process will run
- (d) None of the above
- 18) Which changes to a system are likely to improve a system's throughput?
- (a) Using a simpler scheduler that takes less time to decide which process to run
- (b) Switching from a non-preemptive to a preemptive scheduling algorithm
- (c) Making context switches take more time
- (d) None of the above
- 19) Which of the following are performance issues that the L4 microkernel-based designs is likely to experience compared to a monolithic kernel design like Linux?
- (a) Writing to a file is likely to require more kernel/user mode switches and/or context switches than in a monolithic kernel design
- (b) Microkernel designs make it harder for user-mode code to ensure that data is kept in physical memory and not swapped out to disk
- (c) Sending data between two processes is likely to require more kernel/user mode switches and/or context switches than in a monolithic kernel design
- (d) All of the above
- 20) In lecture, we discussed multi-level feedback queue schedulers that attempts to adjust a process's priority based on the length of its CPU bursts. In such a scheduler, programs at the highest priority:
- (a) Have shorter timeslices than programs at any other priority
- (b) Are not allowed to use more total CPU time than the programs at lower priority
- (c) Will be demoted to a lower priority if they stop using the CPU before the end of their timeslice
- (d) None of the above