Soo Kim – Résumé

Address	16, Seorim 11ga-gil, Gwanak-gu, Seoul, 08840, Republic of Korea	**Nationality**	Republic of Korea
		Mobile Phone	+82 10 8560 6797
Date of Birth	25^{th} September 1991	**Email**	carlsagan96@gmail.com

Personal Profile

Attending Seoul National University (major 3.85/4.3) and expected to graduate 08.2018.
Gained industrial and research experience in the IT startup and the laboratory.

Education

Mar 2011 - Present — **Seoul National University**, *Seoul*
College of Liberal Studies
Candidate for Bachelor's Degree in Computer Science and Engineering
4 Years Full Scholarship - Korea Student Aid Foundation
GPA: 3.75/4.3 GPA(CS major): 3.85/4.3
Related Courses: Data Structures, Algorithms, Operating Systems, Creative Integrated Design, Programming Languages, Principles of Programming, Computer Architecture, Computer Programming, Hardware System Design, Logic Design, Electrical and Electronic Circuits, Discrete Mathematics

Relevant Experience

Jan 2018 - Feb 2018 — **Samsung Electronics**, *Suwon*
Software Development Intern
Mobile Division System Development

Jul 2017 - Dec 2017 — **Programming Research Laboratory, Seoul National University**, *Seoul*
Research Assistant Intern
Developed automated program corrector using machine learning, to feedback assignments of the elementary programming course automatically. Based on *sk_p: a neural program corrector for MOOCs*
Technologies: TensorFlow, Seq2seq

Dec 2015 - Aug 2016 — **Ab180**, *Seoul*
Front-end Developer (6^{th} member in the company)
Airbridge (Mobile app marketing performance analytics)
Developed front-end web, to display marketing performance analysis results without delay (attracted the 600M won (540,000$) investment from TIPS.)
Technologies: React, Redux, Webpack, D3, Eslint, Flow, Enzyme, Amazon S3, Jenkins, Flask, Amazon EC2. / As a team, Scrum, JIRA, Confluence

Projects — **ejs-simple-loader**, *npm package*
Developed ejs loader for webpack. Recorded total 1367 downloads.

Hardware Calculation Accelerator, *Hardware System Design*
Implemented matrix-vector multiplication IP, BRAM for DNN on FPGA, using Verilog, Cpp, Python (resulted 423.15% faster performance than that of CPU in benchmark using MNIST)

Scheduler using Weighted Round Robin, *Operating Systems*
Implemented task scheduler with weighted round robin policy in kernel 3.10 (run with reasonable performance)

Async Web Engine, *Creative Integrated Design*
Developed asynchronous web engine like Netty with TmaxSoft in Java (outperformed Nodejs in response time benchmark using JMeter)

Skills and Others

- **Interests**
  Program Analysis, Software Engineering, Distributed Computing, Cloud Computing, Front-end Web
- **Links**
  Linkedin: www.linkedin.com/in/soo-kim-carlsagan96
  Github: github.com/carlsagan21