

#### 3. Semester projekt Arkitektur og design

#### Udvikling af et blodtrykmålesystem

Arkitektur og design

Navn	${f AU\ ID}$	Studienummer
Caroline Kaagaard Dahl Laursen	$\mathbf{AU572444}$	201611025
Nicolai Bæch	AU580049	201704646
Mathias Egsgaard	AU590400	201705031
Thea Plenus Kjeldahl Kristensen	AU577124	201707180
Sarah Krohn Fenger	AU577425	201707931
Mikkel Rugholm Boisen	$\mathbf{AU578833}$	201708119
Kajene Elankanathan	$\mathbf{AU594051}$	201710472

Vejleder: Samuel Alberg Thrysøe

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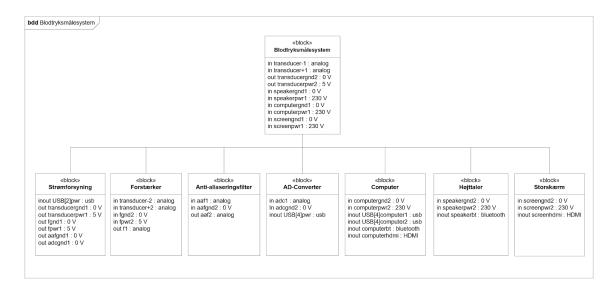
### Ordforklaring

### Indholdsfortegnelse

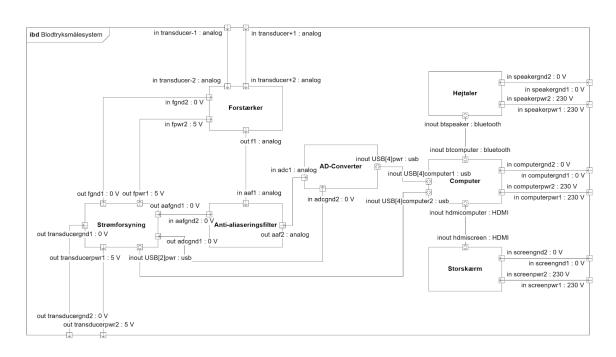
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## Hardwarearkitektur

#### 1.1 BDD



#### 1.2 IBD



#### 1.3 Bloktabel

Funktion	Signalnavn	Område	Port 1	Port 2	Kommentar
			(Source)	(Destination)	
Strøm-	speakerpwr	$0-230~\mathrm{V}~\mathrm{rms}$		speakerpwr	
${f for syning}$	computerpwr	$0-230~\mathrm{V}~\mathrm{rms}$		computerpwr	
	screenpwr	0-230 V rms		screenpwr	
	transducerpwr	4.9-5.1 V	transducerpwr		
	fpwr1	4.9-5.1 V	fpwr1	fpwr2	
	USB[4]computer2	4.9-5.1 V	USB[4]computer2	USB[2]pwr	USB power
					Stel
Reference	speakergnd	0 V		speakergnd	Stel
	computergnd	0 V		computergnd	Stel
	screengnd	0 V		screengnd	Stel
	transducergnd	0 V	transducergn		Stel
	fgnd1	0 V	fgnd1	fgnd2	Stel
	aafgnd1	0 V	aafgnd1	aafgnd2	Stel
	adcgnd1	0 V	adcgnd1	adcgnd2	Stel
Data-	computerbt	bluetooth	computerbt	speakerbt	inout
kommu-					flow-
nikation					specification
	computerhdmi	HDMI	computerhdmi	screenhdmi	inout
					flow-
					specification
	USB[4]computer1	usb	USB[4]computer1	USB[4]pwr	inout
					flow-
					specification
	aaf2	analog	aaf2	adc1	
	f1	analog	f1	aaf1	
	${\rm transducer} +$	analog		transducer+	
	transducer-	analog		transducer-	

#### 1.4 Signaltabel

## 2 Softwarearkitektur

- 2.1 Domænemodel: Overordnet
- 2.2 Domænemodel
- 2.3 Klassearkitektur

# 3 Software