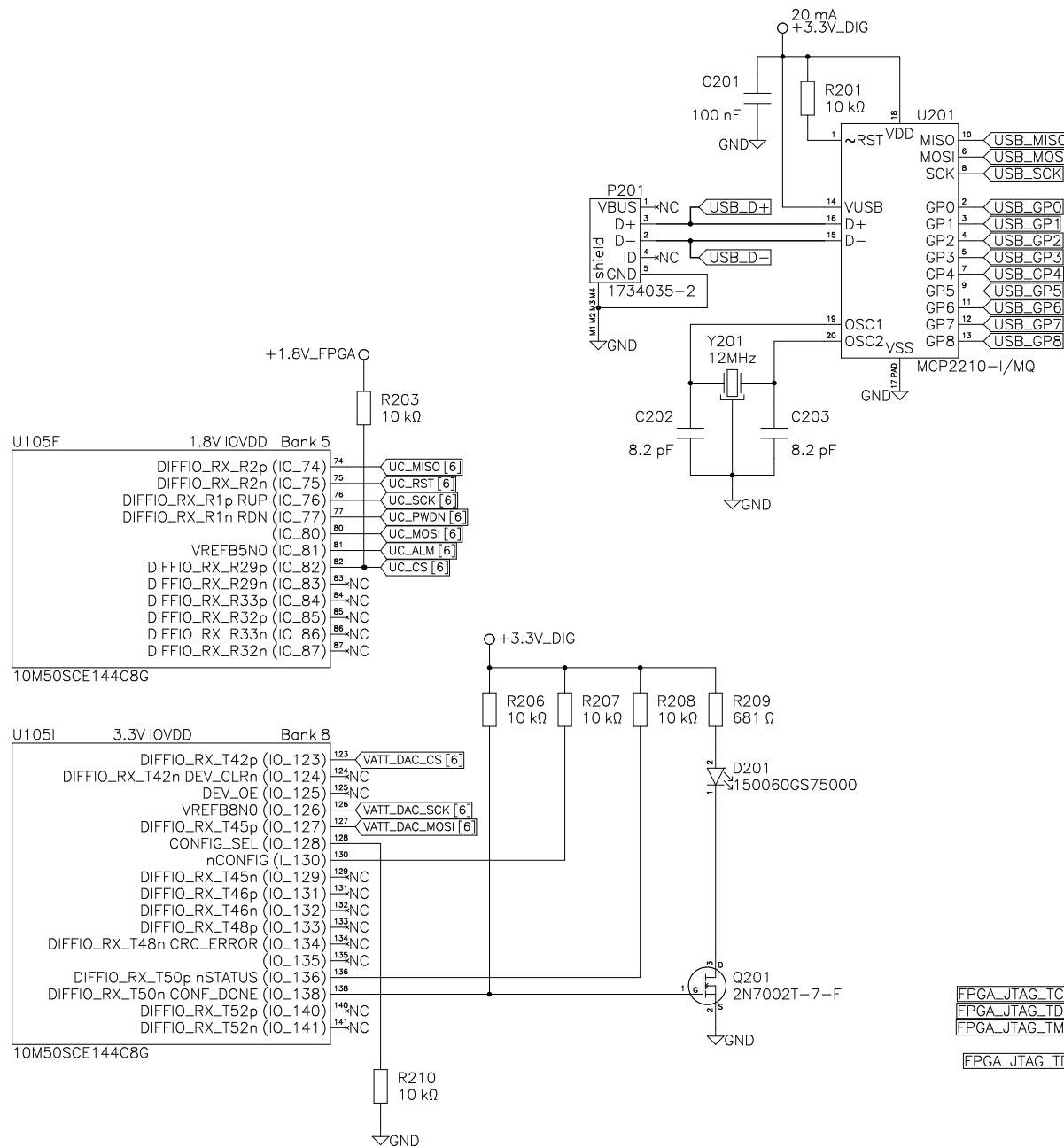


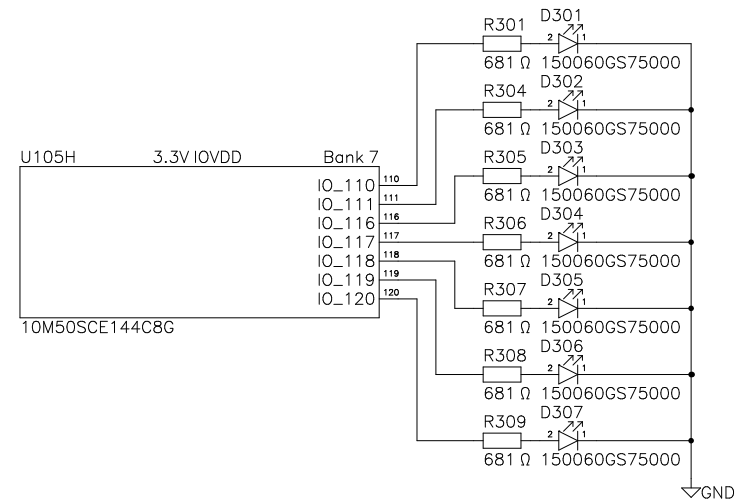
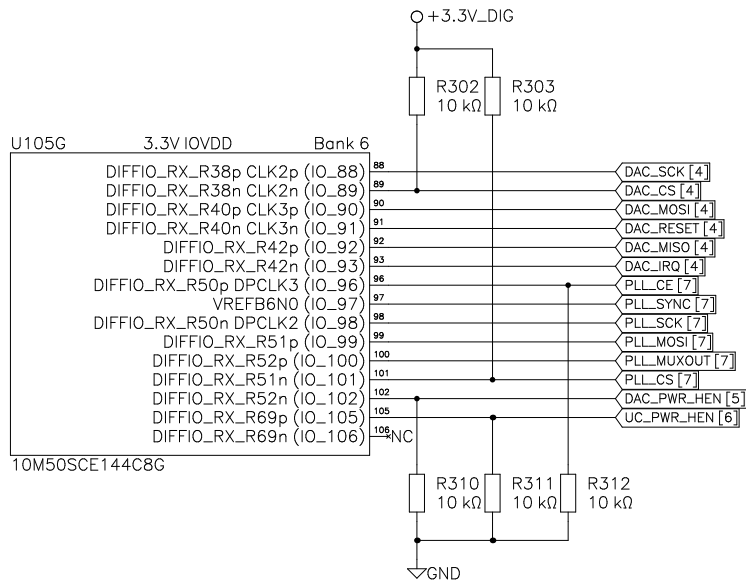
Sheet	Number
FPGA Power	1/7
Project	Revision
X Band Transmitter	2
Drawn by	
Lukas K.	
Date	
2018	





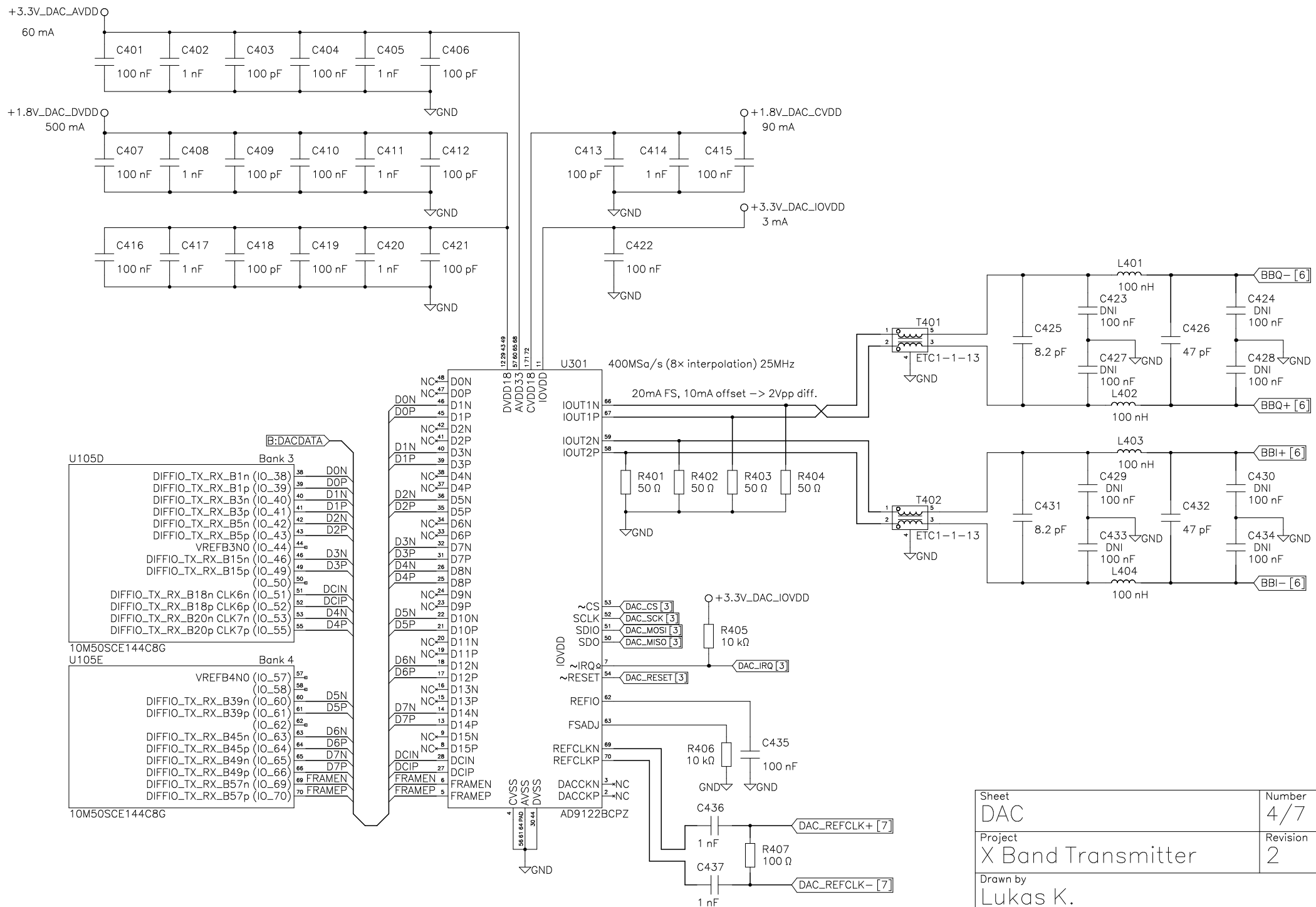
Sheet	Number
FPGA / USB	2/7
Project	Revision
X Band Transmitter	2
Drawn by	
Lukas K.	
Date	
2018	





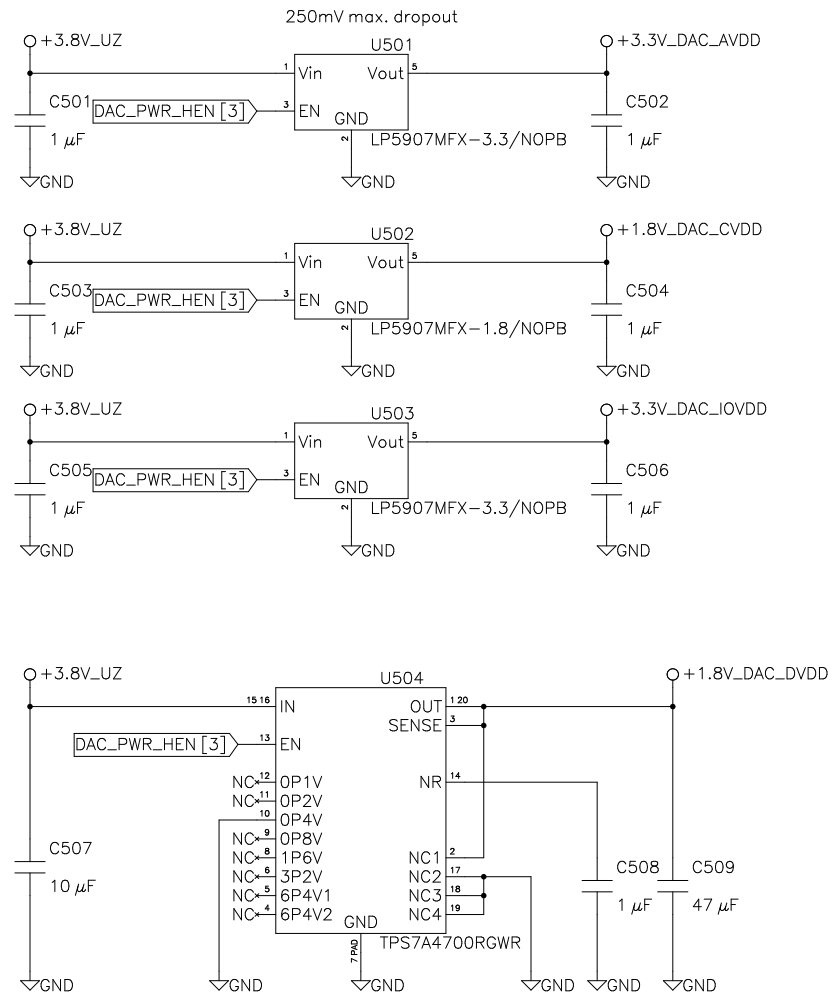
Sheet	Number
FPGA	3/7
Project	Revision
X Band Transmitter	2
Drawn by	
Lukas K.	
Date	
2018	





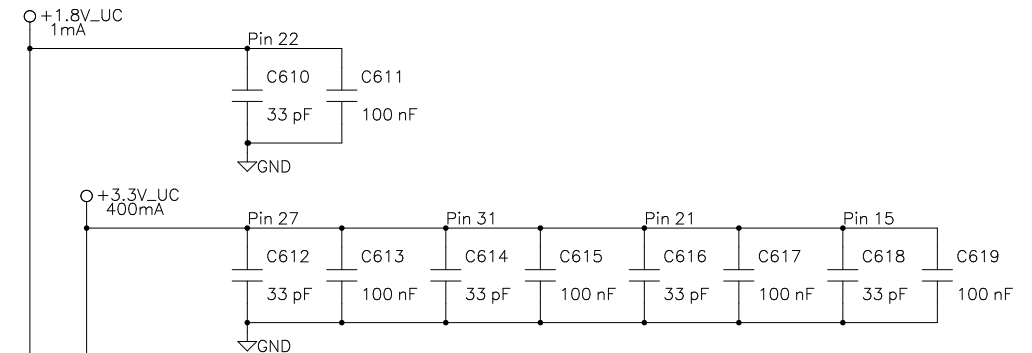
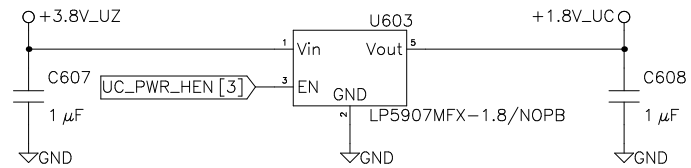
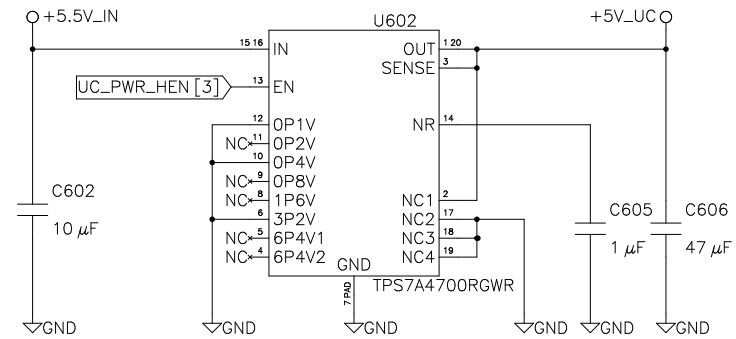
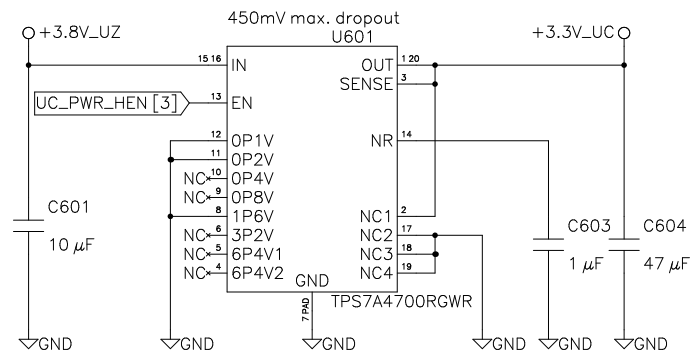
Sheet	Number
DAC	4/7
Project	Revision
X Band Transmitter	2
Drawn by	
Lukas K.	
Date	
2018	



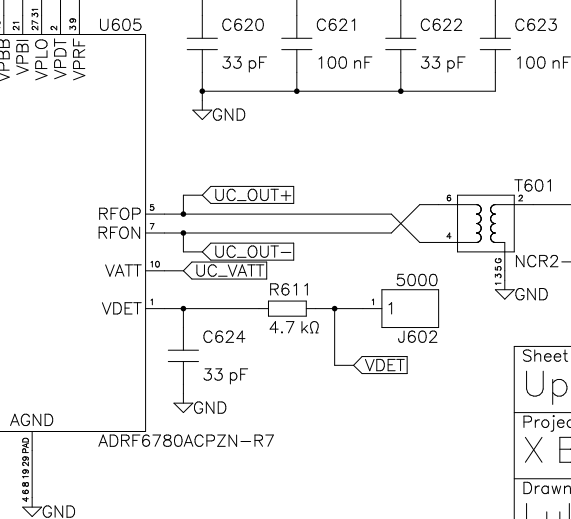
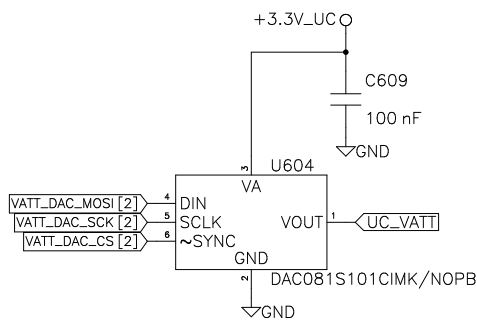
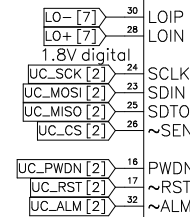
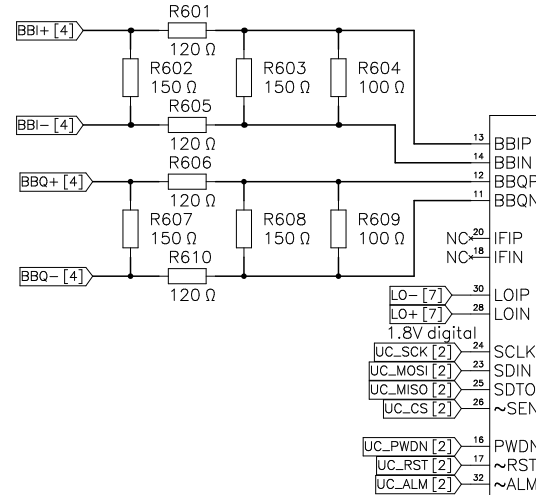


Sheet	Number
DAC power	5/7
Project	Revision
X Band Transmitter	2
Drawn by	
Lukas K.	
Date	
2018	



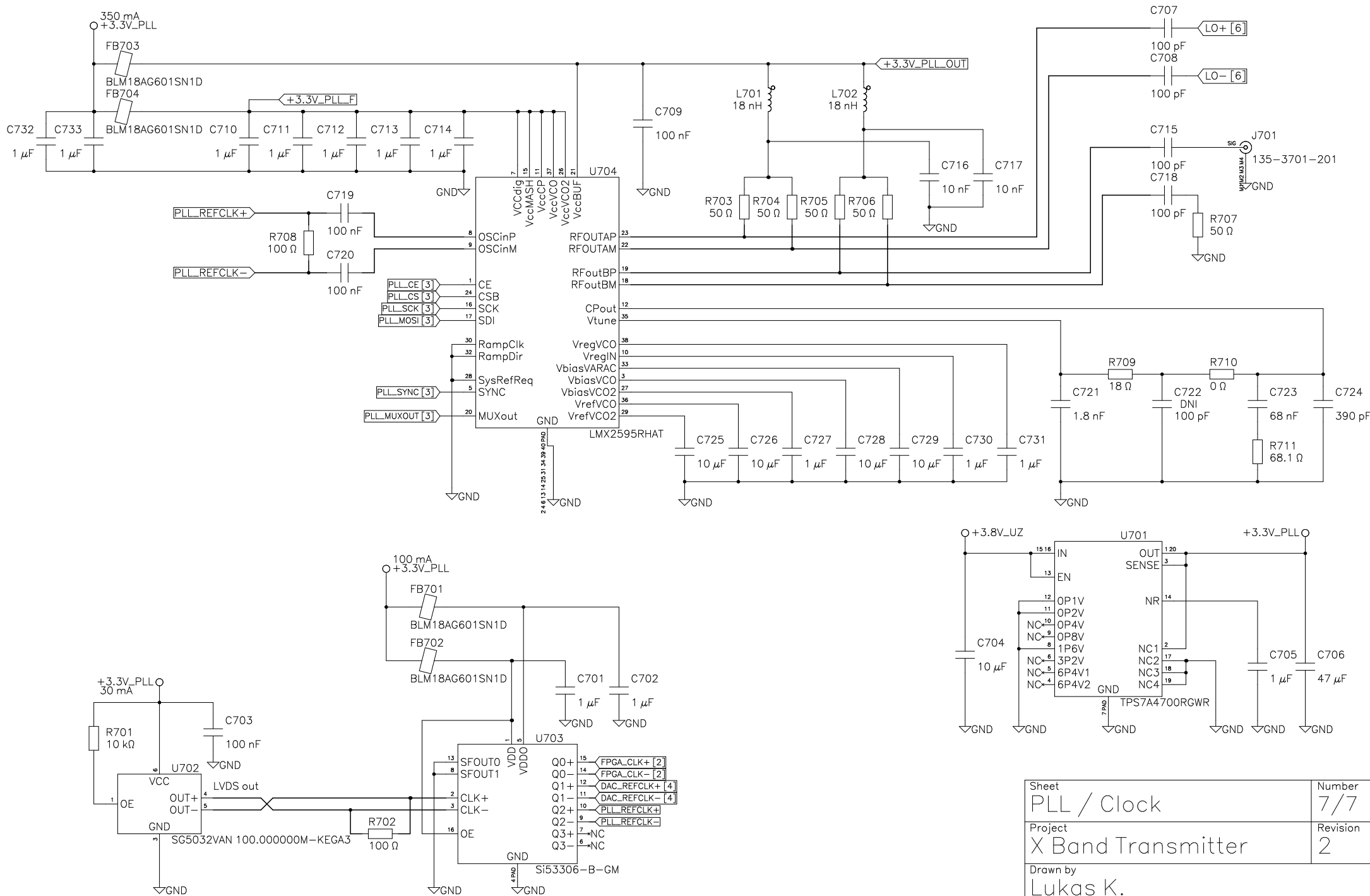


INPUT: -15dBm nominal, 31 μ W, 56mV RMS differential
x5 ATT, 14dB 80mVp, 160mVpp diff



Sheet	Number
Upconverter	6/7
Project	Revision
X Band Transmitter	2
Drawn by	
Lukas K.	
Date	
2018	





Sheet	PLL / Clock	Number	7/7
Project	X Band Transmitter	Revision	2
Drawn by	Lukas K.		
Date	2018		

