

Andrew Clements

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EDUCATION

University Of Virginia, School of Engineering and Applied Science B.S. Electrical Engineering GPA: 3.96 Relevant Coursework: Signals and Systems, Digital Logic Design, Thermodynamics, Heat Transfer, Embedded	Charlottesville, VA 2026
Germanna Community College A.S. Electrical Engineering GPA: 3.8 Relevant Coursework: Circuits, Electronics, EGR Design	Fredericksburg, VA 2024

AWARDS & HONORS

IEEE IThERM – Avram Bar-Cohen Best Paper Award, Runner-Up • Co-Author: Development of Liquid Metal and Silicon Pin Fin Composite Thermal Interface Materials	May 2025
U.S. Provisional Patent – Co-Inventor <i>Stanford University</i> • U.S. Provisional Patent Application 63/809,052 - Silicon Pin Fin Liquid Metal Composite TIMs	May 2025

RELEVANT EXPERIENCE

SWAMI LAB – Biophysical Microsystems Research Group <i>University of Virginia</i> • Co-designed mixed-signal FPGA system integrating high-speed DAC and ADC for impedance signal acquisition • Designed ADA4817 analog front-end with feedback compensation for impedance signal conditioning • Supported DC-coupled driver architecture and SPI control for high-speed data path integration	Jan 2026 - Present Charlottesville, VA
FTSC VAST – Semiconductor Fabrication Training (Cleanroom) <i>University of Virginia</i> • Operated MicroWriter ML3, SUSS MABA6 aligner, spin coaters, Bruker ContourGT, and Gemini SEM • Completed photolithography, DRIE, sputtering (Ti/Au), e-beam deposition, BOE wet etch, ICP-RIE • Diagnosed defects (comets, edge bead, residue, undercut) and improved process repeatability	Fall 2025 Charlottesville, VA
NANOHEAT LAB – Undergraduate Research Internship <i>Stanford University</i> • Studied nanoscale heat transfer in electronic structures using IR microscopy and thermal modeling • Developed and validated thermal interface cooling strategies for power electronics • Reduced measurement uncertainty in conventional TIM testers • Designed, fabricated, and tested Si Pin fin chips to measure thermal conductivity of relevant LM TIMs	Summer 2024, Summer 2025 Stanford, CA
REINKE LAB – Undergraduate Materials Science Research Internship <i>University of Virginia</i> • Studied surface morphology in electrochemically etched Ni-based super alloys • Used atomic force and scanning electron microscopy, conducted image analysis, and performed electrochemical etching	Summer 2023 Charlottesville, VA

PROJECTS & TEAMS

UVA E-BIKE TEAM <i>University of Virginia</i> • Collaborated in the design and fabrication - integrating motor control, battery systems, and embedded electronics • Performed subsystem assembly, electrical integration, and validation testing to ensure reliable operational	Fall 2025 - Present Charlottesville, VA
UVA SOLAR CAR TEAM <i>University of Virginia</i> • Electrical Subteam - contributed to power distribution wiring and high-current system integration • Power systems wiring, thermal constraints analysis, integration testing	Fall 2024 - Spring 2025 Charlottesville, VA
GERMANNA ENGINEERING SOCIETY <i>Germanna Community College</i> • Logistics Lead – coordinated university lab tours and museum tours • Volunteered at Culpepper Airfest, annual STEM-H program, and the Engineering Outreach Group	Fall 2022 - Spring 2024 Fredericksburg, VA

TECHNICAL SKILLS

- **Fabrication & Metrology:** Photolithography, SEM, DRIE, CVD, e-beam evaporation, BOE etch, ICP-RIE, profilometry
- **Software:** MATLAB, C, C++, VHDL, Verilog, Assembly, Spice, kLayout, Fusion 360
- **Cleanroom:** Wafer handling, safety protocols, RCA-style cleaning, process flow execution