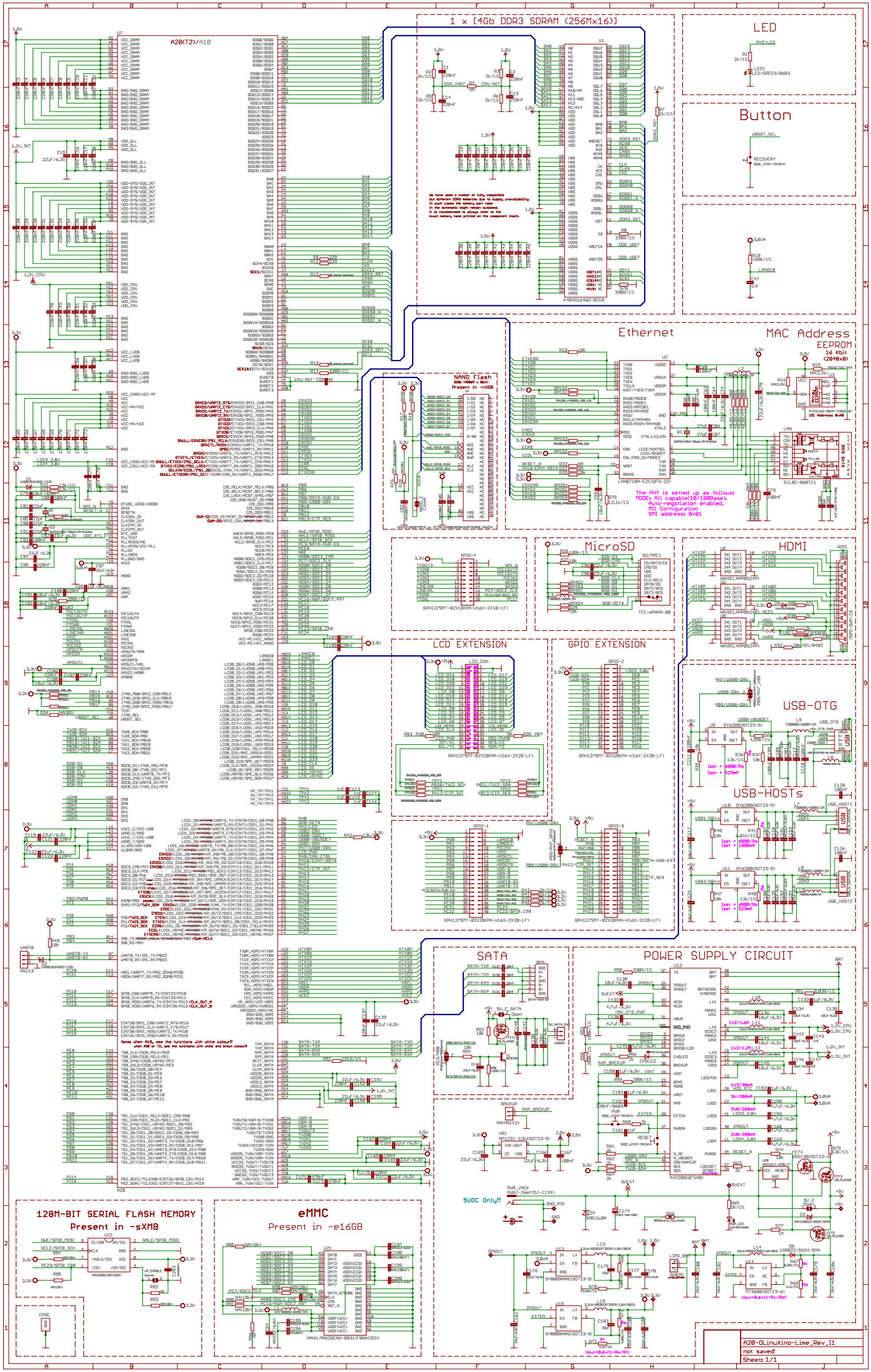
A20(T2)/A10
1 x [4Gb DDR3 SDRAM (256Mx16)]
K4B4G1646D-BCK8
We have used a number of fully compatible
but different DDR3 memories due to supply unavailability.
In such cases the memory part name
in the schematic might remain outdated.
It is recommended to always refer to the
exact memory name printed on the component itself.
LED
Button
Ethernet
MAC Address
EEPROM
16 Kbit
(2048x8)
NAND Flash
3Gb (4Gb) x 8bit
Present in -nXGB
LAN8710A-EZC(QFN-32)
The PHY is setted up as follows:
MODE1 All capable(10/100base),
Auto-negotiation enabled.
MII Configuration
SPI address 0x01
MicroSD
HDMI
LCD EXTENSION
GPIO EXTENSION
USB-OTG
USB-HOSTS
SATA
POWER SUPPLY CIRCUIT
128M-BIT SERIAL FLASH MEMORY
Present in -sXMB
eMMC
Present in -e16GB
A20
A20-OLinuXino-Lime_Rev_I1
not saved
Sheet: 1/1