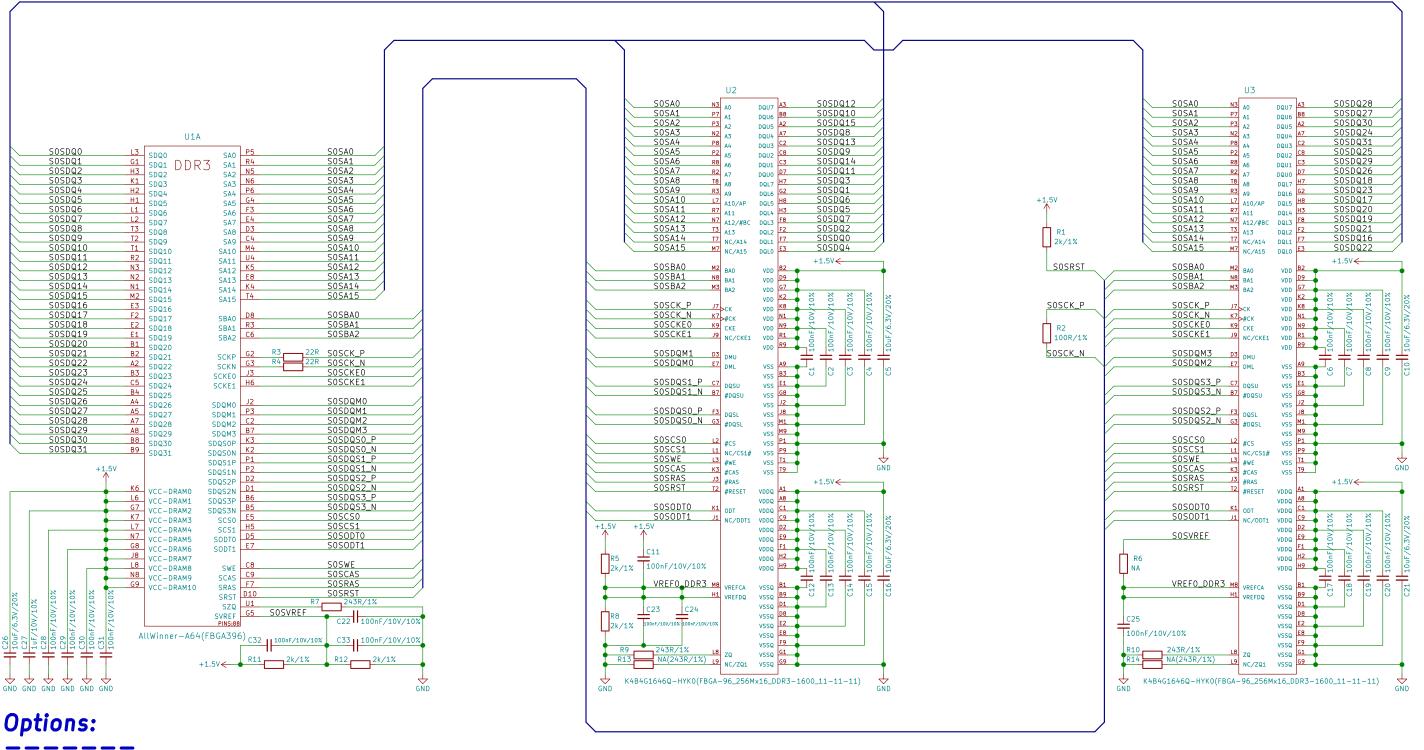
DDR3:1GByte



- 1. Use $2(DDR3\ 256Mx16\ Memory\ chips)x4Gb = 1GByte, i.e. 2xH5TQ4G63MFR-PBC(or K4B4G1646Q-HYK0) -> Default$ 2. Use 2(DDR3 512Mx16 Memory chips)x8Gb = 2GBytes, i.e. 2xH5TC8G63AMR-PBA(or K4B8G1646Q-MYK0)
- Note:

We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

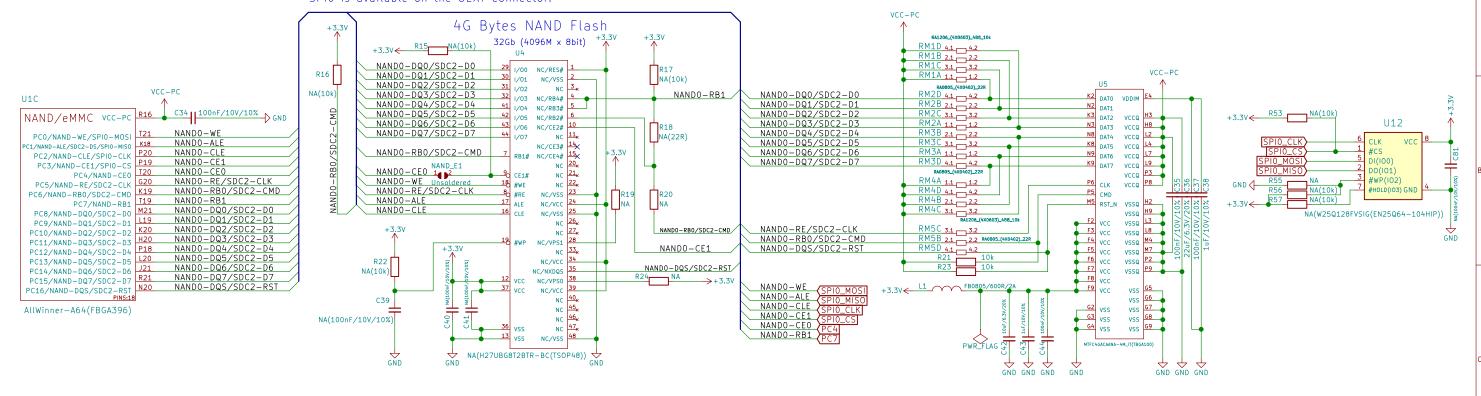
Size: A3 Date: 2016-09-08	Rev: Rev_C
Title: A64-OLinuXino	
File: A64-OlinuXino_Rev_C.sch	
Sheet: /	
OLIMEX LTD, Bulgaria	
<c> 2016</c>	
,,	
DDR3 Memory	



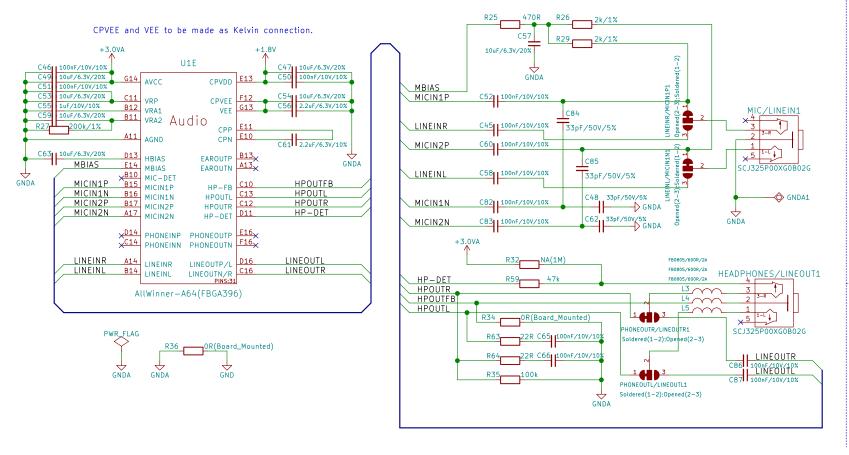
eMMC SPI flash

Option!

eMMC is default, because in this case, SPIO is not multipexed with eMMC as is with the NAND Flash! SPIO is available on the UEXT connector!



Audio



T-Card

