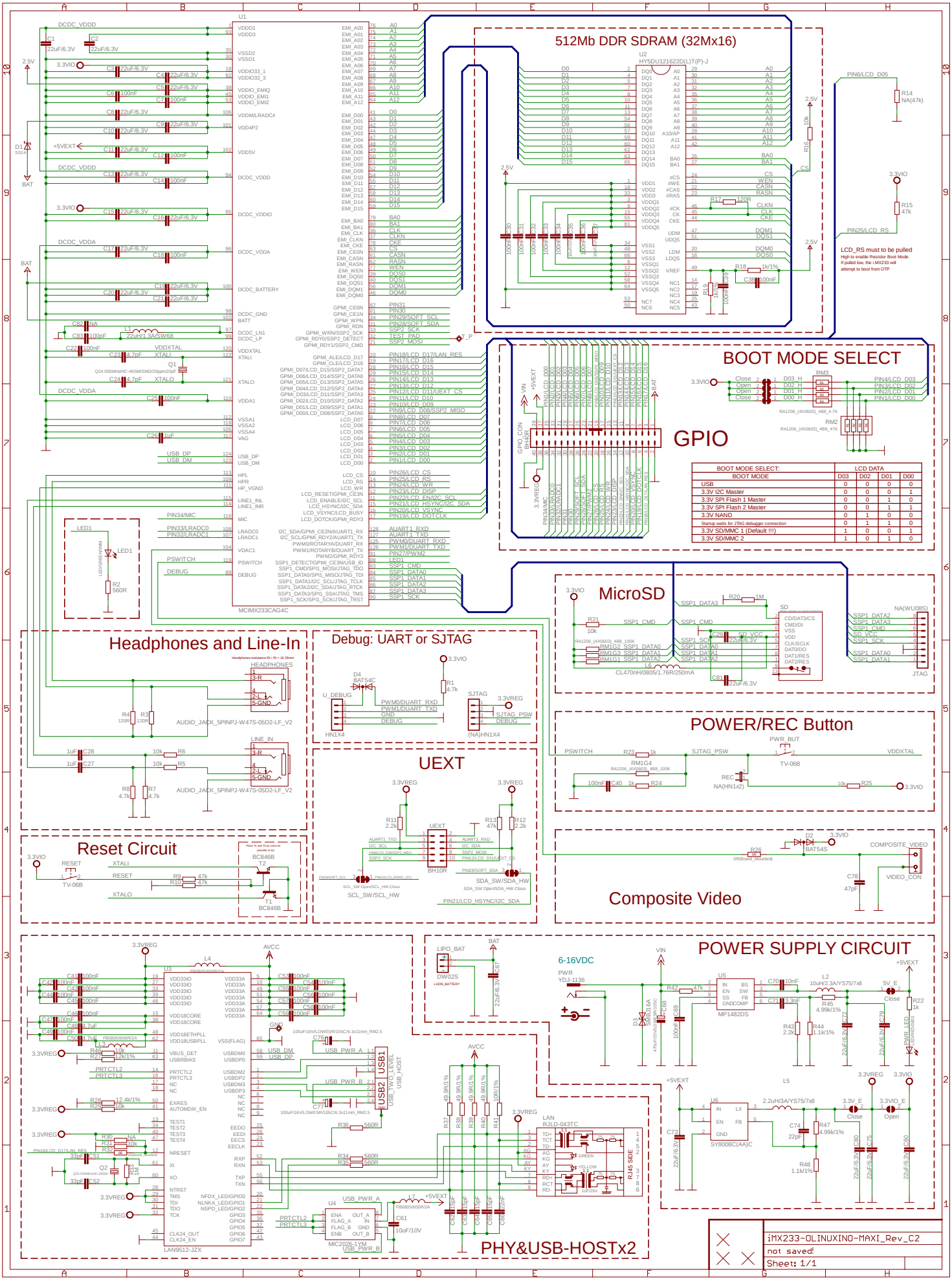
512Mb DDR SDRAM (32Mx16)
BOOT MODE SELECT
GPIO
MicroSD
Headphones and Line-In
Debug: UART or SJTAG
POWER/REC Button
UEXT
Reset Circuit
Composite Video
POWER SUPPLY CIRCUIT
PHY&USB-HOSTx2
MCIMX233CAG4C
LCD_RS must to be pulled
High to enable Resistor Boot Mode.
If pulled low, the i.MX233 will attempt to boot from OTP.
iMX233-OLINUXINO-MAXI_Rev_C2
not saved!
Sheet: 1/1