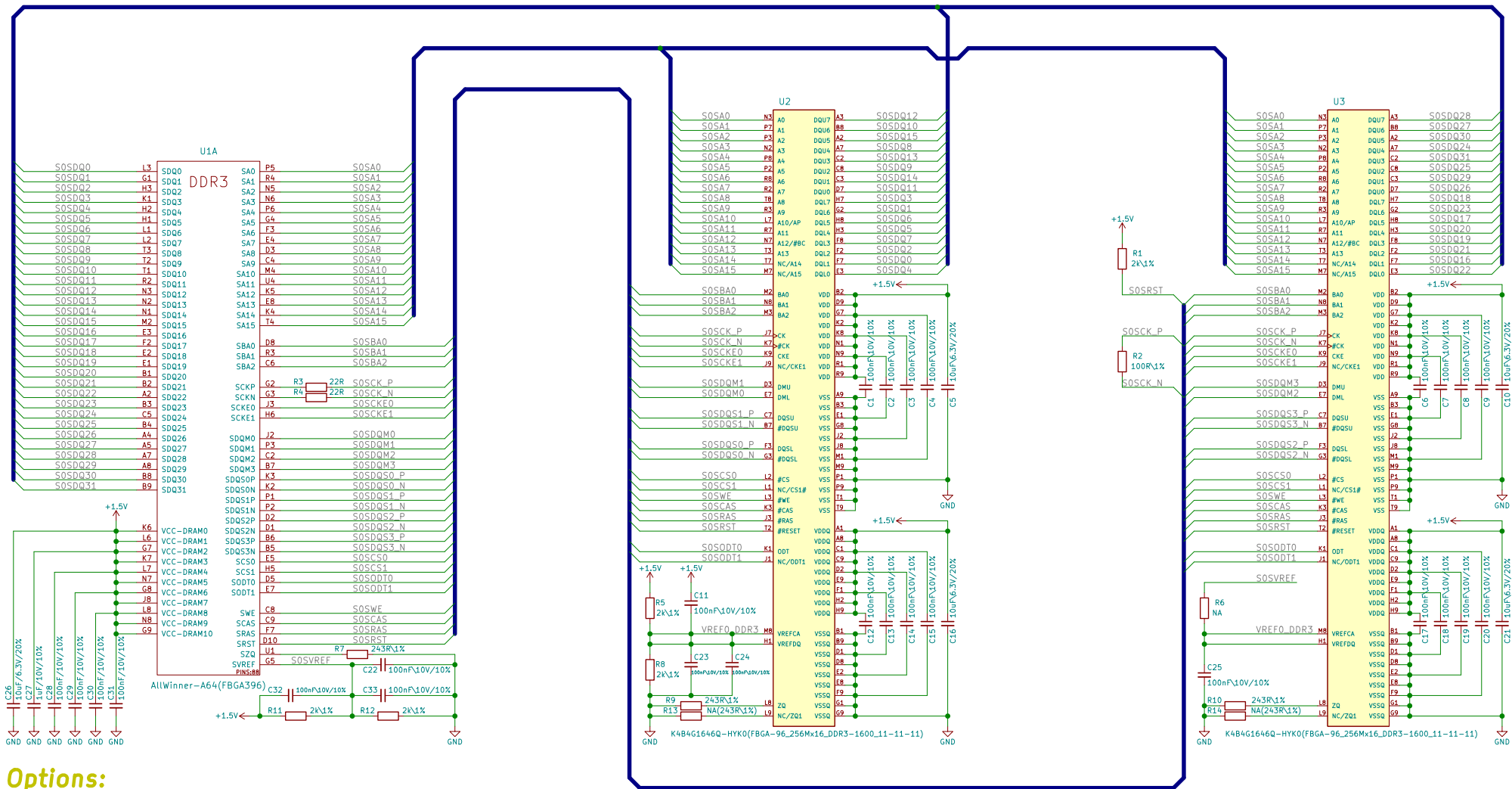


DDR3:1GByte



Options:

=====

1. Use 2(DDR3 256Mx16 Memory chips)x4Gb = 1GByte, i.e. 2xH5TQ4G63MFR-PBC(or K4B4G1646Q-HYK0) -> Default
2. Use 2(DDR3 512Mx16 Memory chips)x8Gb = 2GBytes, i.e. 2xH5TC8G63AMR-PBA(or K4B8G1646Q-MYK0)

Note:

=====

We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

DDR3 Memory

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Sheet: /
 File: A64-OlinuXino_Rev_G.sch

Title: A64-OlinuXino

Size: A3	Date: 2020-02-11	Rev: G
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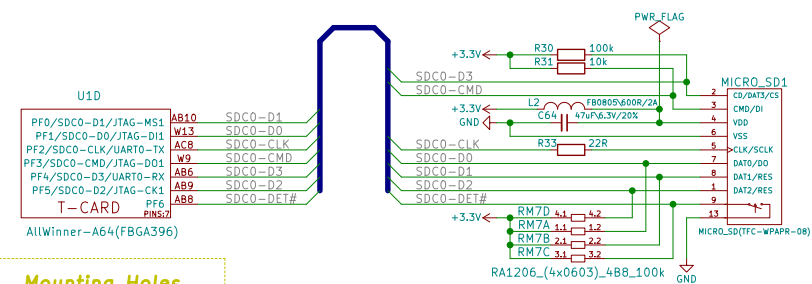
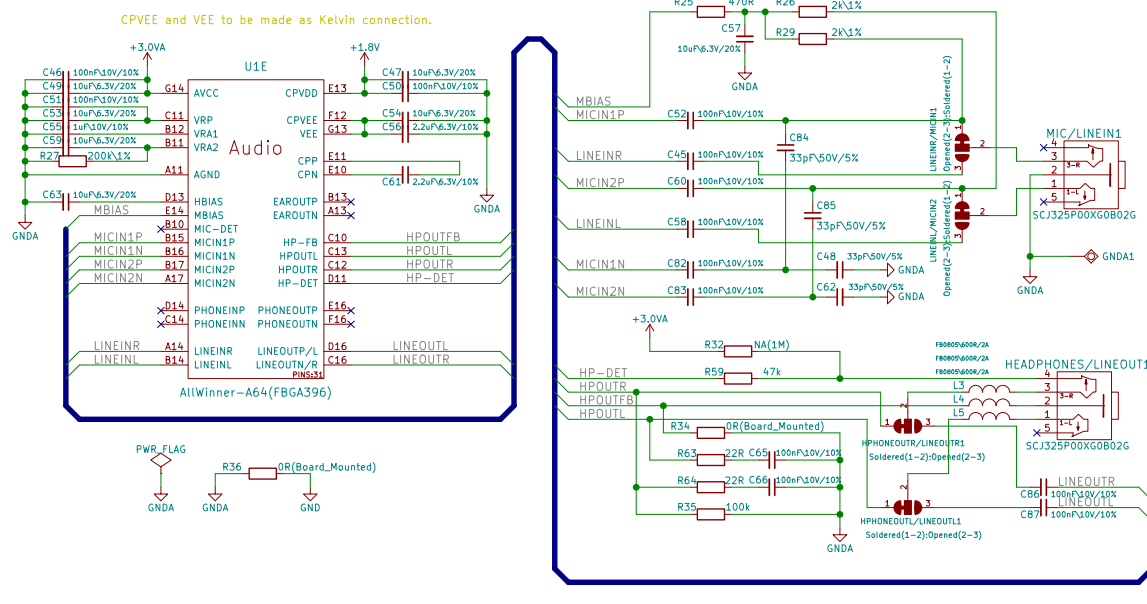
eMMC

16G Bytes eMMC

SPI Flash

16M Bytes SPI Flash

Present in -sXM



Mounting Holes

<c> 2020

OLIMEX LTD, Bulgaria

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File: NAND Flash , eMMC, T-Card and Audio.sch

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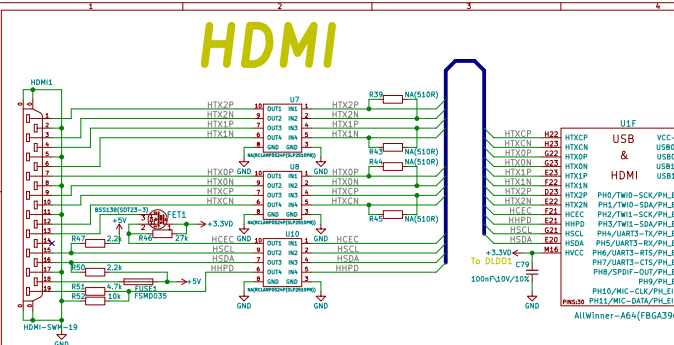
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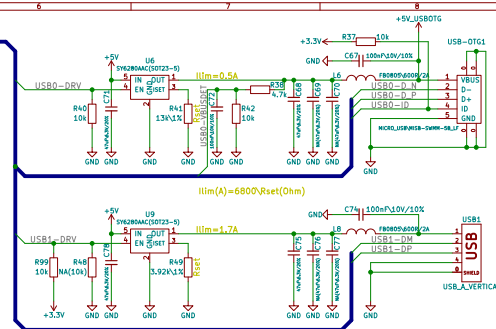
Rev: G

Id: 2/4

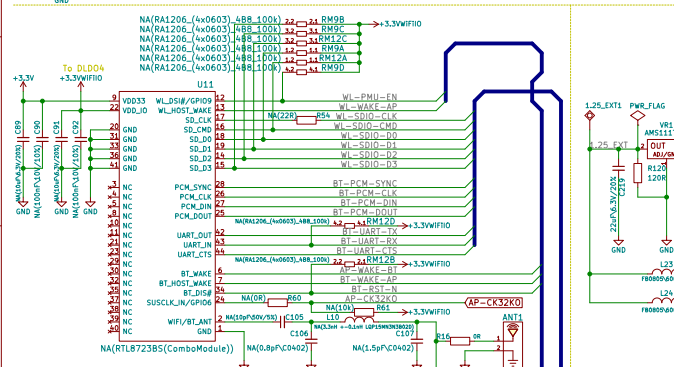
HDMI



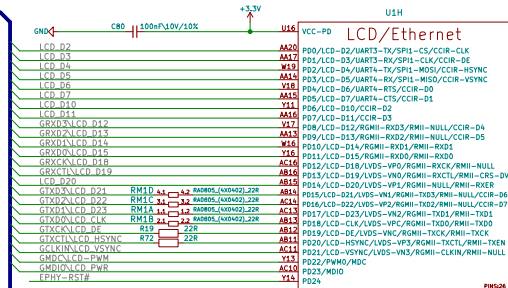
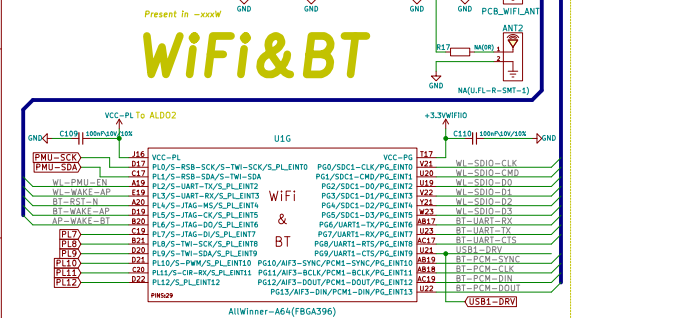
USB



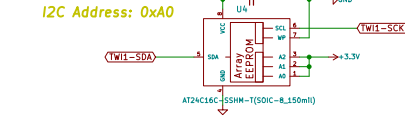
Gigabit Ethernet or LCD



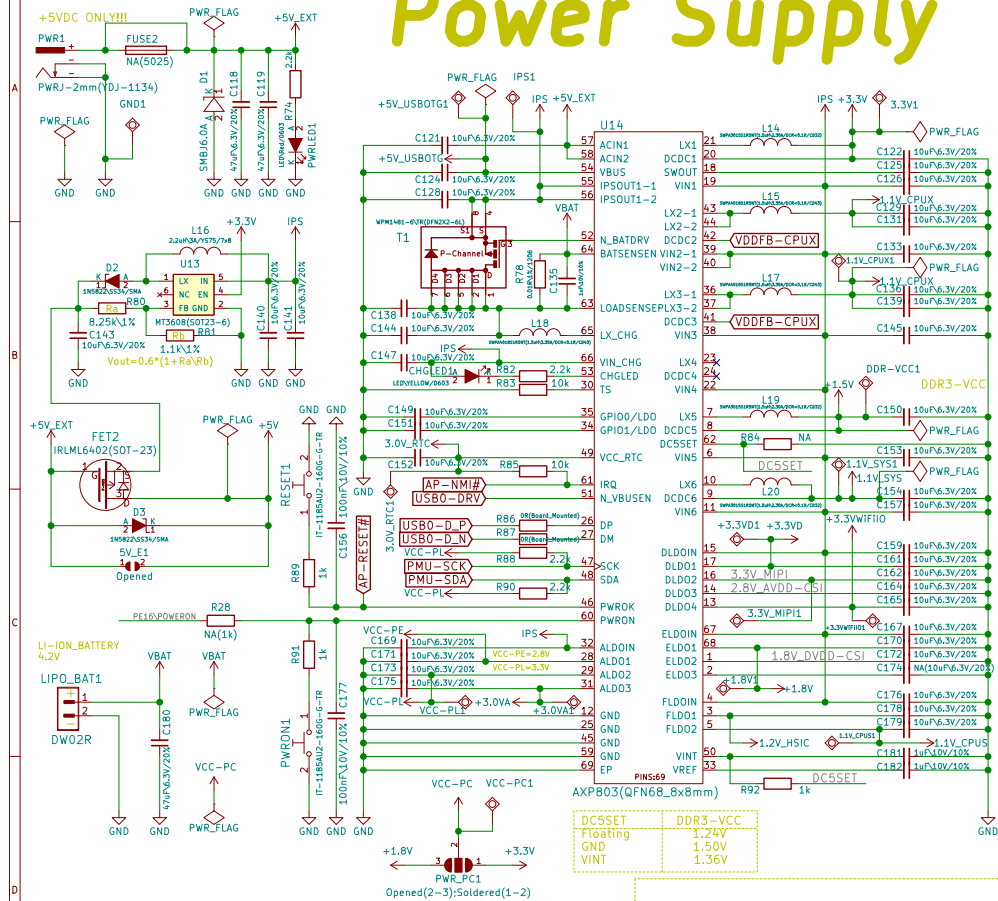
WiFi&BT



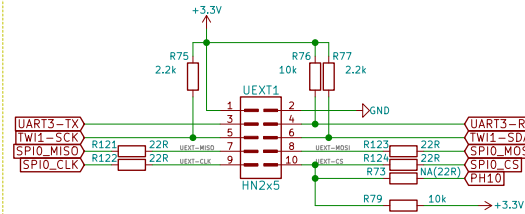
EEPROM



Power Supply



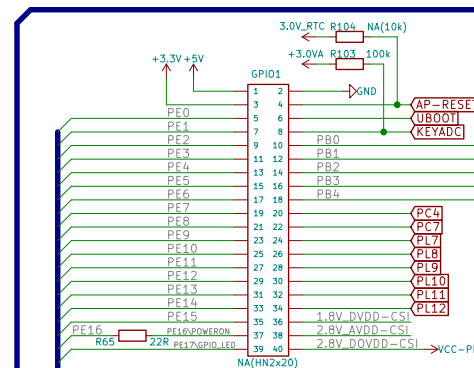
UEXT



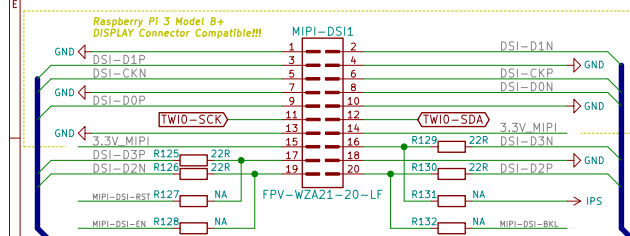
Notes:
=====

1. When **SPL_Flash** is populated, use **PH10** for **SPL_CS** on the **UEXT!**
In this case, **R73** must be populated and **R124** desoldered;

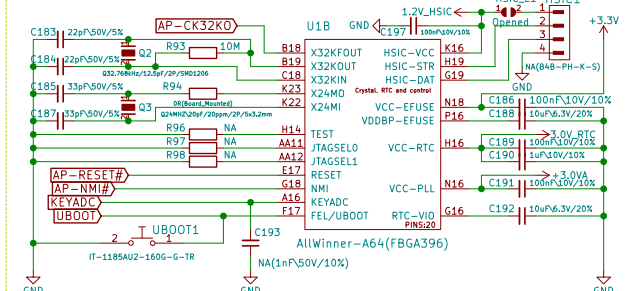
Extension



MiPi-DSI



Crystal, RTC & Control



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Sheet: /Power Supply, Extensions and MiPi-DSI/
File: Power Supply, Extensions and MiPi-DSI .sch

Title: A64-OLinuxino

Size: A3	Date: 2020-02-11
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Rev: G
Id: 4/4