SIGNAL ANALYZER 5480AB WITH 5485A 5486AB 5487A 5488A PLUG-INS SERIAL PEX ALL SERIALS PART NO 05480-90013 (MANUAL) 05480-90016 (FICHE)

A9A4 ACCUMULATOR ASSEMBLY (05480-60003)

## DESCRIPTION

This assembly performs the following operations for Accumulator Bits 4-7:

Count-Up, Count-Down, Shift-Left, Shift-Right, and Shift-In.

This board contains 4 of the 24 flip-flops in the Accumulator Register.

The input levels necessary for proper operation are as follows:

		P . 3		····	~		<del></del> ,-	<del></del>
.7.607.71		·		P	IN .		:	
MODE	9	10	11	12	13	T	υ <i>'</i>	14
Count-Up	• 1	0	0	0	1	B.	1	1
Count-Down	1 1	0	0	0	1	1	P	1
Shift-Left	0, -	0	0 .	1 1	P	.1	1	1.
Shift-Right	_^0 %	1	- O	0	P	1	1	1
Shift-In	0	0	· 1	e 0	P	1	1	1
Clear	?	?	~ <b>?</b>	?	. 1	1	1	0

P = Inverted Pulses
? = Don't Care (i. e., 1 or 0)

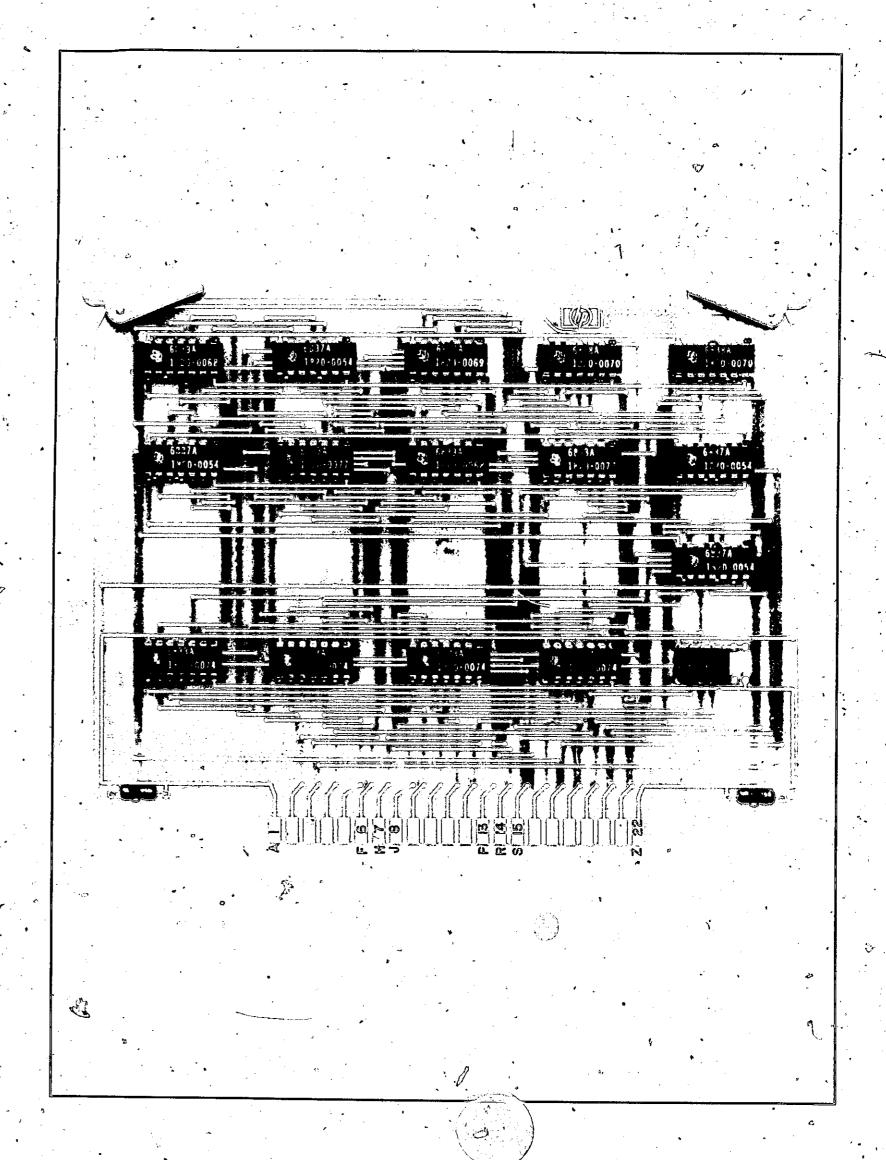
Flip-Flop Outputs are 20, X, 21, Y

Set Inputs are 2, B, 3, C

## CHANGES FOR OLDER BOARDS

Current Series: 832

Older Series: None



NOTES

REFESSICE OESIGNATIONS

A3A &

c1,2

IC1-9, 10-20

R1-5

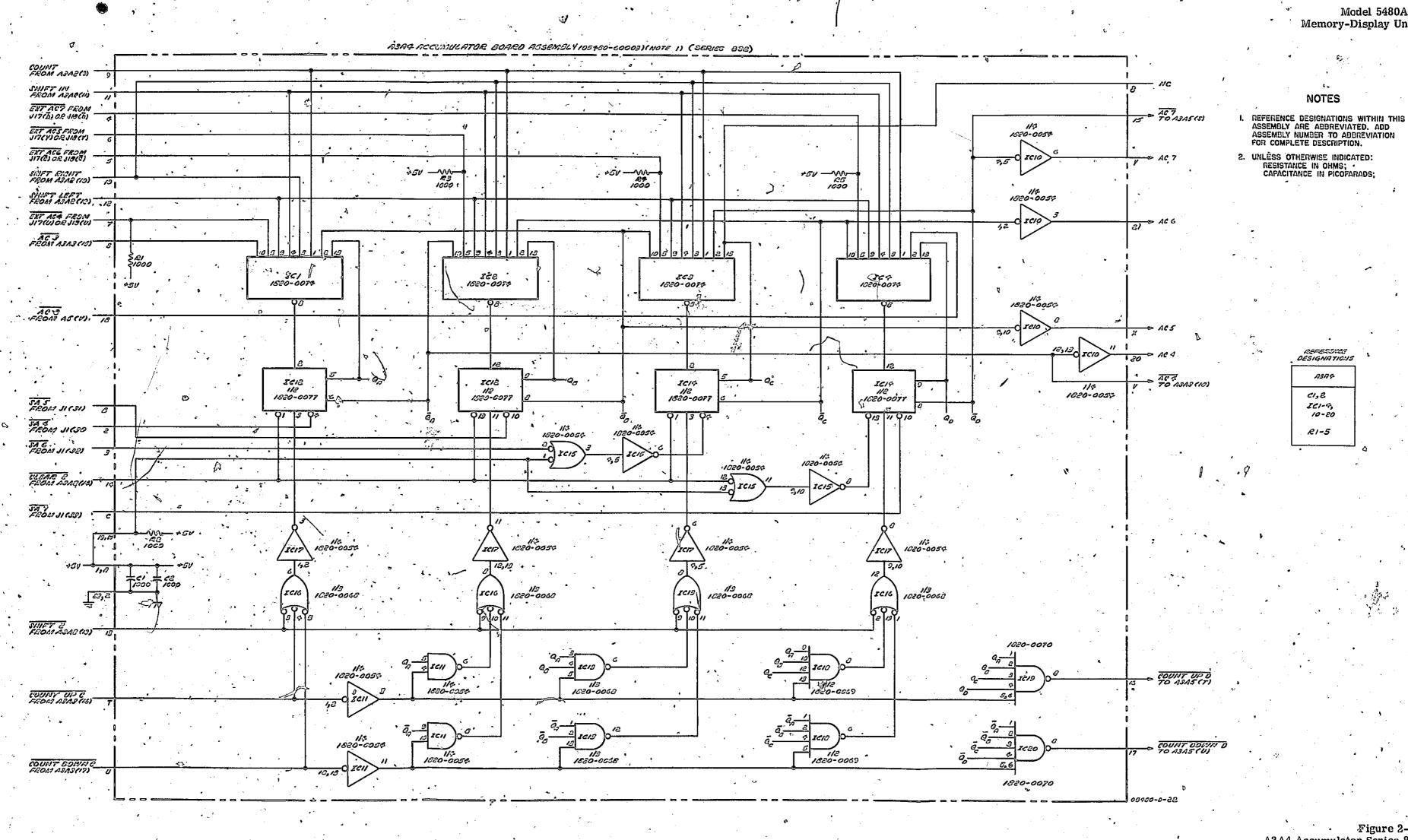


Figure 2-26
A3A4 Accumulator Series 832

# A3A5 ACCUMULATOR ASSEMBLY (05480-60003)

## DESCRIPTION

This assembly performs the following operations for Accumulator Bits 8-11:

Count-Up, Count-Down, Shift-Left, Shift-Right, and Shift-In

This board contains 4 of the 24 flip-flops in the Accumulator Register.

The input levels necessary for proper operation are as follows:

	PIN								
MODE	9	10	11	12	13	T.	U	14	
Count-Up	1	0	0	0	1	P	1	1	
Count-Down	1	0	0	0	1	1	P	10	
Shift-Left	0	0	0	1	æ	1	1	1	
Shift-Right	0	-1	0 ,	0.	₽	1	1	1	
Shift-In	. 0	0	, 1 , 1	0	, ₽	. 1	1	1	
Clear	?	?	?	?	. 1	1	1	, 0	

P = Inverted Pulses

? = Don't Care (i.e., 1 or 0)

Flip-Flop Cutputs are 20, X, 21, Y.

Set inputs are 2, B, 3, C

## CHANGES FOR OLDER BOARDS

Current Series: 832

Older Series: None

#### COMPONENT LOCATOR

See Figure 2-26 for component locator.

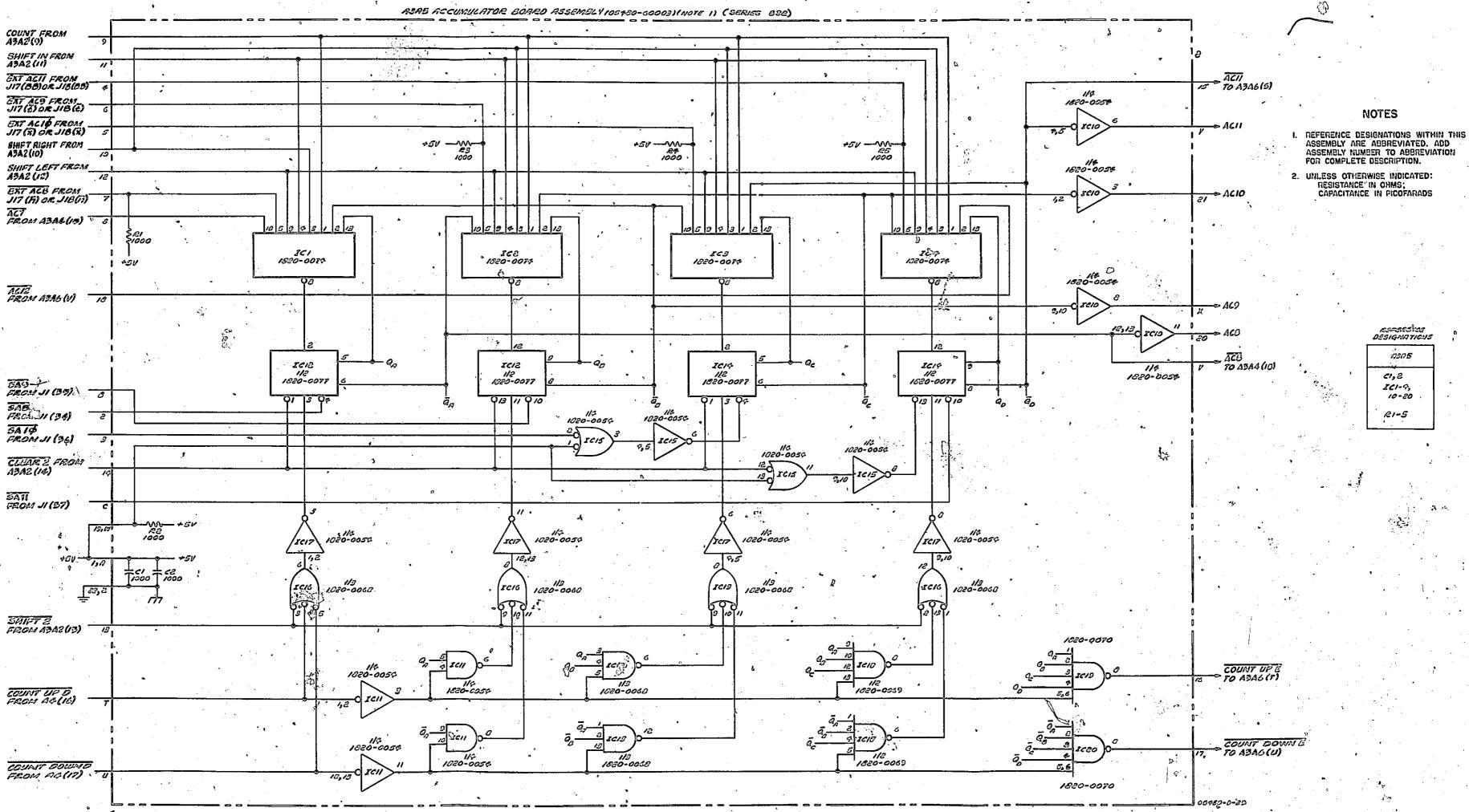


Figure 2-27 A3A5 Accumulator Series 832 2-55

## ASA7 ACCUMULATOR ASSEMBLY (05480-60003)

#### DESCRIPTION

This assembly performs the following operations for Accumulator Bits 16-19:

Count-Up, Count-Down, Shift-Left, Shift-Right, Shift-In

This board contains 4 of the 24 flip-flops in the Accumulator Register.

The input levels necessary for proper operation are as follows:

		PIN								
	MODE	, 9	10	- 11	12	13	T	บ	14	
	Count-Up	1	0	0	0	, 1	P	1	1	
1	Count-Down	. 1	0	0	0	1	1	P.	1	
	Shift-Left	0	0	0	1	P	1	1	1	
1	Shift-Right	0 4	1	0	0	P	<b>1</b>	1	1	
	Shift-In	0	0	1 .	0	P	1	1	1	
0	Clear	?	?	?	? ,	01	1	1	.0	

P = Inverted Pulses

? = Don't Care (i.e., 1 or 0)

Flip-Flop Outputs are 20, X, 21, Y

Set Inputs are 2, B, 3, C

## CHANGES FOR OLDER BOARDS

Current Series: 832

· Older Series: None

#### COMPONENT LOCATOR

See Figure 2-26 for component locator.

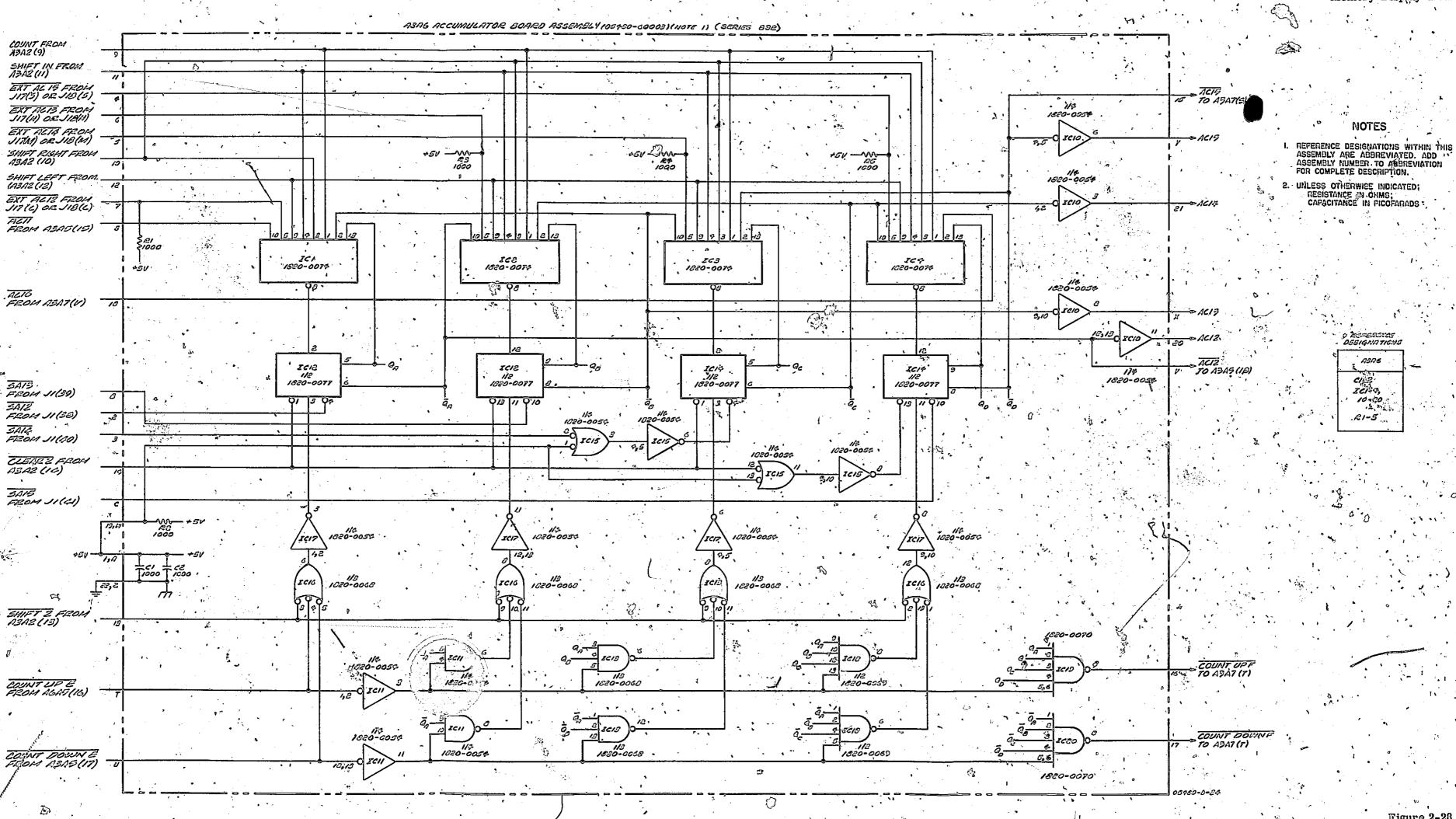


Figure 2-28
A3A6 Accumulator Series 832

## ASA6 ACCUMULATOR ASSEMBLY (05480-60003)

### DESCRIPTION

This assembly performs the following operations for Accumulator Bits 12-15:

Count-Up, Count-Down, Shift-Left, Shift-Right, Shift-In

This board contains 4 of the 24 flip-flops in the Accumulator Register.

The input levels necessary for proper operation are as follows:

			•	P	IN			
MODE	. 19	10	11	12	13	<sup>A</sup> T	U	14
Count-Up	1	0.	,0	0	1	P	1,	1
Count-Down	1 1	. 0	0	0	. 1	1	P	1
Shift-Left .	0	o o	0	1	P	1	1 1	. 1
Shift-Right	0	1	0 }	0	P	1	1	1
Shift-In	0	0	1	္ဟ <sub></sub> 0	P	1	1	1
Clear	?	?	?	° ?	1	1,	1,	0

P = Inverted Pulses

? = Don't Care (i.e., 1 or, 0)

Flip-Flop Outputs are 20, X, 21, Y

Set Inputs are 2, B, 3, C

## CHANGES FOR OLDER BOARDS

Current Series: 832

Older Series: None

## COMPONENT LOCATOR

See Figure 2-26 for component locator.

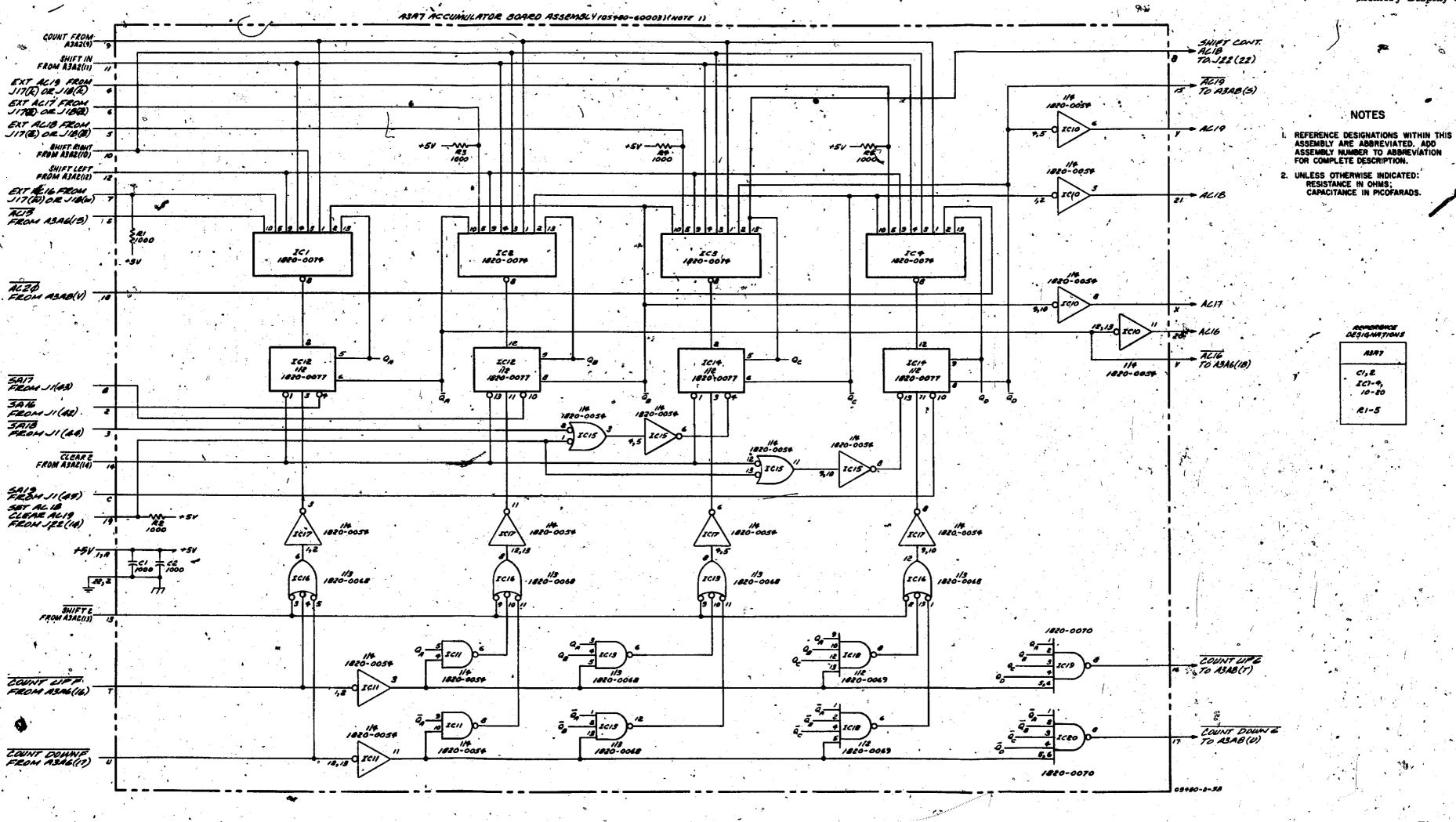


Figure 2-29
A3A7 Accumulator Series 832

Model 5480A/B Memory-Display Units

## A3A8 ACCUMULATOR ASSEMBLY (05480-60005)

## DESCRIPTION '

This assembly performs the following operations for Accumulator Bits 20-23:

Count-Up, Count-Down, Shift-Left, Shift-Right, and Shift-In

This board contains 4 of the 24 flip-flops in the Accumulator Register.

The input levels necessary for proper operation are as follows:

MODE	PIN									
	9	10	11	12.	13	T	. บ	. 14		
Count-Up	1	0 .	0	Ó	1	· P	1	1		
Count-Down	1	0	0	0	1	1	P	1		
Shift-Left	0	0	10	1	P	1	1	1 .		
Shift-Right	° 0	1	0	0	P	1	1	1		
Shift-In	0	0	. 1.	0	P	1	1.	1		
Clear	· ?	?	?	?	1	1	1	<u>`o</u>		

P = Inverted Pulses

? = Don't Care (i. e., 1 or 0)

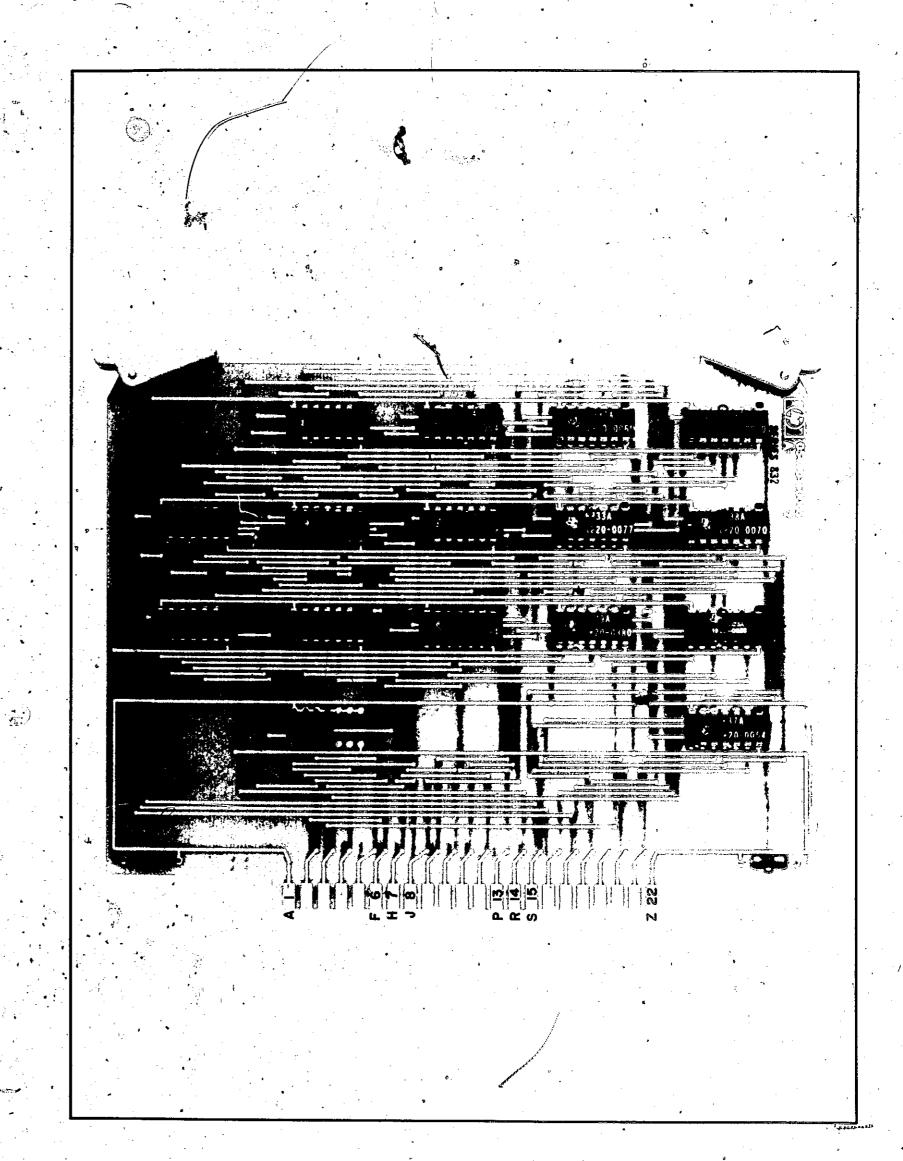
Flip-Flop Outputs are 20, X, 21, Y

Set Inputs are 2, B, 3, C

### CHANGES FOR OLDER BOARDS

Current Series: 832

Older Series: None



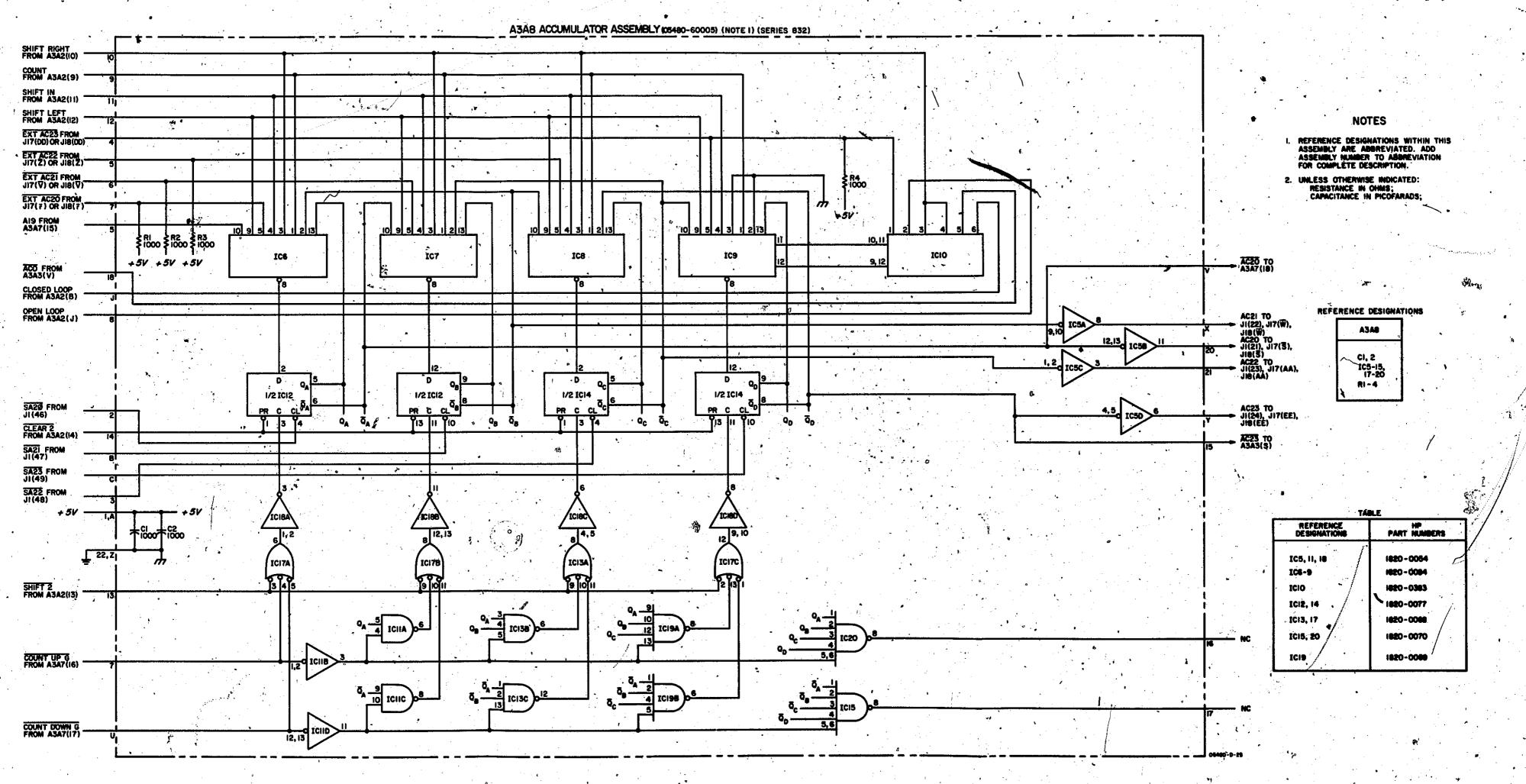


Figure 2-30
A3A8 Accumulator Series 832

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A3A9 ADDRESS REGISTER (05480-60077, 05480-60007)

#### DESCRIPTION

Address Register bits 0 and 1 provide the proper address for storing or retrieving information from the magnetic core memory. This board assembly contains two flip-flops of the Display Address Régister and two flip-flops of the Process Address Register.

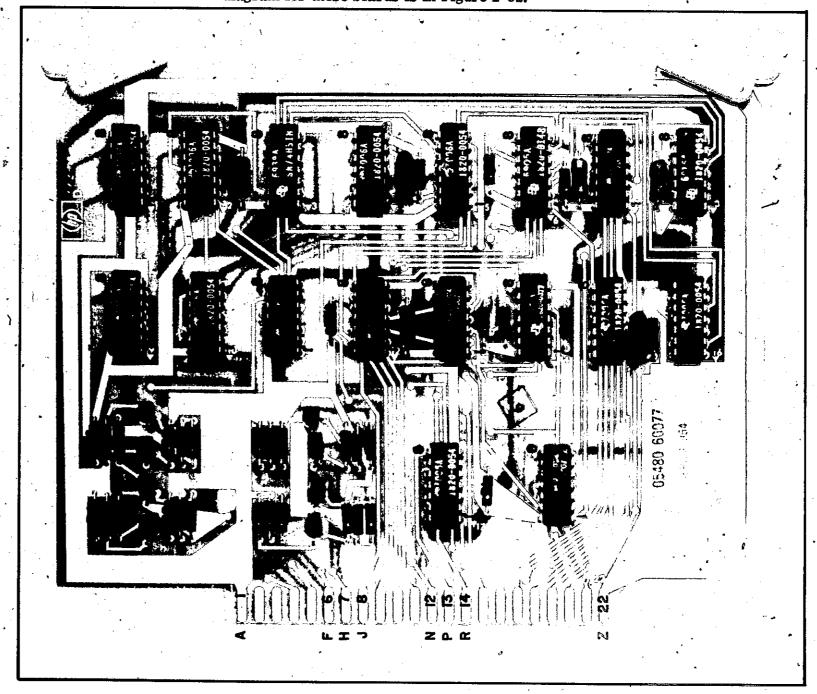
The board also contains circuitry for gating the 20 MHz clock into the Count-Up or Count-Down lines.

#### CHANGES FOR OLDER BOARDS

Current Board: 05480-60077, Series 964

Older Boards: 05480-60007, Series 852 and 832

The Series 852 and 832 board circuits and components are identical, the same schematic drawing may be used for both. The schematic diagram for these boards is in Figure 2-32.



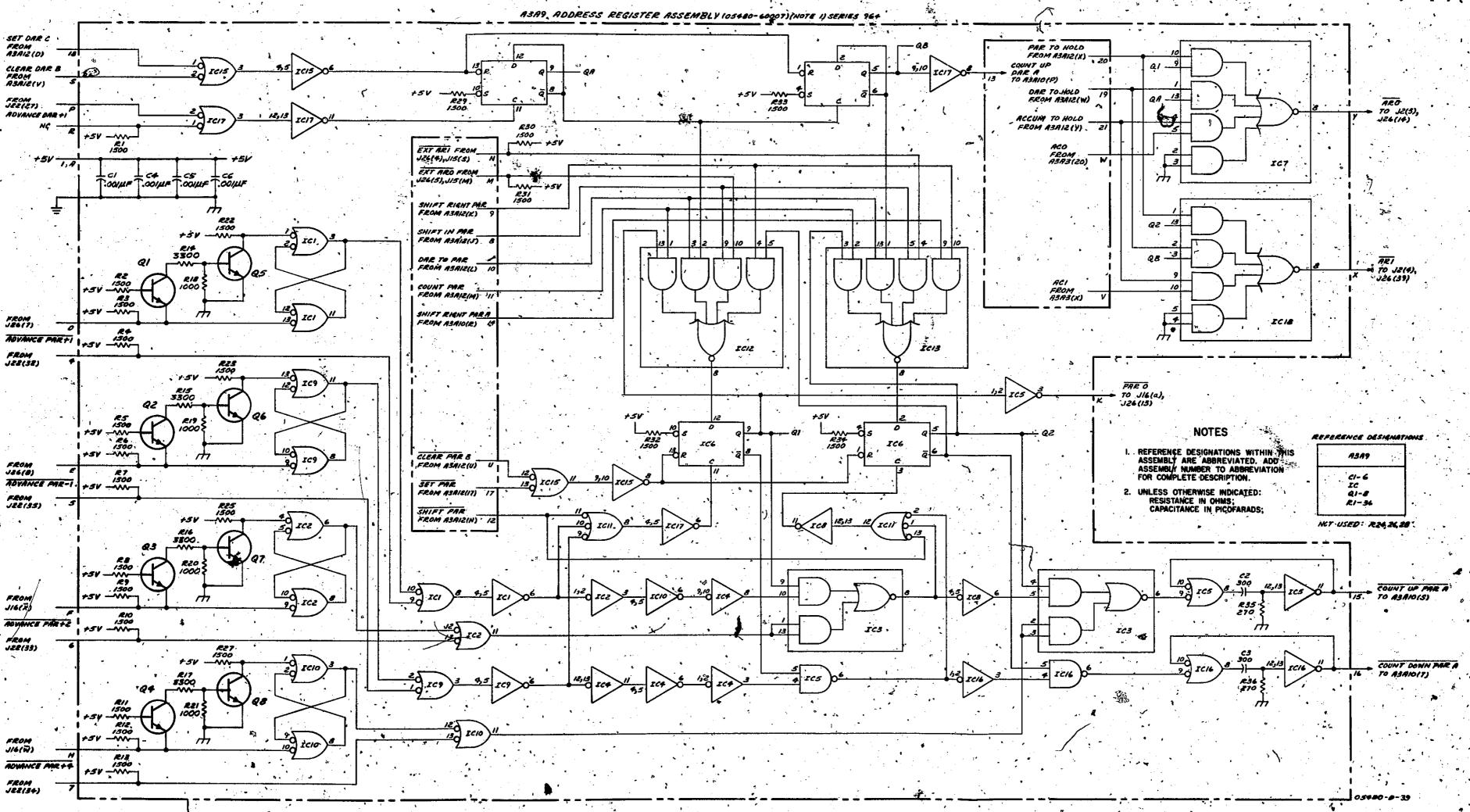
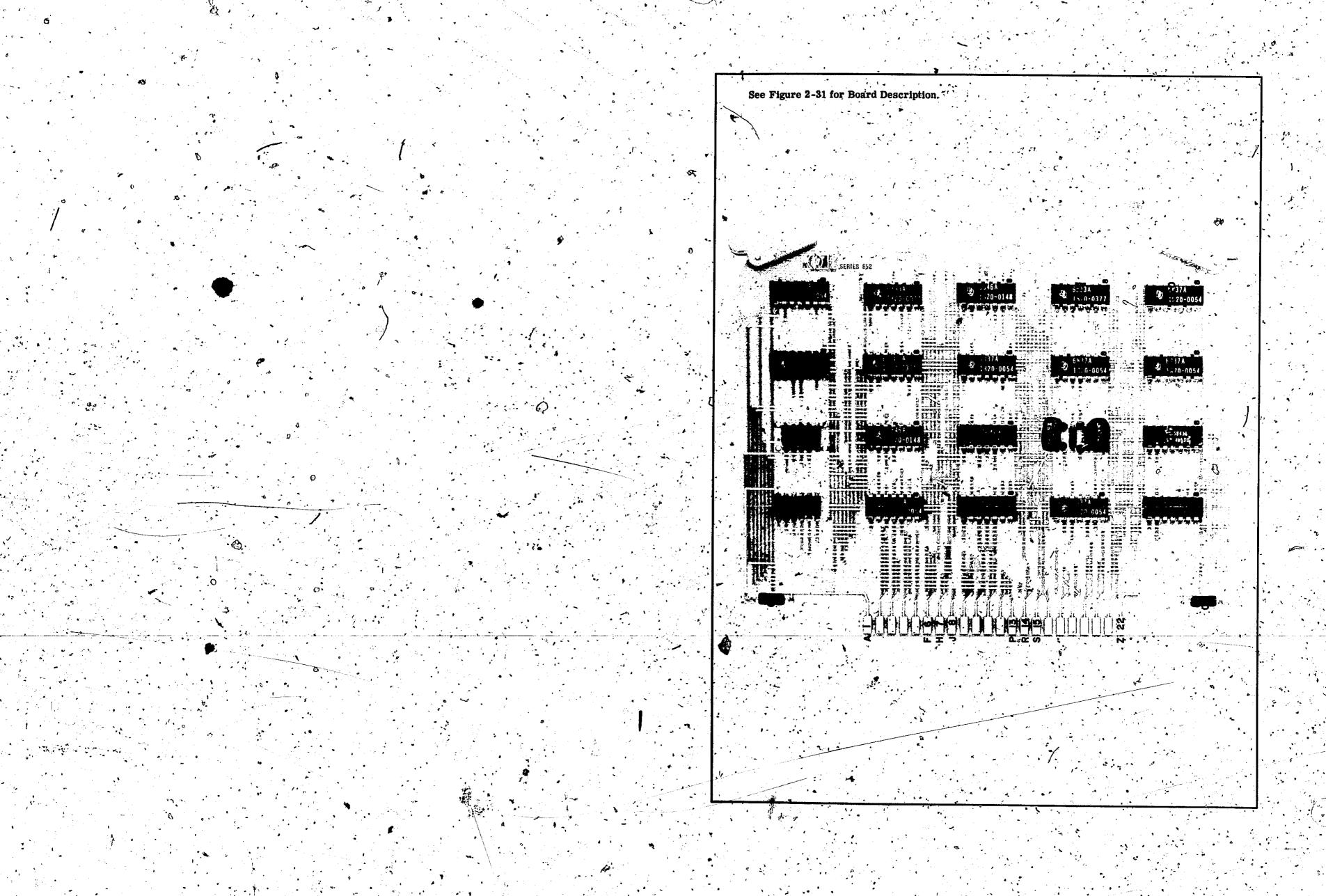


Figure 2-31 A3A9 Address Register Series 964



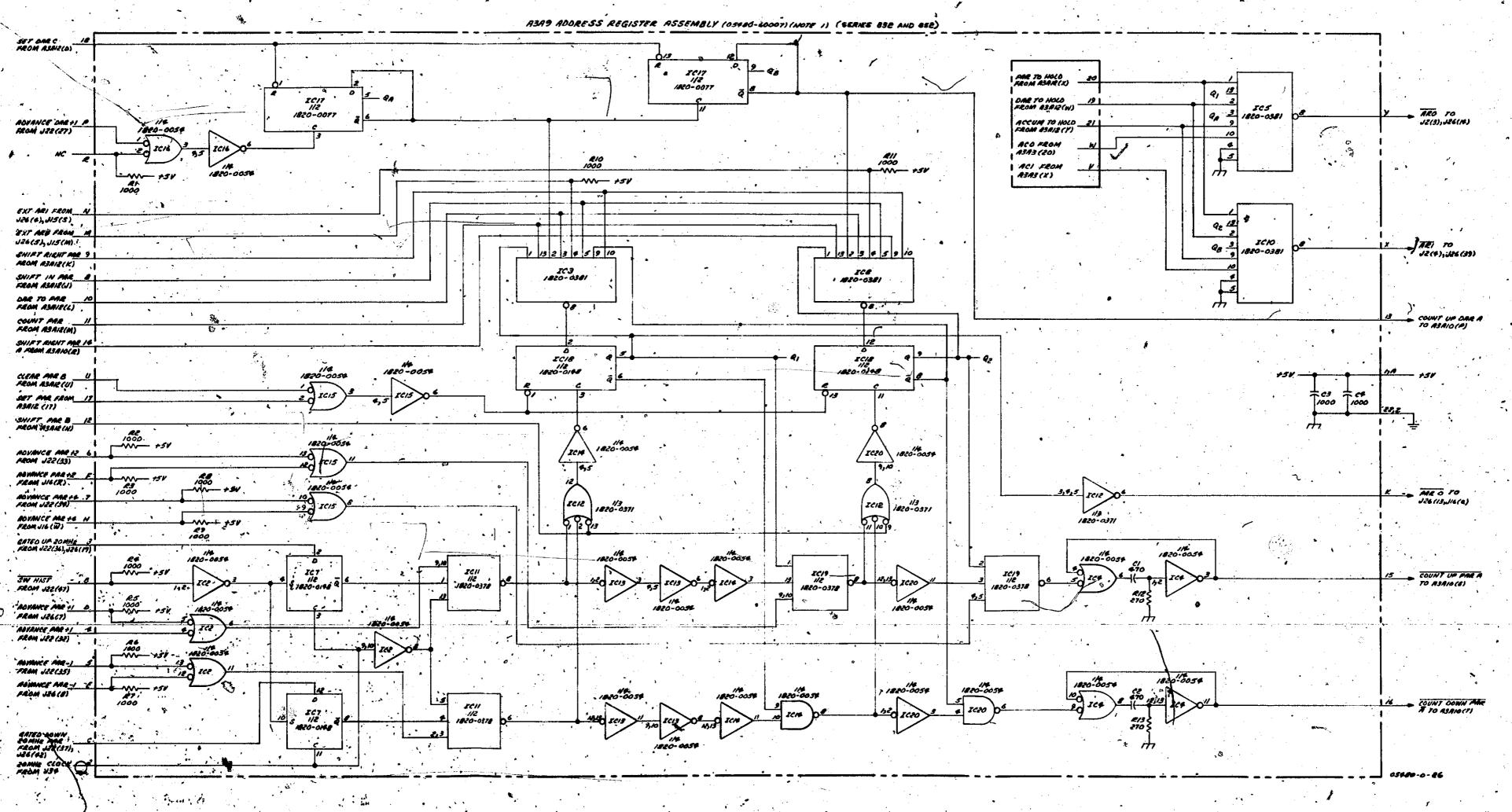


Figure 2-32 A3A9 Address Register Series 832, 852

## A3A10, A3A11 ADDRESS REGISTER ASSEMBLY (05480-60006)

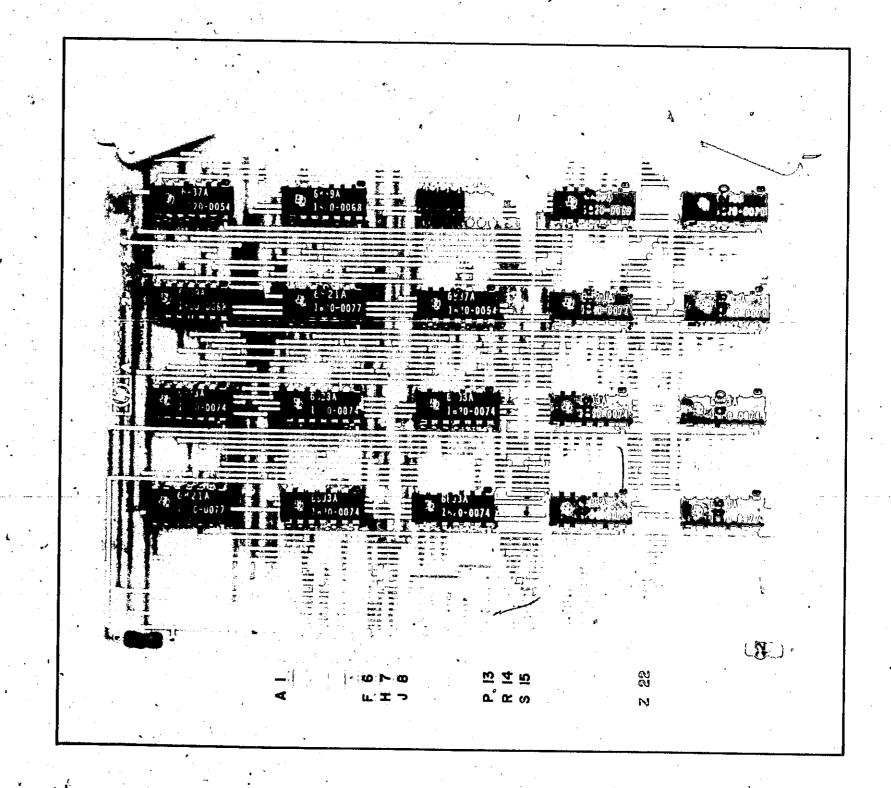
#### DESCRIPTION

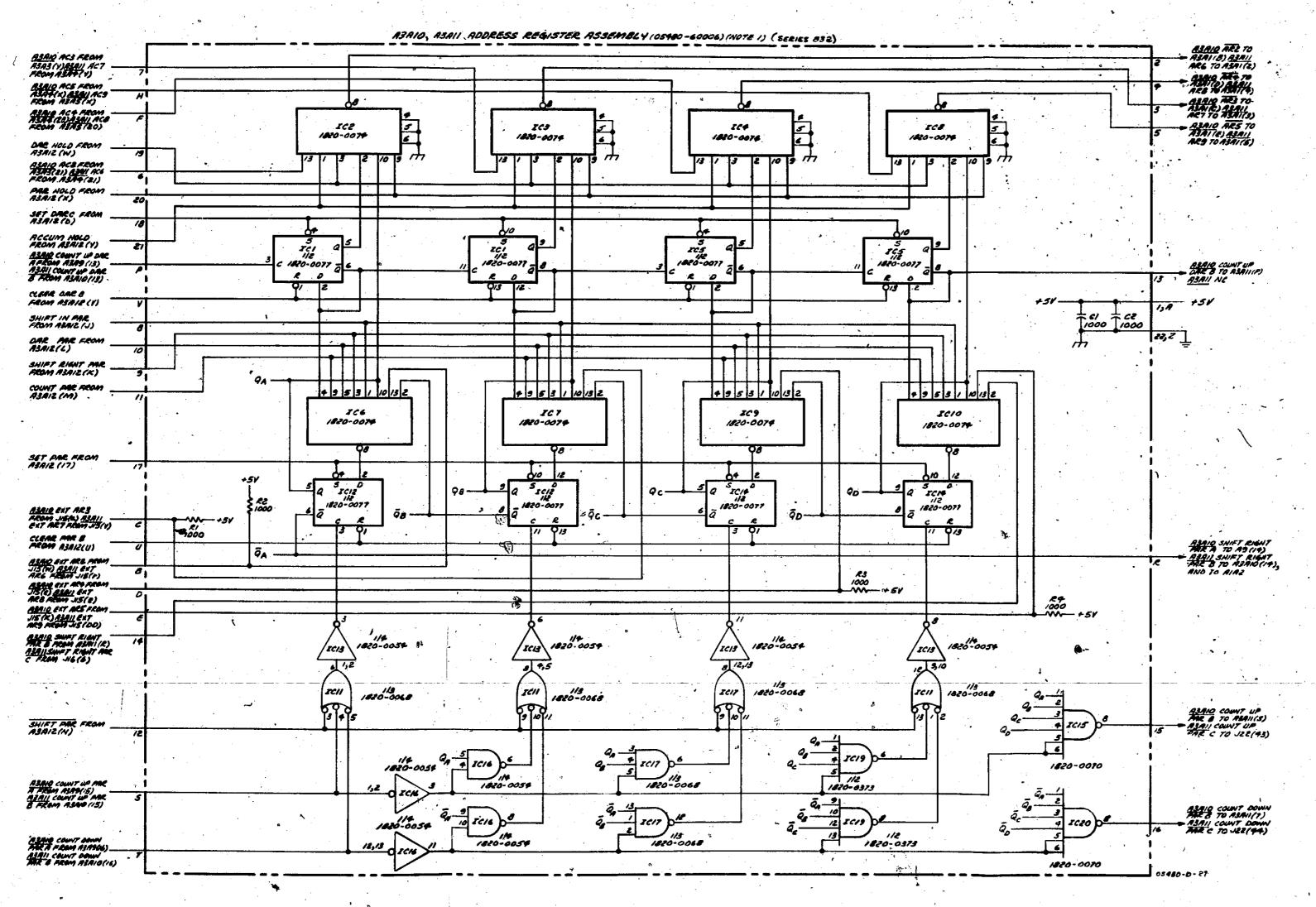
The 05480-60006 Address Register provides the proper address for storing or retrieving information from the magnetic core memory. The board contains four flip-flops of the Display Address Register, which is used for all display, interlaced display, and output operations; and four flip-flops of the process address register, which is used for all data input and processing operations.

### CHANGES FOR OLDER BOARDS

Current Series: 832

Older Series: None





NOTES

- I. REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMMITTE DESCRIPTION.
- 2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS; CAPACITANCE IN PICOFARADS;

REFERENCE DESIGNATIONS

A3A10, A3A1.

C1, 2

EC1-17,
19,20

R1-4

## A3A12 ADDRESS REGISTER CONTROL ASSEMBLY (05480-60075, 05480-60008)

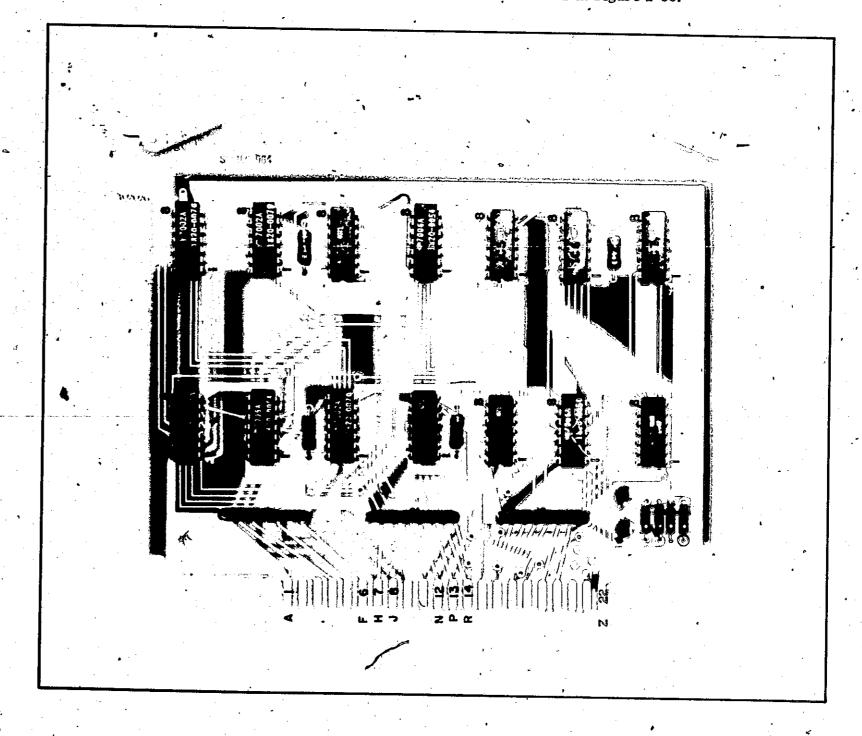
#### DESCRIPTION

The Address Register Control provides proper levels for operation of the Address Register (A3A9, A3A10). The board contains a flip-flop which holds the Process Address Register in one of four operating modes: Count, Shift-Right, Shift-In, and Transfer DAR to PAR; a flip-flop for determining which information is sent to the Horizontal DAC (Process Address Register, Display Address Register, or Accumulator); and four buffer amplifiers for the following pulses: Clear Process Address Register, Clear Display Address Register, Shift Clock, and Set Process Address Register.

## CHANGES FOR OLDER BOARDS

Current Board: 05480-60075, Series 964
Older Boards: 05480-60008, Series 832

The schematic diagram for the older board is in Figure 2-35.



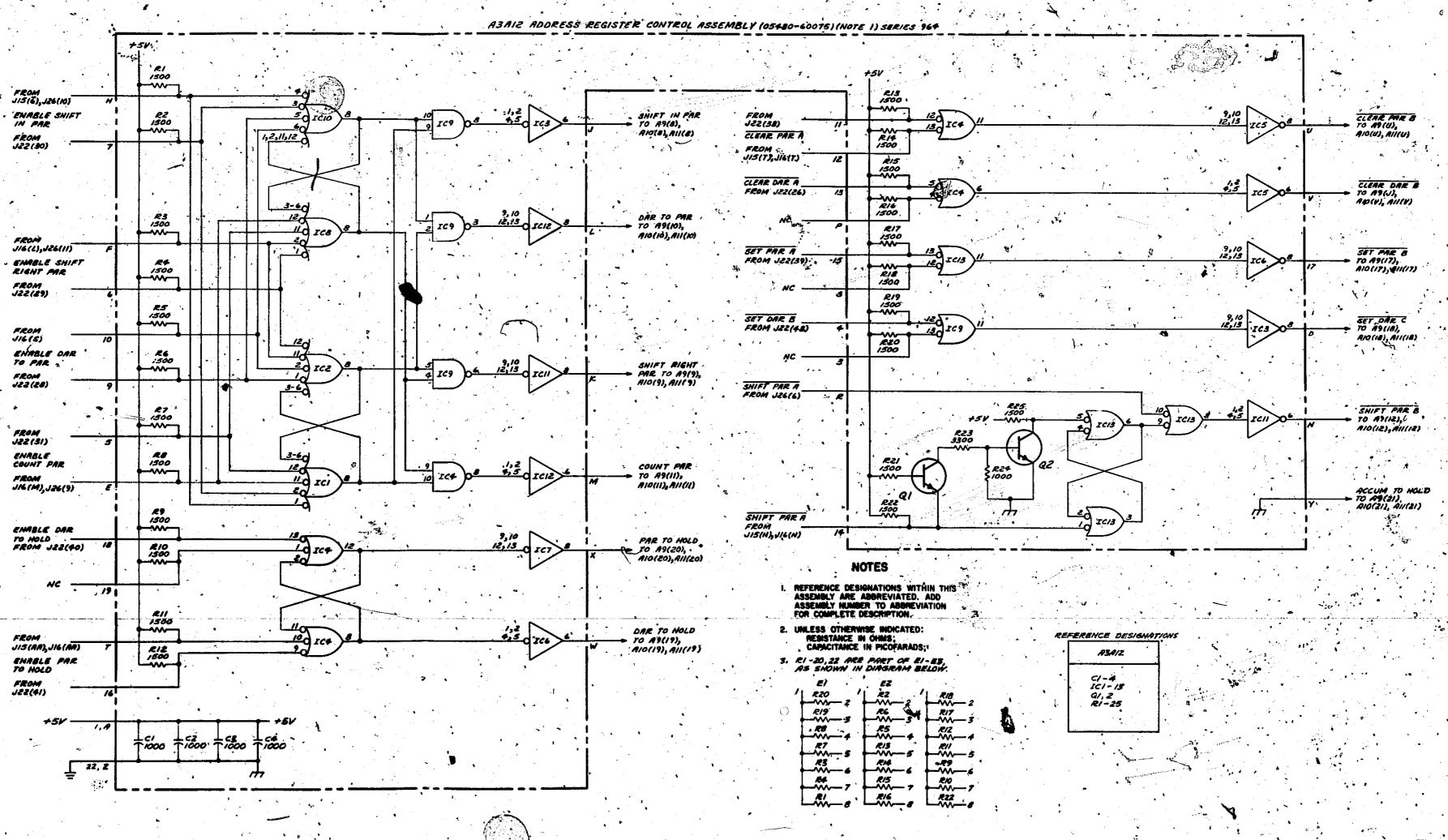
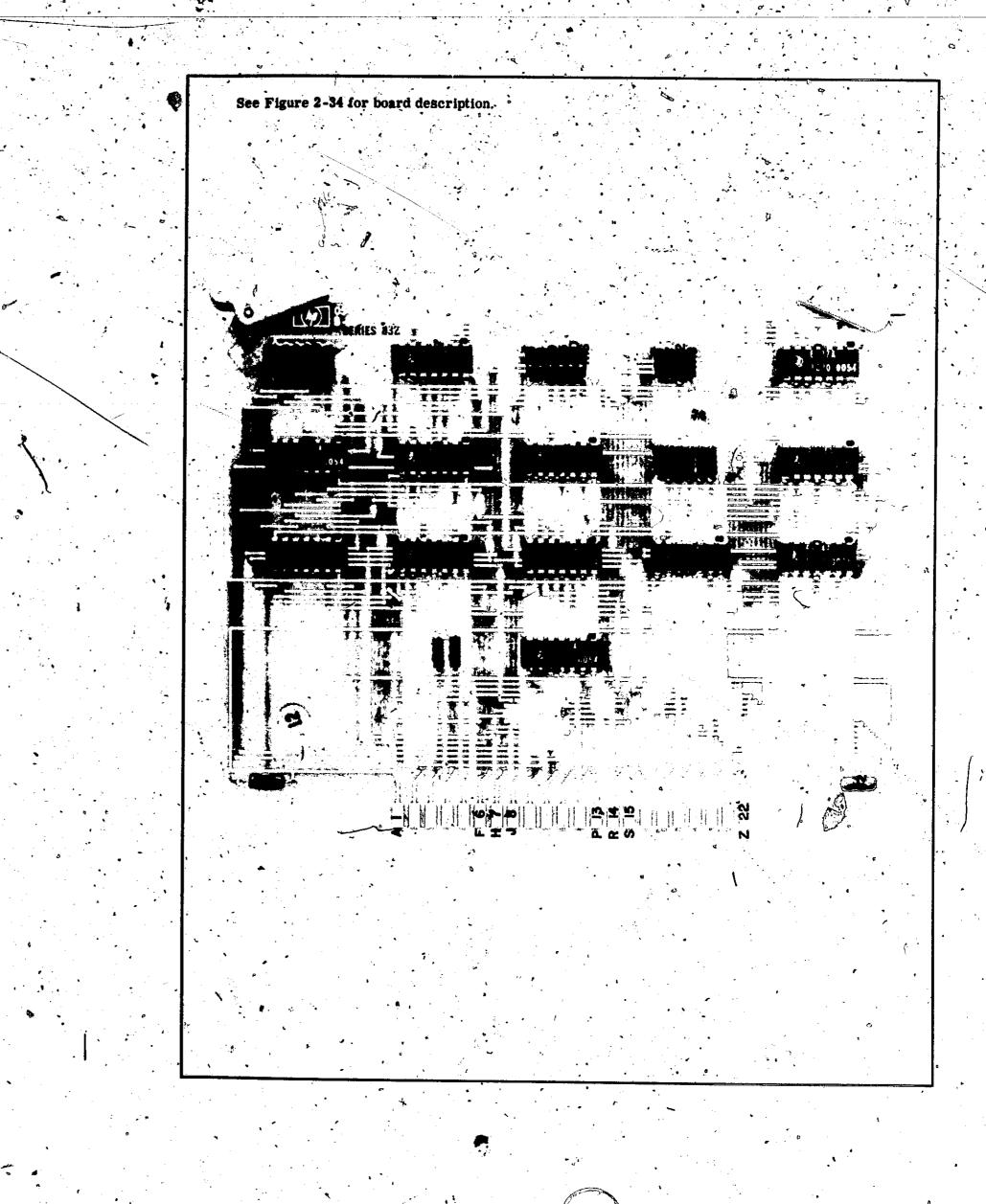
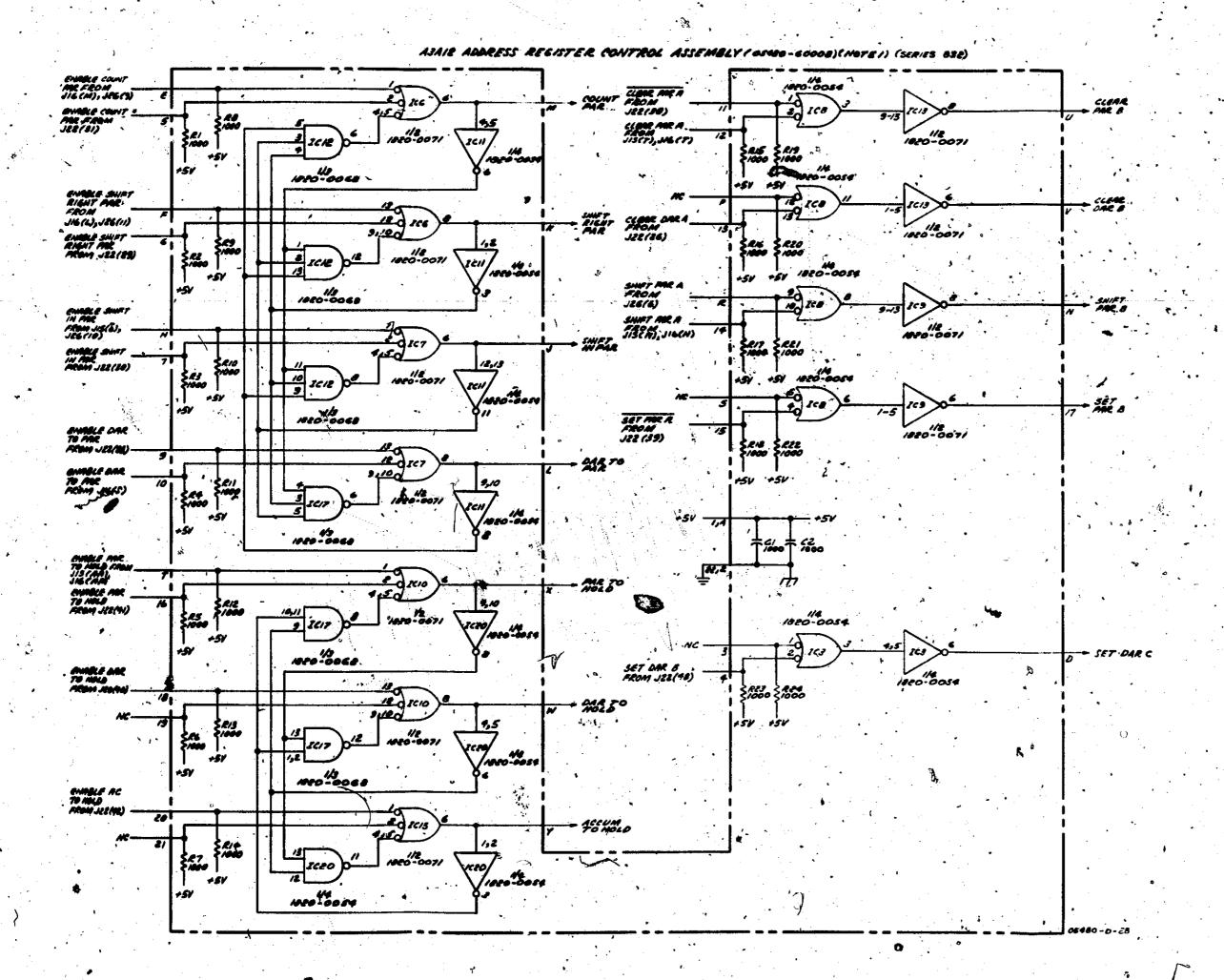


Figure 2-34
A3A12 Address Register Control Series 964





#### , NOTES

REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION.

2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS; CAPACITANCE IN PICOFARADS;

#### REFERENCE DESIGNATION.

	A3AIZ
٠	C1,2
	15,17,20
	R1-24

Figure 2-35 A3A12 Address Register Control Series 832 2-71

#### A4A1 LIGHT DRIVER AND FLIP/FLOP ASSEMBLY (05480-60012)

#### **DESCRIPTION** ·

The 05480-60012 consists of two 4-state flip-flops. One 4-state flip-flop indicates which pushbutton was last pushed. The other 4-state flip-flop drives one of four transistors, which in turn drives one of four front-panel indicator lights.

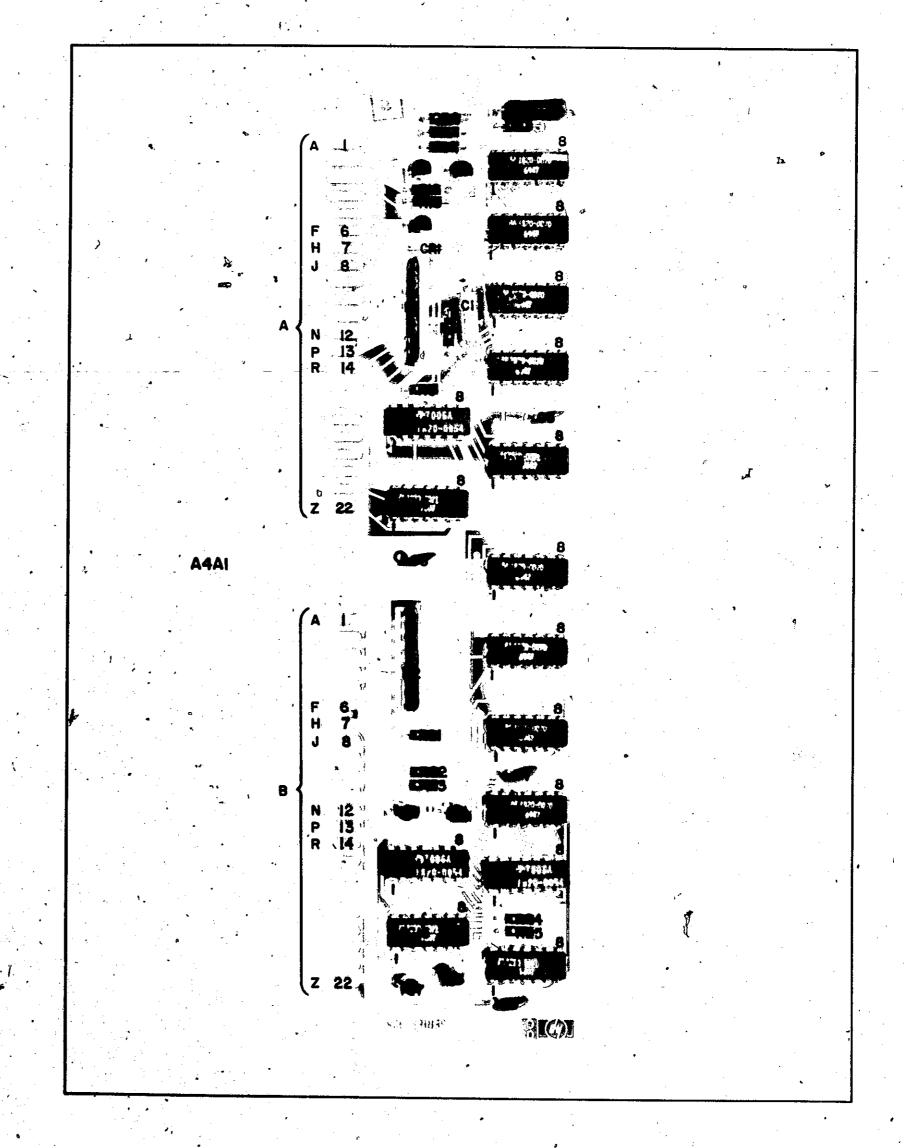
The board also has a voltage monitor which indicates when the voltage difference between the +19.5 V and -19.5 V supplies is less than 30 V, and causes the RE-SET lamp to light

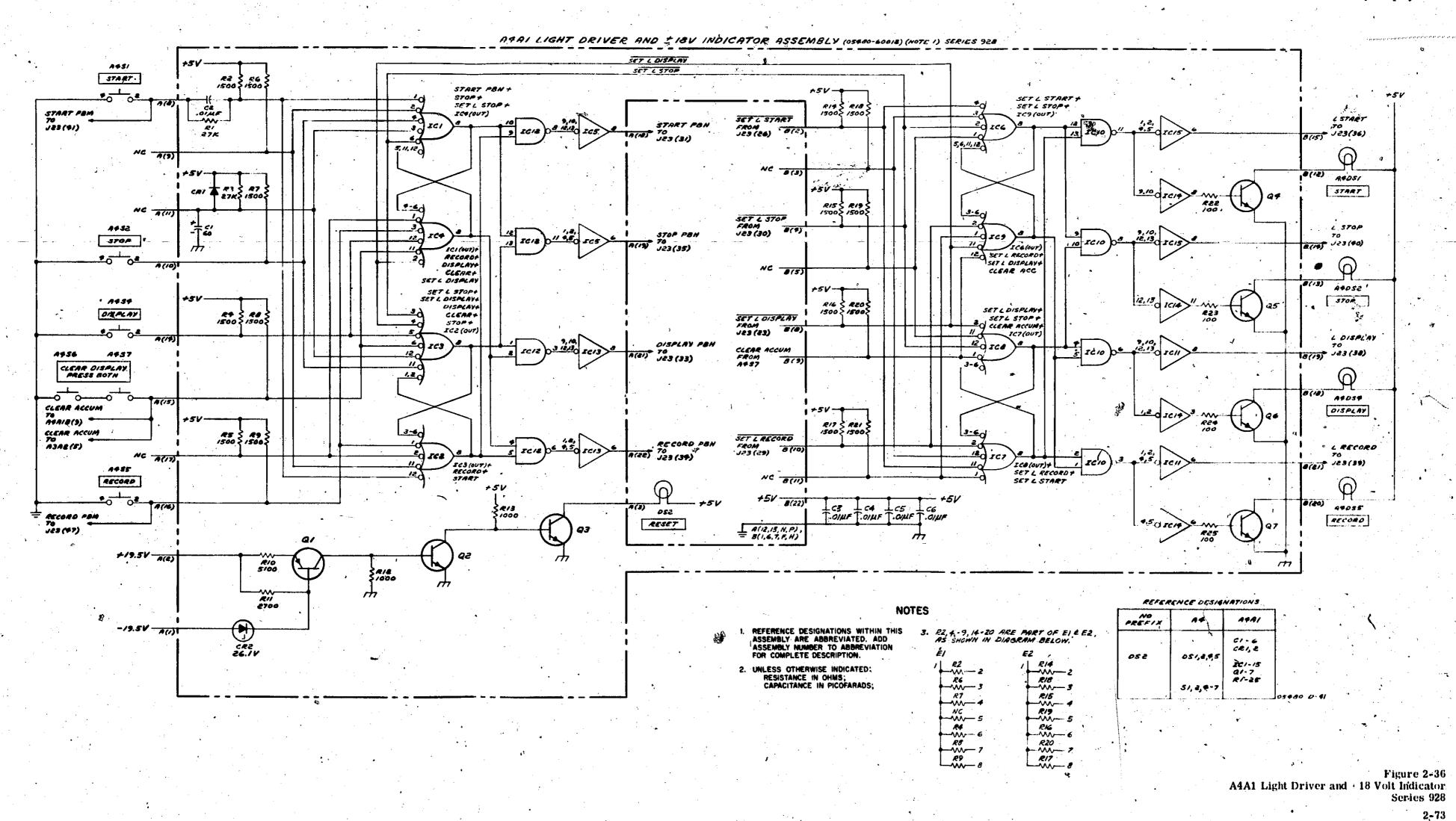
#### CHANGES FOR OLDER BOARDS

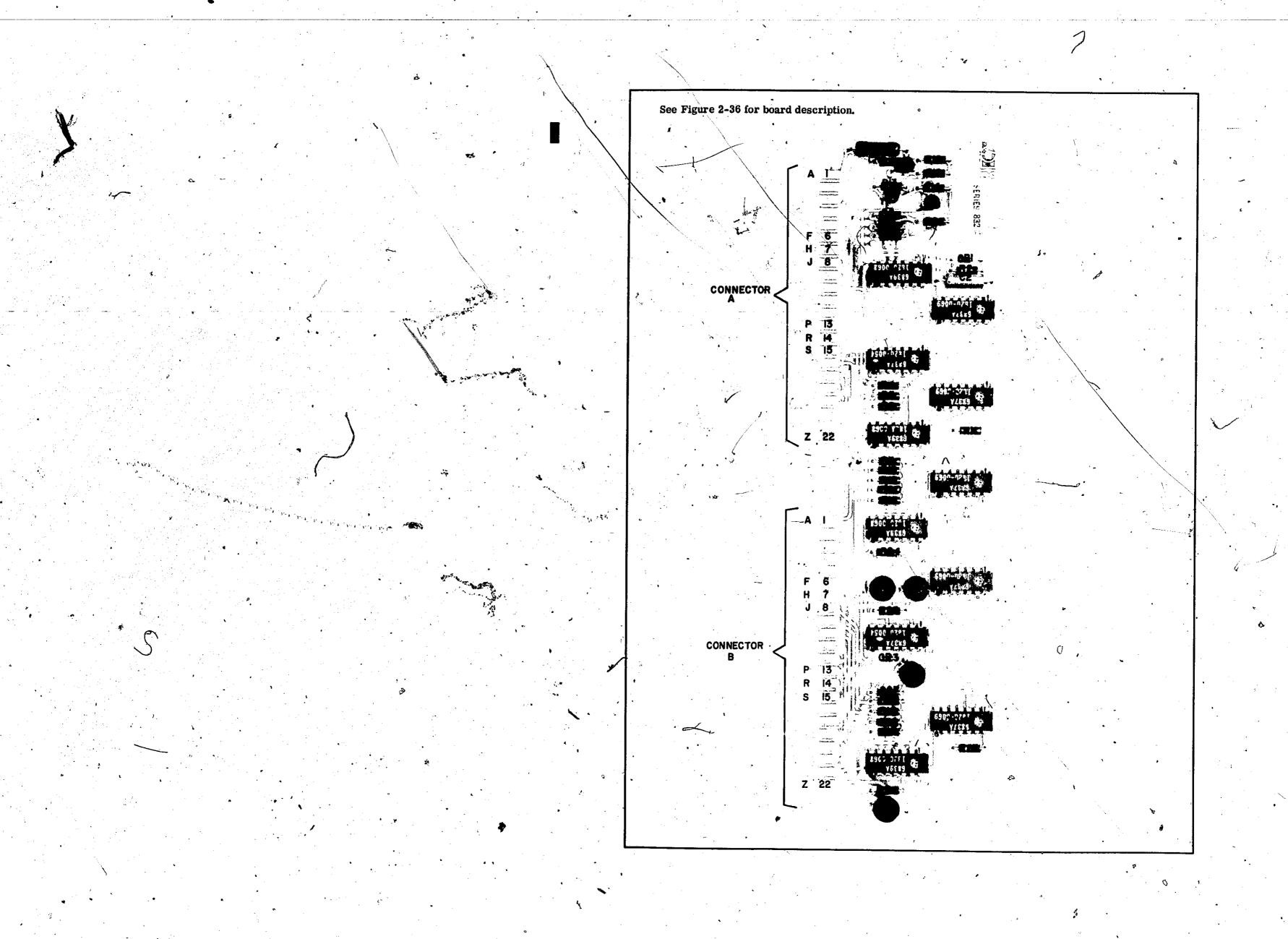
Current Series: 928

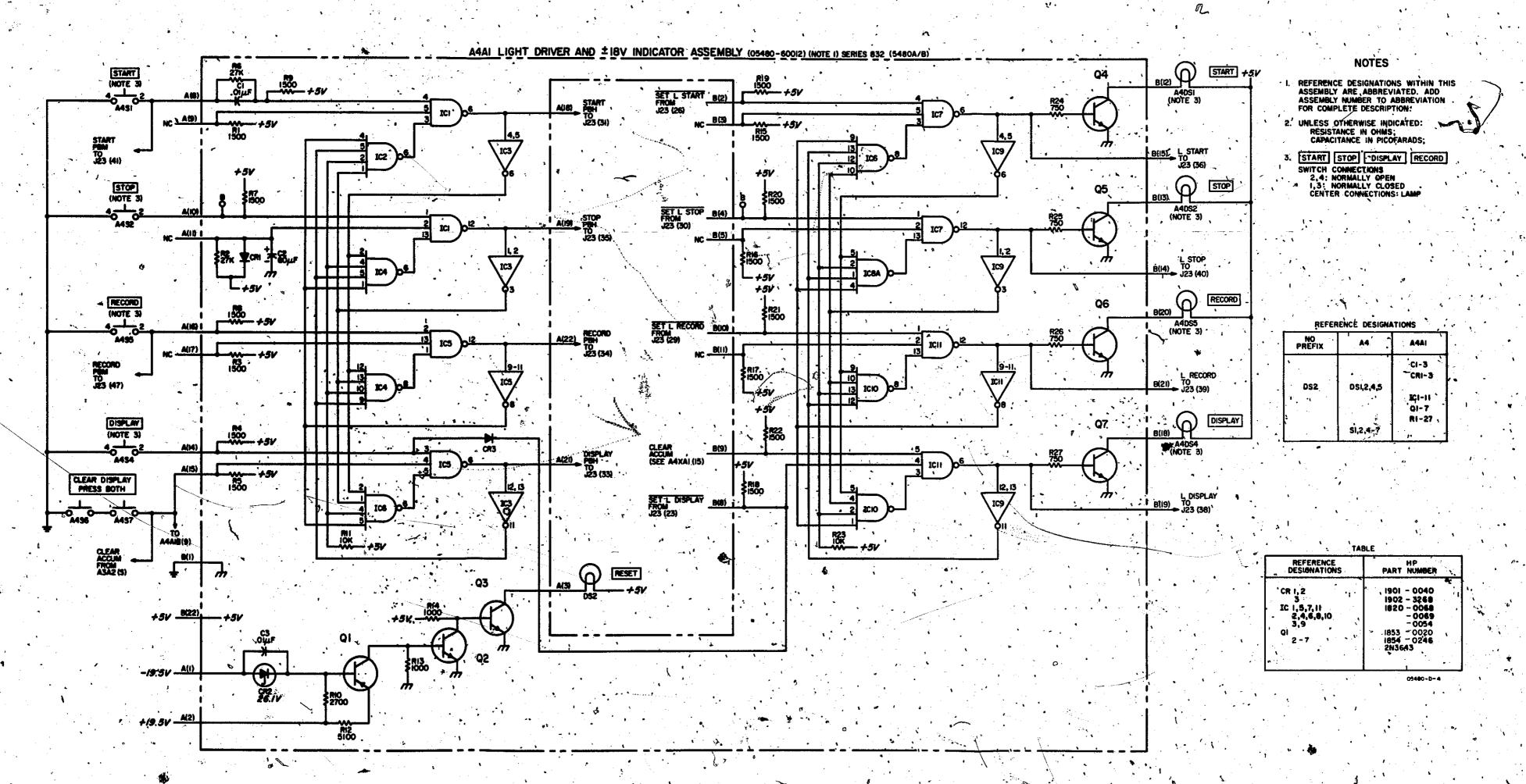
Older Series: 832 (see Figure 2-37)

The current board is a direct replacement for the older board.









A4A1 Light Driver and ± 18 Volt Indicator Series 832

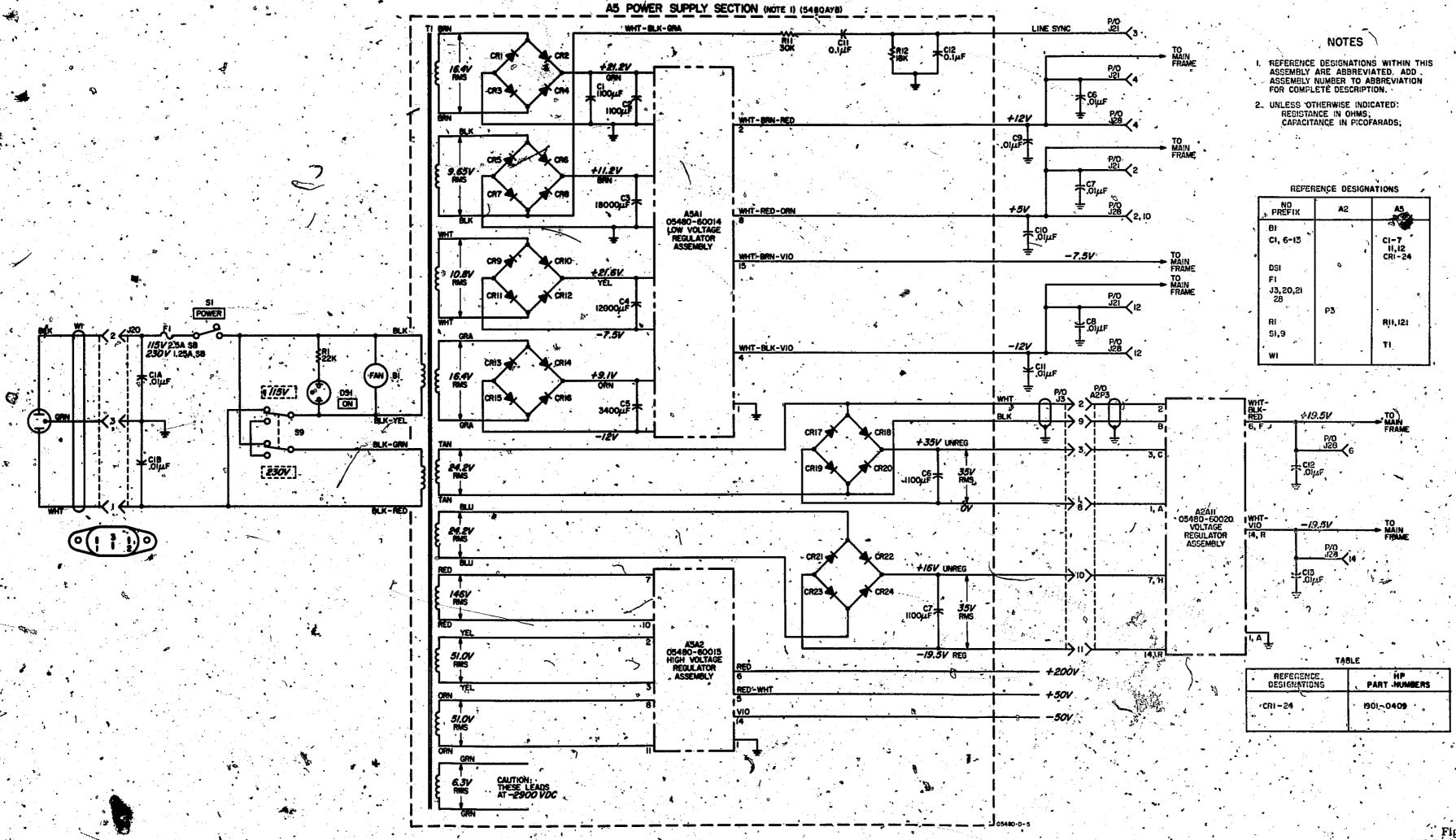


Figure 2-38
A5 Power Supply 2 2-77

### · A5A1 LOW VOLTAGE REGULATOR (05480-60014)

### DESCRIPTION

The Low Voltage Regulator includes four power supplies to drive digital, analog, and memory circuitry. The nominal voltages are +12 V, +5 V, -7.5 V, and -12 V. All supplies are current-limited so they will withstand output short circuits. In addition, the +5 V supply includes SCR crowbar circuitry to prevent output over-voltages from damaging the load.

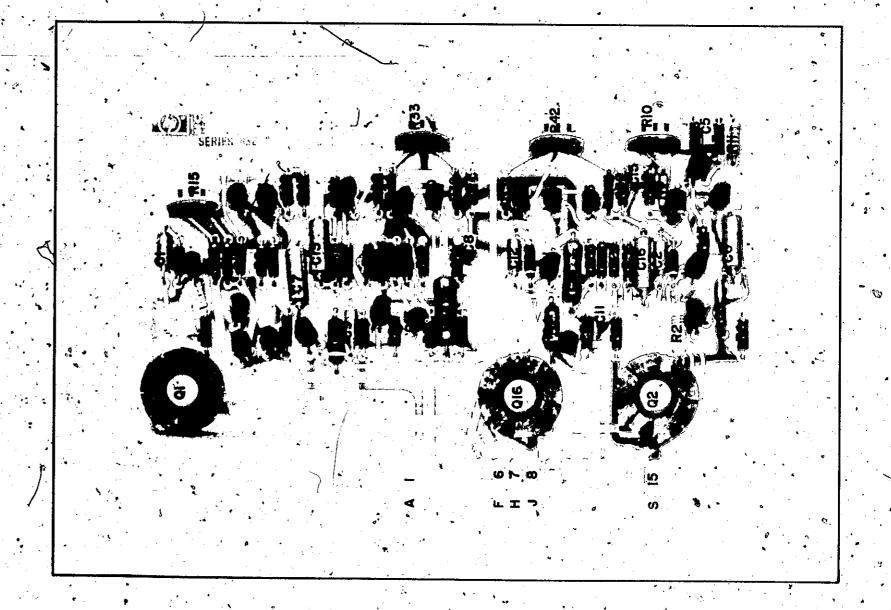
## CHANGES FOR OLDER BOARDS

Current Series: 832

Ò

Some boards with this series number have a . 01 UF capacitor at C14. The current capacitor (0. 22 UF) provides better suppression of a parasitic oscillation which was found in some of these supplies.

Older Series: None.



#### NOTES

I REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION

2' UNLESS OTHERWISE INDICATED:
RESISTANCE IN OHMS,
CAPACITANCE IN PICOFARADS,

REFERENCE DESIGNATIONS ÂS. 4541 CI-16 CRI-3 QI-26 RI-48 CRI,25,86 QI-5 RI-6,10 DELETED:

Figure -2-39 . A5A1 Low Voltage Regulator Series 832

#### A5A2 HIGH VOLTAGE REGULATOR (05480-60015)

### DESCRIPTION

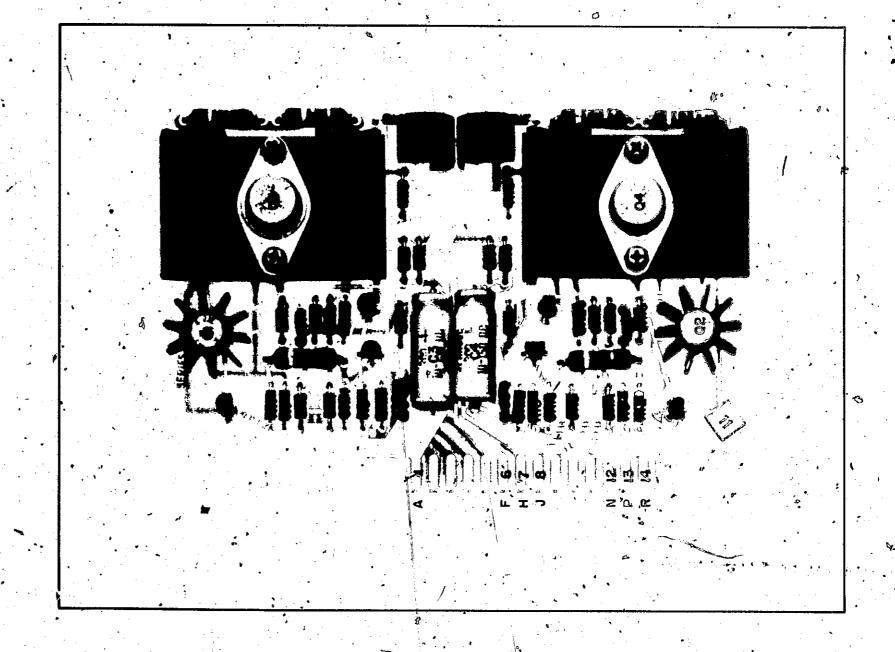
This board provides power to the CRT, CRT High Voltage Supply, and Deflection Amplifiers. The plus- and minus-50 V supplies are regulated and current-limited; they are used for the CRT HV supply and electrodes in the CRT. The nominal +200 V is unregulated, has current limiting, and with the -50 V supply is used in the Vertical and Horizontal Deflection Amplifiers.

### CHANGES FOR OLDER INSTRUMENTS

Current Series: 928

Older Series: 832 (see Figure 2-41)

The current board is a direct replacement for the older board.



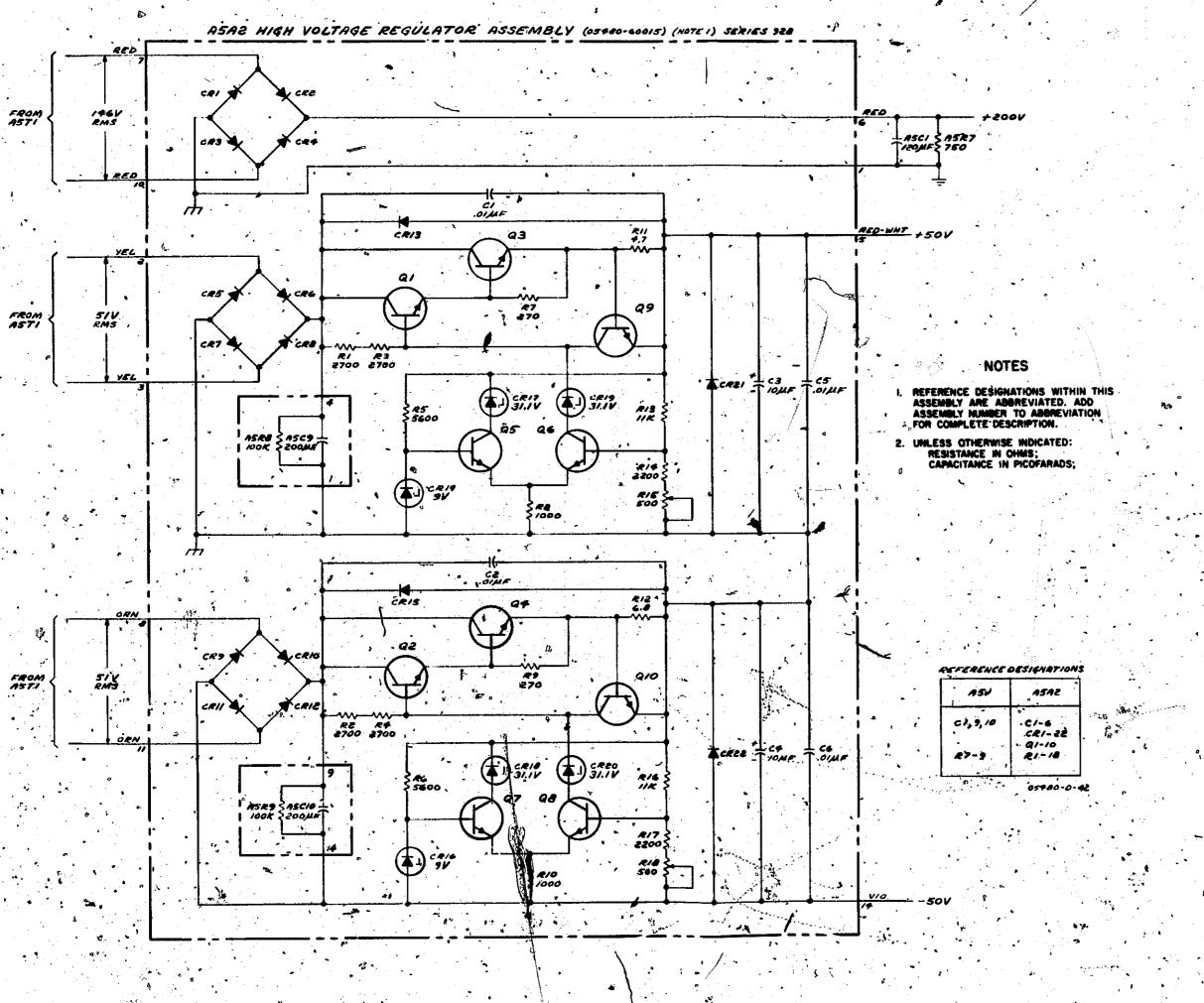


Figure 2-40 A5A2 High Voltage Regulator Series 928

