START

VOLUME I

SIGNAL ANALYZER SYSTEM

5480A/B
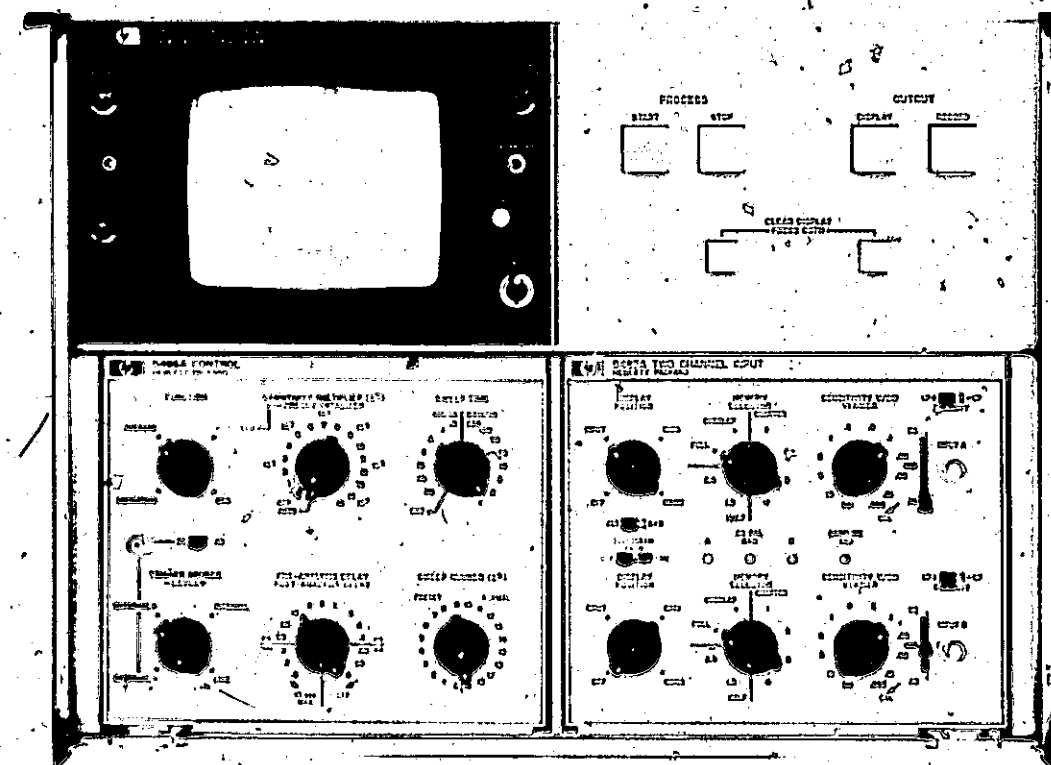
HEWLETT **hp** *PACKARD*

CERTIFICATION

The Hewlett-Packard Company certifies that this instrument was thoroughly tested and inspected and found to meet its published specifications when it was shipped from the factory. The Hewlett-Packard Company further certifies that its calibration measurements are traceable to the U.S. National Bureau of Standards to the extent allowed by the Bureau's calibration facility.

WARRANTY AND ASSISTANCE

All Hewlett-Packard products are warranted against defects in materials and workmanship. This warranty applies for one year from the date of delivery, or, in the case of certain major components listed in the operating manual, for the specified period. We will repair or replace products which prove to be defective during the warranty period provided they are returned to Hewlett-Packard. No other warranty is expressed or implied. We are not liable for consequential damages.

Service contracts or customer assistance agreements are available for Hewlett-Packard products that require maintenance and repair on-site.

For any assistance, contact your nearest Hewlett-Packard Sales and Service Office. Addresses are provided at the back of this manual.

SIGNAL ANALYZER
5480A/B with 5485A, 5486A/B,
5487A, 5488A PLUG-INS

ALL SERIALS

This manual applies to all standard Hewlett-Packard Model 5480A/B Signal Analyzer Systems, as described in Section I of this manual. Refer to INSTRUMENT IDENTIFICATION, Paragraph 1-5.

SPECIAL INSTRUMENTS

The information required to relate this manual to special modifications is supplied on special insert sheets. If this information is missing, contact any HP Sales and Service Office, giving full specification number, instrument name, and serial number.

HEWLETT **hp** PACKARD

GENERAL INFORMATION

MANUAL CONTENT AND ORGANIZATION

This manual provides general information required for servicing Hewlett-Packard 5480A/B Signal Analyzer Systems. Operating information and specific servicing information for each unit in the system are in other manuals, as follows:

Operating Manual

General system information

Packaging information

System installation

Incoming Inspection Check

Operating information

Service Manual – Volume I

System block diagrams, including principles of operation

Adjustment procedures

Adjustment and test point locations and waveforms

Troubleshooting procedures

Service Manual – Volumes II and Higher

Each of these manuals is for one unit in the Signal Analyzer System, and contains the following information:

General information about the unit

Wiring and schematic drawings and/or lists

Component locators

Parts lists

Assembly function descriptions

MANUAL AND MANUAL CHANGES (ORDERING INFORMATION)

This manual provides general servicing information for all Hewlett-Packard 5480A/B Signal Analyzer Systems. Manual changes for special instruments will be detailed in special change sheets included with manuals for those systems. If information is missing, it may be obtained by contacting the nearest Hewlett-Packard sales and service office listed at the back of this manual. When requesting information or additional manuals for your system, be sure to include complete Model or Specification number, instrument name, and serial number. (For instrument identification information, see Paragraph 1-5.)

TABLE OF CONTENTS

LIST OF TABLES

LIST OF TABLES (Cont'd)

LIST OF FIGURES

Figure 1-1. 5480A/B Signal Analyzer System

MODEL 5480A/B

RACK MOUNTING KIT

POWER CABLE

EXTENDER BOARDS
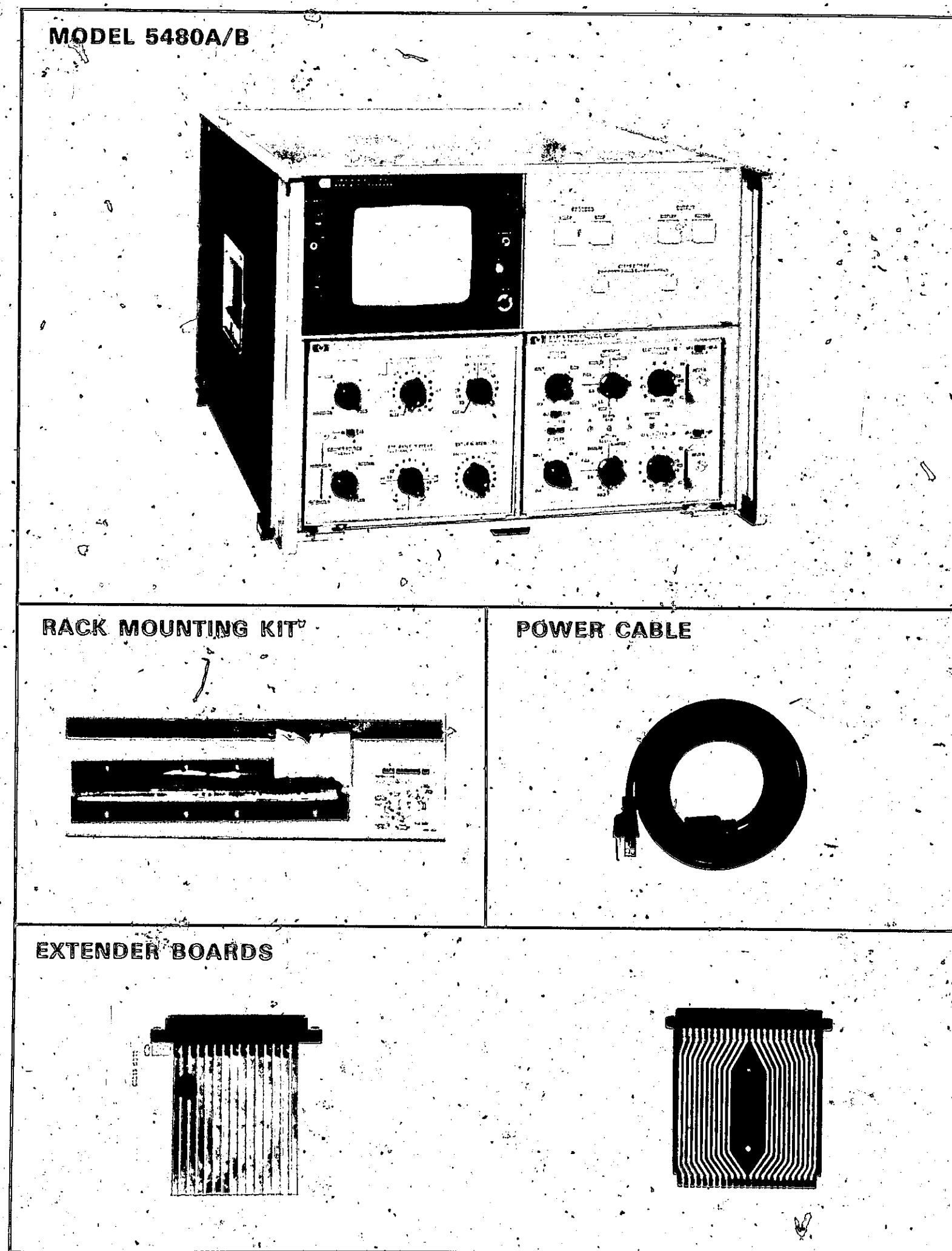
SECTION I

GENERAL INFORMATION

1-1. SYSTEM DESCRIPTION

1-2. General

1-3. The Hewlett-Packard 5480A/B Signal Analyzer Systems are:

a. Oscilloscopes for looking at noisy signals.

b. Digital computers that can perform Averaging, Summation, Histogram, and Multichannel Scaling (MCS) functions and display the results on an integral CRT.

1-4. In the AVERAGE and SUMMATION functions, data are accepted in analog form at the input, converted to digital form, processed according to routines selected by the FUNCTION switch, and stored in an internal 1024-word, 24 bits-per-word magnetic core memory stack. In HISTOGRAM and MCS functions, input data are directly processed according to routines selected by the FUNCTION switch. The Signal Analyzer System's primary intended use is as a signal averager using digital data processing to extract repetitive waveforms from noisy signals. Complete technical specifications for the 5480B are given in Table 1-1. Specification differences between the 5480B and the 5480A are given in Table 1-2.

1-5. INSTRUMENT IDENTIFICATION

1-6. General

1-7. Each unit in the 5480A/B Signal Analyzer System is identified by a model number and name as a separate instrument as listed below.

5480B Signal Analyzer System consists of:

5480B Memory/Display Unit (Main Frame Unit)
5486B Control (Logic Plug-in Unit)
One Analog Plug-in Unit from the list below

5480A Signal Analyzer System consists of:

5480A Memory/Display Unit (Main Frame Unit)
5486A Control (Logic Plug-in Unit)
One Analog Plug-in Unit from the list below

Analog Plug-in Units (may be used in either 5480A or 5480B Signal Analyzer System):

5485A Two Channel Input
5487A Four Channel Input
5488A Average/Correlation Input

1-8. Although the 5486A can be used with the 5480B, and the 5486B can be used with the 5480A, such use

is not recommended, and performance of these combinations is, therefore, not specified. The 5486B should be used only with the 5480B, and the 5486A used only with the 5480A.

1-9. Serial Numbers

1-10. Each of the units listed in Paragraph 1-6 is identified by a two-section, eight-digit (000-00000) serial number located on its rear panel. The five-digit portion of the serial number is unique to each instrument (with that model number); the three-digit portion is used to document changes. Include the complete serial number, model number, and instrument name in correspondence about any unit in your Signal Analyzer System.

1-11. CRT WARRANTY

1-12. The 5480A/B and its plug-ins are certified and warranted as stated on inside front cover of this manual. The CRT (Cathode Ray Tube), however, is covered by a warranty separate from the rest of the instrument. The CRT warranty and warranty claim forms are located at the back of the Operating Manual. Should CRT fail within time specified on warranty, return CRT with warranty form completed.

1-13. STORAGE AND SHIPMENT

1-14. Packaging

1-15. To protect valuable electronic equipment during storage or shipment, always use the best packaging methods available. Your Hewlett-Packard Sales and Service Office can provide packaging material such as that used for original factory packaging. Contract packaging companies in many cities can provide dependable packaging on short notice.

1-16. OPERATING INSTRUCTIONS

1-17. Operating instructions similar to those in the System Operating Manual are in Figs. 1-2 thru 1-13.

1-18. DOCUMENTATION

1-19. The Signal Analyzer System described throughout this manual consists of:

5480A/B Memory/Display (Main Frame Unit, MFU) and 5486A/B Control Plug-in Unit (Logic Plug-in, LPI) and one of the following Analog Plug-in Units (API):

5485A Two-Channel Input
5487A Four-Channel Input
5488A Average/Correlation Input

1-20. SERVICE AIDS

1-21. Service Kit

1-22. This System Service Manual is intended to support the service kits for the HP 5480A/B Signal Analyzers. The kits contain most-often-needed replaceable parts (see lists in Section IV) for the following 5480A/B system units:

5480A/B Memory/Display
5485A Two Channel Input
5486A/B Control
5487A Four Channel Input
5488A Average/Correlation Input

1-23. Board Exchange

1-24. Hewlett-Packard provides exchange printed circuit boards for the 5480A/B system units listed in Paragraph 1-22. The board exchange program works as follows:

a. Repair 5480A/B system with Service Kit. Replace faulty boards in system with good boards from kit. Tag bad boards with a description of trouble caused in system.

b. Order rebuilt board(s) from Hewlett-Packard.

c. When a replacement board arrives, place it in the appropriate service kit. Save the box the board came in.

d. The box in which you received your replacement board will also contain an address label to be used for returning the board to the repair center, and a form to provide information about the trouble the board caused. Place the bad board in the box, along with the trouble description form (filled in), and stick the address label over the old address on the box. Send the board to the repair center.

Table 1-1. Specifications

GENERAL SPECIFICATIONS

MODELS

Specifications are for 5480B Signal Analyzer (Memory/Display mainframe), equipped with 5486B Control and 5485A Two Channel Input plug-ins.

PRIMARY FUNCTIONS

Averaging, 3 methods: Calibrated, Weighted, and Summation

Histograms: Frequency, and Time Interval

Multichannel Scaling (MCS)

Variance, option.

DIGITAL MEMORY

1024 channels (1000 for data) horizontal, by 24 bits/channel (16,777,216 to 1 range) vertical.

POWER FAIL INDICATION

Momentary power interruption activates PROCESS STOP and RESET lamp, with memory contents protected.

CALIBRATOR

(Output in PROCESS mode): Square wave, alternating between zero (+20 mV – 0 mV) and 1V (±20 mV) every 6.4 cm, at all SWEEP TIME settings.

OPERATING MODES

SELECTION

By pushbuttons which remain lighted during mode

PROCESS

Input signal is processed into memory at a rate depending on SWEEP TIME setting. START enables PROCESS to begin at next trigger input. STOP immediately terminates any mode.

OUTPUT DISPLAY

Continuously displays data from memory without flicker.

OUTPUT RECORD

Continuous display is suppressed; memory is scanned once, with SWEEP TIME set for desired rate. Simultaneous analog and digital outputs for recording.

Table 1-1. Specifications (Cont'd)

COMMON PROCESSING FUNCTIONS

GENERAL

The following functions affect data processing as well as display, and their use is common to most of the major 5480B functions and operating modes. Qualifications are noted.

MEMORY SELECTION

(Each Input Channel) Selects memory for data storage, display, or clearing. Contents of unselected segments are unchanged by any operating mode.

HORIZONTAL RESOLUTION

(10 cm trace) 1000 points for FULL memory, 500 (alternate) points for HALF, 250 (every fourth) points for QUARTER.

PRIORITY

Input A over Input B if any mutually selected segments.

OVERLAP DISPLAY

In OUTPUT DISPLAY mode all quarters displayed on four traces vertically separated for comparison. Each Input control positions two traces with 2 cm baseline separations. In PROCESS mode, acts same as FULL memory.

TRIGGER SOURCE

External:	Sweep triggered by an externally applied signal.
Coupling:	AC or DC selectable
Slope:	+ or − selectable
Trigger Level:	−4V to +4V adjustable
Minimum Input:	100 mVrms, Rise time < 10 msec.
Maximum Input:	170V peak
Input Impedance:	1 Meg ohm, shunted by 30 pF.
Internal:	Sweep triggered by internally generated pulse at end of each sweep, after POST-ANALYSIS DELAY.
Line:	Sweep triggered by power line frequency.

SWEEP TIME

1 msec/cm through 50 sec/cm in 1, 2, 5 steps; external at 5 msec/cm or slower.

HORIZONTAL DISPLAY EXPANSION

X5 Magnifier

Table 1-1. Specifications (Cont'd)

OPERATING MODES (Cont'd)

CLEAR DISPLAY

Erases memory segments in use, resets count of number of sweeps completed. Widely separated buttons must be pressed simultaneously to activate.

COMMON DISPLAY FUNCTIONS

GENERAL

The following display functions do not alter the data processing unless noted.

CRT

Included in mainframe: 8 x 10 cm rectangular display; parallax-free internal graticule, P31 phosphor. Compatible with HP 197A/198A cameras. Adaptors available for other camera systems.

CRT CONTROLS

Intensity, focus, trace alignment, horizontal position and horizontal magnifier (X1 and X5).

CONTINUOUS DISPLAY

Memory contents displayed flicker-free during PROCESS mode for all functions except MCS. Display is interlaced with data processing. Rear panel switch for disabling continuous display.

DISPLAY SELECTION

(Each input channel):

Data:	Displays contents of memory segment in use.
Input:	For all averaging functions, displays sampled input at PROCESS sweep rate during PROCESS mode.
Noise:	(For Calibrated Averaging and Weighted Averaging) Displays difference (NOISE) between sampled input and averaged data in memory during PROCESS mode. Output provided.
Off:	Disables Input channel.

VERTICAL SCALE

Range ±8.38 cm. Scale expandable in binary increments up to 2^{15} (to approximately 30 counts/cm). Expansion useable in OUTPUT modes and during PROCESS mode for SUMMATION and HISTOGRAM functions.

VERTICAL DISPLAY RESOLUTION

10 bits, 0.1% of full ±8.38 cm display.

SENSITIVITY VERNIER

(Each input channel): Variable reduction in vertical display by factor of at least 2.5 below calibrated position.

VERTICAL POSITION: (Each input channel). Display level adjustable ±4 cm.

Section I
General

Model 5480A/B

Model 5480A/B

Section I
General

Table 1-1. Specifications (Cont'd)

Table 1-1. Specifications (Cont'd)

AVERAGING

AVERAGING METHODS

Three alternate methods provided are CALIBRATED AVERAGING, WEIGHTED AVERAGING, and SUMMATION AVERAGING.

SIGNAL TO NOISE IMPROVEMENT

Proportional to the square root of the number of sweeps averaged. For white gaussian noise, up to 60 dB (1000 to 1 ratio) is possible under many circumstances. For a given experiment, the ultimate improvement is determined from the synchronous component of the Noise specification (Calibrated Averaging).

POST-ANALYSIS DELAY

Uncalibrated delay between end of sweep and readiness for next trigger. SYNC OUTPUT pulse, which is derived from trigger, is therefore delayed. Variable between 10 ms and 10 seconds, plus detented OFF position.

PRE-ANALYSIS DELAY

Calibrated delay between trigger and start of analysis sweep. Delays of 0, and 20 μsec to 0.5 sec in 1, 2, 5 sequence, 15 steps, Accuracy ($\pm$0.01%, -0 +10 μsec).

ADC (ANALOG-TO-DIGITAL-CONVERTER)

Sampling Rate: 100,000 samples/sec at 1 msec/cm through 2 samples/sec in 1, 2, 5 steps, on internal time base: 20,000 samples/sec or slower with external base.

Sample Time: 1.2 μsec

Clock Rate: 20 MHz

Resolution: 9 bits, from 50 sec/cm through 5 ms/cm, and external time base; 7 bits at 2 ms/cm; 5 bits at 1 ms/cm.

Calibrated Averaging

SWEEP NUMBER

Number of sweeps to be processed is manually selected in a binary sequence (2^N) from single sweep (0 switch position) to 2^{19} (524,288) sweeps. PRESET used for Calibrated Averaging. When the sweeps counted reaches the SWEEP NUMBER setting the PROCESS mode is terminated and OUTPUT DISPLAY activated.

INPUT CHARACTERISTICS

(For 5485A Two Channel Input)

NUMBER OF INPUTS

Two, may be simultaneously averaged into different segments of memory.

Calibrated Averaging (Cont'd)

INPUT COUPLING

AC, DC, and ground (disconnected from input), switch selectable.

BANDWIDTH

DC Coupled: DC to 50 kHz
AC Coupled: 2 Hz to 50 kHz

SENSITIVITY

5 mV/cm to 20V/cm in 1, 2, 5 steps.   Accuracy $\pm$3% at 25°C; Temp. Stability $\pm$.05% per °C.

AVERAGE BASELINE DRIFT

< $\pm$0.2% of full scale/°C for source impedance $\leq$ K Ohm.

INTEGRAL LINEARITY

$\pm$0.2% of full scale

NOISE

Synchronous, RMS < .1% of full scale; Random RMS (will average out) < 1% f.s. at 2 msec/cm and slower, < 3% f.s. at 1 msec/cm.

INPUT IMPEDANCE

1 Meg ohm shunted by 25 pF.

MAXIMUM INPUT

(Linear Operation) Equivalent to CRT deflection of $\pm$8.38 cm.

MAXIMUM INPUT

600V peak, ac coupled; dc coupled, 150V at 5 mV/cm, increasing to 350V at 20V/cm.

POLARITY INVERSION

+UP or -UP selectable.

A+B

Adds Channel B input to Channel A input and sum is fed through Channel A.   Polarity of either channel may be inverted to give difference (A - B).

ALT

Processes and displays both channels simultaneously, by means of chopping.

DC   BALANCE ADJUSTMENTS

Provided for each input.

Table 1-1. Specifications (Cont'd)

Weighted Averaging

GENERAL

Specifications are the same as for Calibrated Averaging except as follows:

SWEEP NUMBER

NORMAL setting causes Weighted Averaging Function to be automatically entered when selected SWEEP NUMBER is exceeded. This selection determines sweeps averaged, which remains fixed, and averaging is continuous.

Summation Averaging

GENERAL

Specifications under CALIBRATED AVERAGE apply except as follows:

AUTOMATIC SCALING

Display may be automatically scaled down in binary increments until SWEEP NUMBER setting is reached. Display is calibrated at each switching interval and at preset SWEEP NUMBER. Weighted averaging is not possible.

BASELINE

Can be adjusted to zero volts.

HISTOGRAMS

Frequency Histogram

DISPLAY

Relative probability, vertical; frequency, horizontal.

INPUT

At Trigger Source BNC

Input Frequencies: Rate DC to 1 MHz. Rise Time < 10 msec. For frequencies below 1 kHz, normally use Time Interval Histogram.

Other Specs: See Trigger Source (Common Processing Functions).

FREQUENCY RANGES

200 Hz/cm through 10 MHz /cm in 1, 2, 5 steps.

COUNTING RATE

Proportional to Frequency Range at one-half the sample rate; Approximately 50,000 counts/second, maximum, at 10 MHz/cm.

Table 1-1. Specifications (Cont'd)

Frequency Histogram (Cont'd)

PRESET TOTALIZER

Automatically switches from PROCESS to OUTPUT DISPLAY when count in channel with maximum count reaches preset total. Preset values 10^2 to 10^7 in powers of 10, and OFF.

CONTINUOUS DISPLAY

During PROCESS mode, for all ranges except 10 MHz/cm.

Time Interval Histogram

GENERAL

Same as Frequency Histogram except following:

DISPLAY

Relative probability, vertical; Time interval, horizontal

TIME INTERVAL DISPLAY

1 msec/cm through 50 sec/cm in 1, 2, 5 steps.

COUNTING RATE

Measures during alternate time intervals.

MULTICHANNEL SCALING (MCS)

DISPLAY

Counts, vertical; Time, horizontal. During the PROCESS mode the continuous display is inactive and the vertical scale is unmagnified.

INPUT

At rear panel BNC.

Input Frequencies: Pulses; rate: DC to 1 MHz; width 500 nsec, min.; pulse pair resolution 500 nsec.

Input Impedance: 3000 ohms, Min.

DWELL TIME PER CHANNEL

With FULL 1000 channel memory selected, 10 μsec thru 0.5 sec in 1, 2, 5 steps. With external time base, 50 μsec or slower, depends on Sample Input rate. Dwell time is increased X2 using HALF memory, X4 using QUARTER.

SWEEP SHAPE

Sawtooth only; External time base input allows rates of 5 msec/cm or slower.

Section I
General

Model 5480A/B

Model 5480A/B

Section I
General

Table 1-1. Specifications (Cont'd)

Table 1-1. Specifications (Cont'd)

MULTICHANNEL SCALING (MCS) (Cont'd)

TRIGGERING

External or internal. See TRIGGER SOURCE (Common Processing Functions).

PRESET SWEEP NUMBER

Sweeps to be processed are selectable in binary sequence (2^N) from single sweep (2^0) to 524, 288 sweeps (2^{19}).

TIMING INPUTS AND OUTPUTS

CONNECTORS

Rear Panel, female BNC

SAMPLE INPUT

For external time base. Enabled with SWEEP TIME set to EXT. Requires pulses, 1004/sweep, independent of memory segments in use. < 20 kHz rate, levels 0V or more negative to +2V or more positive.

SAMPLE OUTPUT

One pulse as each memory channel in use is sampled. Pulses per sweep are 1000 for FULL memory, 500 for each HALF, and 250 for each QUARTER, or combinations. Pulse from TTL logic, from > +2.5V to < +0.8V (at 5 mA maximum sinking current); Pulse width 100 nsec.

SYNC OUTPUTS

POSITIVE provides a > +10V, > 0.5 μsec pulse into > 10 k ohm load at start of PROCESS SWEEP, before PRE-ANALYSIS DELAY. NEGATIVE is same, except > -10V.

DISPLAY OUTPUTS

CONNECTORS

Rear panel, female BNC, except as noted.

SCALE CALIBRATOR

Switch positions CRT dot to "zero" or "full scale" (4 cm, vertical; 10 cm, horizontal). Equivalent voltages on all analog outputs for calibration. OFF position for normal operation.

CONTINUOUS DISPLAY

Interlace switch IN enables, OUT disables; should be disabled for use of most outputs except for remote CRT. Automatically disabled in OUTPUT RECORD mode.

VERTICAL SCOPE OUTPUT

For remote CRT; same displays and controls as internal CRT except no horizontal magnification. Proportional to CRT display, 1V/cm, -5V to +5V nominal into open circuit.

DISPLAY OUTPUTS (Cont'd)

SWEEP VOLTAGE OUTPUT

0 to 1V ramp, proportional to CRT deflection. Output impedance, 400 ohms. Output adjustable by resistor change, up to 10V.

Z AXIS

Blanking pulses for oscilloscope: From TTL logic > +2.4V blank, < +0.8V sinking 10 mA maximum at unblank. Pulse width 5 μsecs nominal.

DAC (DIGITAL-TO-ANALOG CONVERTER) OUTPUTS

Derived directly from digital memory.

HORIZONTAL DAC OUTPUT

0V to +10V nominal into 5 K ohm load, proportional to memory address (CRT sweep); 0.2% linearity.

VERTICAL DAC OUTPUT

-4V to +4V nominal into 5 K ohm load, proportional to CRT display of memory data (relative to cleared baseline) 0.5 V/cm, 0.2% of f.s. linearity.

PLOTTER OUTPUTS

For XY recording and instrument interface.

Connector:	Data and control on single connector. Mates with optional 10640B cable (see accessories available) to 7591A Plotting System.
X Axis:	In parallel with Horizontal DAC output.
Y Axis:	In parallel with Vertical Scope Output, except output impedance ohms.
Seek Output:	> 5 V into 1 K Ohm load, > 50 μsec pulse, to point plotter to seek a null.
Plot Input:	> +2V, 200 nsecs pulse, from point plotter after point is plotted. Input impedance 3K Ohms, nominal. External SWEEP TIME setting required for use.
Pen Lift Output:	Contact closure to ground during sweep only, in OUTPUT RECORD mode, lowers pen at start of sweep, lifts pen at end of sweep. 50 mA, maximum current. Parallel output provided on BNC.
Servo Enable/Disable Output:	In OUTPUT RECORD mode: > +2.4V from TTL logic during sweep only, for enable; < +0.8V at < 5 mA sinking current for disable of recorder servo.

NOISE OUTPUT

(See NOISE, under Common Display Functions.) During PROCESS mode, output at SWEEP TIME rate as a pulse for each data point; nominal peak amplitude 0.5V/cm deflection of NOISE display on CRT. Provides up to ±4V into 5 K ohm load. Output is same while CRT is displaying INPUT, DATA (with Interlace out), or NOISE. Pulses are coincident with Z axis output which may be used for control of external gating circuitry.

VARIANCE OUTPUT

(With Variance option installed in Analog plug-in.) Output conditions are same as for NOISE OUTPUT. Nominal Calibration in volts is $N^2/32$, where N = noise amplitude in cm on CRT in NOISE display for specific point. Provides 0 to +2V into 10 K ohm load.

Section I
General

Model 5480A/B

Model 5480A/B

Section I
General

Table 1-1. Specifications (Cont'd)

Table 1-1. Specifications (Cont'd)

DIGITAL OUTPUTS

DIGITAL OUTPUTS FOR COMPUTER

Two 50-pin connectors provide system logic connections for both memory data and control interface to computers. All circuitry and wiring internal to the 5480B are included. Optional 10625A Interface Kit for all HP 2114, 2115 and 2116 series computers includes plug-in boards for computer, interconnecting cable, and software. Standard computer peripherals such as a teleprinter, tape reader, high speed punch, etc., are interfaced through the computer.

Interface, through a coupler, may also be made to a teleprinter tape punch, parallel printer or other computers. Data from the 5480B is 24-bit parallel, twos-complement binary; TTL Integrated Circuit logic levels are used.

PHYSICAL

ENVIRONMENTAL

Operating Range, 0° to 55°C.

POWER

115/230V, 50-60 Hz, 175W with plug-ins

WEIGHTS

5480B: (Without plug-ins): Net 59 lb (26, 8 Kg), Gross 64 lb (29, 2 Kg)

5485A: Net 8 lb (3, 64 Kg), Gross 13 lb (5, 90 Kg)

5486B: Net 9 lb (4, 10 Kg), Gross 14 lb (6, 35 Kg)

ACCESSORIES FURNISHED

Detachable power cord, 7-1/2 feet (230 cm) long, NEMA plug. Rack mount conversion parts. One set of extenders for printed circuit boards. Spare fuse, Manual.

PLUG-INS

Requires two; 5486B and 5485A, or alternates 5487A, or 5488A.

DIMENSIONS: (5480B) (See Outline Diagram below.)

ACCESSORIES AVAILABLE

10640B Cable Assembly (for 7591A Plotting System)
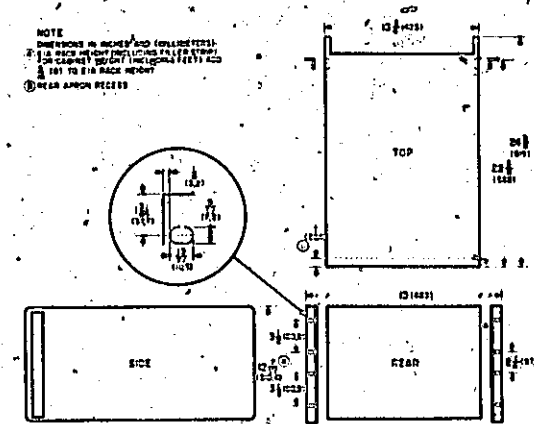
5487A FOUR-CHANNEL INPUT

GENERAL

Alternate to 5485A Two-Input Plug-In. Specifications are the same as for the 5485A, except as follows:

NUMBER OF INPUTS

Four. All may be displayed, and averaged simultaneously into different segments of memory, by means of chopping. Sampling sequence: Inputs A, B, C, D, (repeatedly).

INPUT SENSITIVITY

50 mV/cm to 20 V/cm (±3%), in 1, 2, 5 sequence.

AVERAGE BASELINE DRIFT

< +.2% of f. s. /°C for source impedance ≤ 1 K.

BANDWIDTH

(max.) 25 kHz

MEMORY SELECTION PRIORITY

Input A over B, B over C, C over D, for mutually selected segments.

OMITTED

Polarity reversing and A+B selection.

DIMENSIONS

(Storage space) 6 in. (15, 3 cm) x 7-5/16 in. (18, 6 cm) x 14 in. (35, 6 cm)

WEIGHT

Net, 8 lb (3, 64 Kg); Gross 13 lb (5, 90 Kg).

OPTION 001 (for 5487A) Variance (see Display Outputs)

5488A CORRELATOR INPUT

GENERAL

Alternate to 5485A Two-Input Plug-In. Specifications are the same as for the 5485A except as follows:

FUNCTIONS

Two Channel Input and Correlation

Table 1-1. Specifications (Cont'd)

Two-Channel Input

GENERAL

Same as 5485A except:

INPUT SENSITIVITY

50 mV/cm to 20 V/cm (±3%) in 1, 2, 5 sequence

DRIFT

$\leq \pm.2\%$ of f. s. for source impedance < 1 K ohm.

OMITTED

Polarity inversion, and signal addition (A+B)

Correlation

GENERAL

Cross-correlation between two inputs, or auto-correlation of a single signal, connected to both inputs in parallel.

INPUT CHARACTERISTICS

Same as above, except:

Drift: $\leq \pm.2\%$ of f. s. for source impedance ≤ 1 K ohm

Horizontal Resolution: (Points in correlation function) 1000 for FULL memory, 500 for HALF, 250 for QUARTER.

Delay Increments: ( ) 10 μsec to 0.5 sec in 1, 2, 5 sequence with external time base, 50 μsec and slower

Delay Offset: 20 μs to 5 sec in 1, 2, 5 sequence, or 0 μs.

Maximum Delay: (1000 increments) 500 seconds; longer with external time base.

Analog-to-Digital Converter (ADC): (For Correlation) 4-bit comparator type with parallel grey code output.

Multiplier: Hardwired digital

Vertical Calibration: Depends on settings of both input attenuators: $C = 20 \times A \times B$ (volts^2/cm).

DIMENSIONS

(Storage space) Same as 5487A.

WEIGHT

Net, 8 lb (3, 64 Kg); Gross 13 lb (5, 90 Kg).

OPTION 001 (for 5488A); Variance (see Display Outputs)

Table 1-2. Specifications (5480A)

This table lists specifications for the 5480A that are different from those for the 5480B.

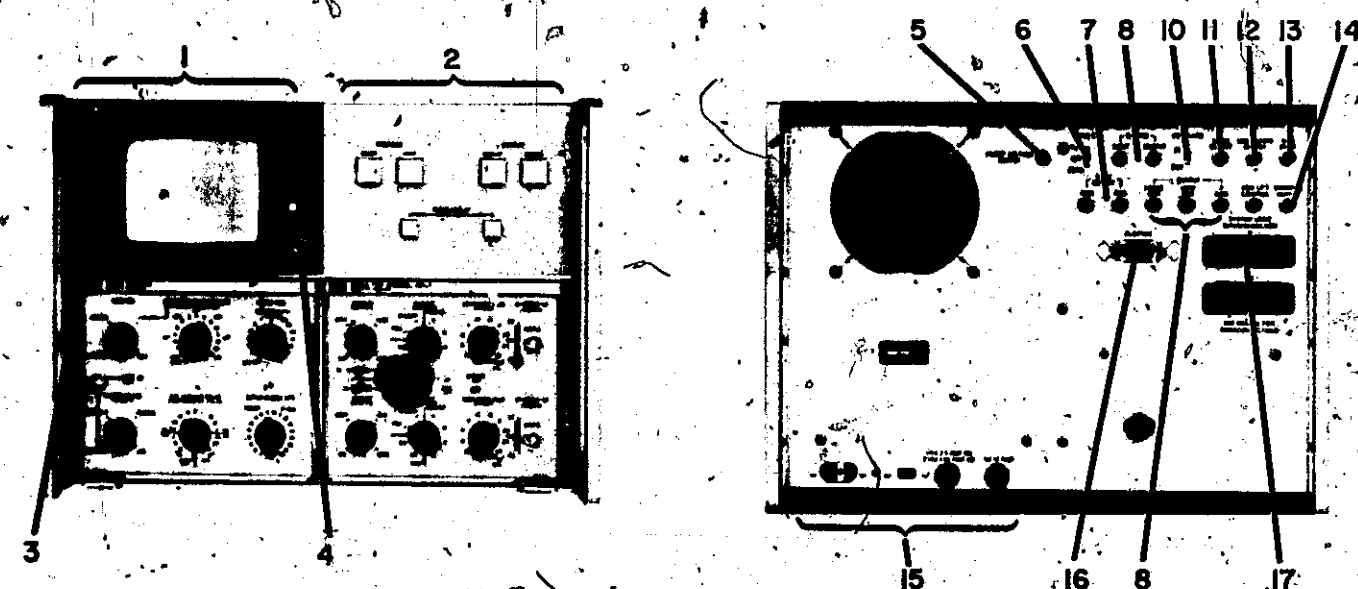## MULTICHANNEL SCALING (MCS) MODE

Sweep Modes

Sawtooth or triangular sweep. External time base input allows any desired sweep rate.

Triggering

External triggering is possible on sawtooth sweep only.

Pen Lift Control: > 2.4V = Pen up; < 0.8V = Pen down (TTL Compatible)

Figure 1-2. Memory-Display Unit (5480B)

NOTE: Except for identification engraving, 5480B front panel is identical to 5480A front-panel shown above.

DESCRIPTION; Provides memory, logic, power supply, and display output for Signal Analyzer System.

CONTROLS, CONNECTORS, INDICATORS

Front Panel

1. DISPLAY

CRT: Provides visual output from 5480B. See Figures 1-9 through 1-14 for interpretation of display.

INTENSITY: Adjusts CRT trace intensity. Prevent CRT phosphor burns by keeping intensity as low as possible while maintaining good trace visibility.

TRACE ALIGN: Rotates trace for alignment with graticule.

FOCUS: Adjusts CRT focus. Set for smallest spot size.

MAGNIFIER: Expands horizontal display scale by factor of 5 (in X5) to provide increased resolution.

Figure 1-2. Memory-Display Unit (5480B) (Cont'd)

Front Panel (Cont'd)

2. MEMORY

PROCESS and OUTPUT controls are pushbutton switches containing indicator lamps. Lighted switch button indicates which Memory activity is in progress.

PROCESS START: Enables signal processing by Memory. Processing begins at first sync pulse after pushbutton is pressed. If pressed while STOP or DISPLAY button is lighted, processing resumes at next sync pulse. Button remains lighted during signal processing.

PROCESS STOP: Stops signal processing by Memory. No CRT display.

OUTPUT DISPLAY: Causes Memory contents to be displayed on CRT. Stops signal processing if pressed while START button is lighted.

OUTPUT RECORD: When signal processing is stopped, causes 5480B to step through Memory contents once at rate selected by SWEEP TIME control. Memory contents are provided in analog form at rear panel OUTPUT AND PLOTTER connectors (items 9 and 16). Output signal can be recorded by X-Y recorder at rate which recorder pen can follow. (A Pen Lift signal is provided through the PLOTTER connector).

CLEAR DISPLAY: Two pushbutton switches must be pressed simultaneously to erase Memory contents being displayed on CRT. Use of two buttons prevents accidental erasure of Memory contents.

3. CALIBRATOR

Female banana connector. One-volt peak-to-peak squarewave. Output frequency depends on SWEEP TIME to maintain same number of cycles on CRT display regardless of SWEEP TIME setting.

4. POWER

Switch controls ac power to 5480B.

White indicator lights whenever ac power is ON.

Amber RESET lamp lights to indicate that ac power was momentarily interrupted (longer interruptions do not light the lamp). Memory contents are protected against ac interruption, but processing cannot continue until the interruption has been acknowledged by resetting the 5480B by turning ac power off for about one second, then on again.

Rear Panel

5. SWEEP VOLTAGE OUTPUT

Female BNC connector. Provides sawtooth (0V to +1V) signal synchronized with analyzer sweep. Set INTERLACE to OUT when using this connector.

6. SCALE CAL

Provides means for calibrating an X-Y recorder connected at OUTPUT or PLOTTER connectors (items 9 and 16). When analog plug-in DISPLAY control is set to DATA, SCALE CAL operates as follows:

In FULL, a dot appears at upper right corner of CRT; In ZERO, a dot appears at lower left corner of CRT; In OFF, 5480B operates normally.

Figure 1-2. Memory-Display Unit (5480B) (Cont'd)

Rear Panel (Cont'd)

7. **SYNC OUTPUTS**

   Female BNC connectors. POS provides +12V, > 0.5 μsec pulse at end of each sweep (plus post-analysis delay); NEG provides same, except -12V.

8. **SAMPLE**

   SAMPLE INPUT connector. Female BNC. Signal applied here establishes sampling rate when 5486B SWEEP TIME switch is set to EXT. Signal frequency must be less than 20 kHz and levels must vary from 0V or more negative to +2V or more positive.

   SAMPLE OUTPUT connector. Female BNC. Up to one-thousand logic level sample pulses per sweep. Output available from either internal or external sweeps. One pulse for each input sample, number depends on MEMORY SELECTOR settings. Can be 0, 250, 500, 750, or 1000.

9. **OUTPUT**

   Female BNC connectors. Provides necessary analog outputs for driving X-Y recorder, point plotter, oscilloscope, or other analog device.

   HORIZ DAC provides 0V to +10V ramp, proportional to each Memory address.

   VERT DAC provides -5V to +5V, proportional to vertical display.

   Z-AXIS provides 2V logic-level signal for oscilloscope blanking.

   PEN LIFT CONTROL provides relay closure to ground to lower recorder pen at start of sweep and opens to lift pen at end of sweep.

10. **INTERLACE**

    Controls display sweep. When set to IN, display address register sweeps memory and CRT continuously at 1 msec/cm, providing continuous flicker-free display, regardless of Logic Plug-in SWEEP TIME setting.

    When SWEEP VOLTAGE OUTPUT (item 5) is being used, set INTERLACE to OUT. This will make SWEEP VOLTAGE OUTPUT a single ramp for each triggering sync pulse.

11. **NOISE OUTPUT**

    Female BNC connector. Provides difference between sampled input signal and data stored in memory, if 1) PROCESS START button is lighted and 2) INTERLACE is set to OUT, or DISPLAY and ON/OFF switches are set to display INPUT or NOISE from one or more input channels.

12. **VERT SCOPE OUTPUT**

    Female BNC connector. Provides -5V to +5V ac signal (proportional to amplitude of displayed signal) riding on a dc offset voltage that is proportional to setting of Analog Plug-in Vertical POSITION control(s).

13. **MCS INPUT**

    Female BNC connector. Signal input for MCS (Multichannel Scaling) mode (see Figure 3-14).

14. **VARIANCE OUTPUT**

    Female BNC connector. Provides square of noise only (variance of input signal) when variance option is installed in analog plug-in (see Figure 1-11). By observing this output on an unused 5480B input channel, operator can tell if average waveform is made up of more than one output signal locked to the input sync frequency.

15. **POWER**

    Connector accepts flat-connector end of HP Part No. 8120-0078 power cord.

    115/230 slide switch allows operation from either 115V or 230V ac lines. Use a narrow-blade screwdriver to slide switch so numbers indicating operating line voltage are exposed. Be sure correct fuse is installed: For 115V, use 2.5A slow-blow, HP Part No. 2110-0015. For 230V, use 1.25A slow-blow, HP Part No. 2110-0021.

    5V fuse: For internal +5V supply, use 10A, HP Part No. 2110-0051.

16. **PLOTTER**

    14-pin female ribbon connector. Provides data and control paths between 5480B and point plotter.

17. **SYSTEM LOGIC INTERCONNECTION** for I/O Coupler or Computer.

    The I/O coupler provides for interface connections between the Model 5480B and computer, teleprinter, printer, tape reader, or tape punch.

PRE-ANALYSIS AND POST-ANALYSIS DELAYS

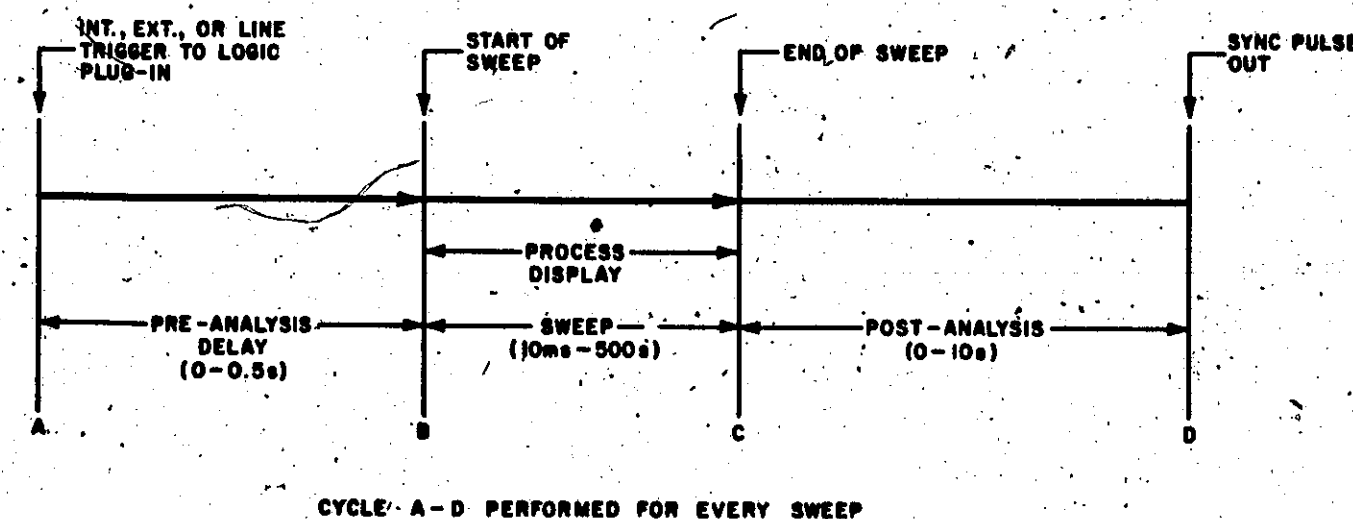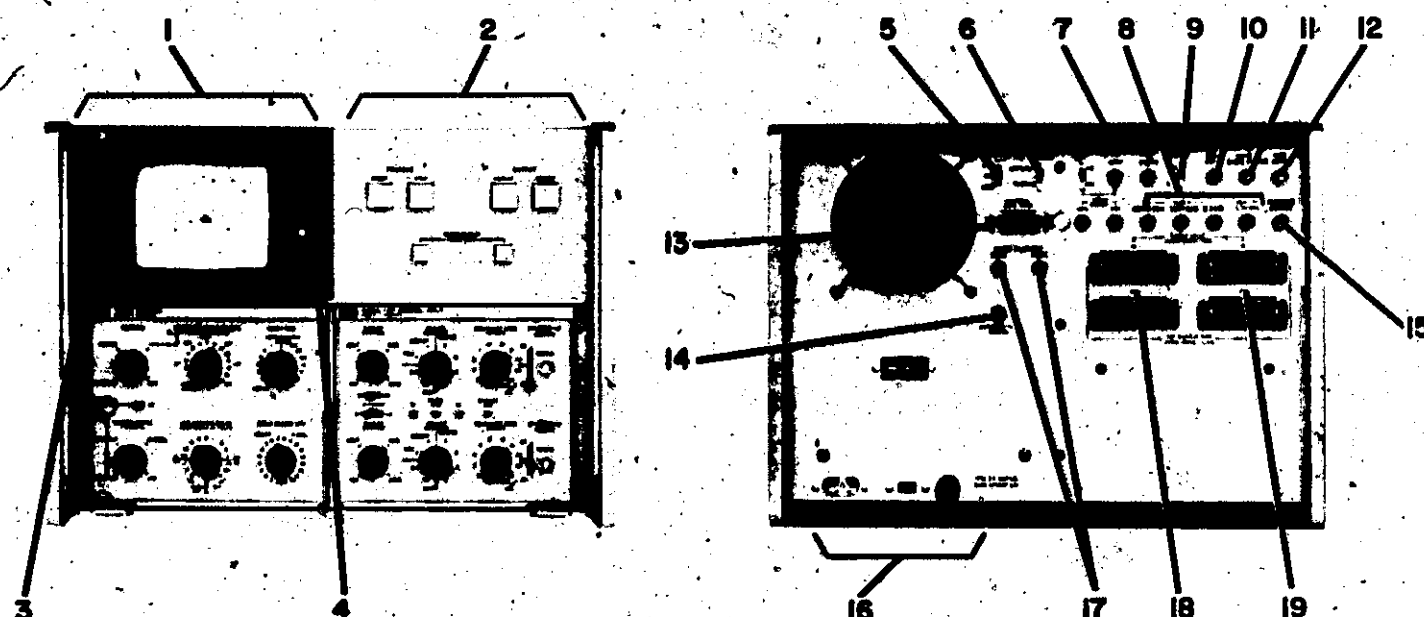

CYCLE A-D PERFORMED FOR EVERY SWEEP

Figure 1-3. Memory-Display Unit (5480A)

DESCRIPTION: Provides memory, logic, power supply and display output for Signal Analyzer System.

CONTROLS, CONNECTORS, INDICATORS

Front Panel

1. **DISPLAY**

   CRT: Provides visual output from 5480A. See Figures 1-9 through 1-14 for interpretation of display.

   INTENSITY: Adjusts CRT trace intensity. Prevent CRT phosphor burns by keeping intensity as low as possible while maintaining good trace visibility.

   TRACE ALIGN: Rotates trace for alignment with graticule.

   FOCUS: Adjusts CRT focus. Set for smallest spot size.

   MAGNIFIER: Expands horizontal display scale by factor of 5 (in X5) to provide increased resolution.

2. **MEMORY**

   PROCESS and **OUTPUT** controls are pushbutton switches containing indicator lamps. Lighted button indicates Memory activity in progress.

   PROCESS START: Enables signal processing by Memory. Processing begins at first sync pulse after pushbutton is pressed. If pressed while STOP or DISPLAY button is lighted, processing resumes at next sync pulse. Button remains lighted during signal processing.

Front Panel (Cont'd)

2. **MEMORY (Cont'd)**

   PROCESS STOP: Stops signal processing by Memory. No CRT display.

   OUTPUT DISPLAY: Causes Memory contents to be displayed on CRT. Stops signal processing if pressed while START button is lighted.

   OUTPUT RECORD: When signal processing is stopped, causes 5480A to step through Memory contents once at rate selected by SWEEP TIME control. Memory contents are provided in analog form at rear panel OUTPUT connectors (item 8). Output signal can be recorded by X-Y recorder at rate which recorder pen can follow. (A PEN LIFT signal is also provided at rear panel.)

   CLEAR DISPLAY: Two pushbutton switches must be pressed simultaneously to erase Memory contents being displayed on CRT. Use of two buttons prevents accidental erasure of Memory contents.

3. **CALIBRATOR**

   Female banana connector. One volt peak-to-peak square wave. Output frequency depends on SWEEP TIME to maintain the same number of cycles on CRT display regardless of SWEEP TIME.

4. **POWER**

   Switch controls ac power to 5480A.

   White ON lamp lights whenever ac power is applied.

   Red RESET lamp lights to indicate that ac power has been interrupted. Memory contents are protected against interruption, but processing cannot continue until the interruption has been acknowledged by resetting the 5480A by turning ac power off for about one second, then on again.

Rear Panel

Panel illustrated is for 5480A's having serial prefix 852 or 836. Rear panel of 5480A's having serial prefix 928 and above is same as 5480B, shown in Figure 1-2.

5. **SCALE CAL**

   Provides means for calibrating and X-Y recorder connected at OUTPUT connectors (item 8). When analog plug-in DISPLAY control is set to DATA, SCALE CAL operates as follows:

   In FULL, a dot appears at upper right corner of CRT.
   In ZERO, a dot appears at lower left corner of CRT.
   In OFF, 5480A operates normally.

6. **HORIZ SWEEP WAVEFORM**

   Useable in MCS only. Allows operator to select SAWTOOTH or TRIANGLE sweep voltage. In SAWTOOTH, Memory is stepped from 0 (left) to 1000 (right) linearly, jumps back to 0 (left) and steps linearly to 1000 (right) again. In triangle, Memory is stepped from 0 (left) to 1000 (right) linearly, then from 1000 (right) to 0 (left) linearly, then 0 (left) to 1000 (right), etc.

   SAWTOOTH gives continual left-to-right sweeps while TRIANGLE gives alternate left-to-right and right-to-left sweeps (steps through Memory).

Rear Panel (Cont'd)

7. **SAMPLE**

    INT/EXT Switch: In INT, 5480A uses internal 100 kHz time base to establish sampling rate. In EXT, 5480A uses signal applied at SAMPLE INPUT connector to establish sampling rate.

    SAMPLE OUTPUT connector. Female BNC. One-thousand logic level sample pulses per sweep. Output available from either internal or external sweeps.

    SAMPLE INPUT connector. Female BNC. Signal applied here establishes sampling rate when INT/EXT switch set to EXT. Signal frequency must be less than 20 kHz and levels must vary from 0V or more negative to +2V or more positive.

8. **OUTPUT**

    Female BNC connectors. Provides necessary analog outputs for driving X-Y recorder, point plotter, oscilloscope, or other analog device.

    HORIZ DAC provides 0V to +10V ramp, proportional to each Memory address.

    VERT DAC provides -5V to +5V, proportional to vertical display.

    Z AXIS provides 2V logic-level signal for oscilloscope blanking.

    PEN LIST CONTROL provides logic level signal to drop X-Y recorder pen at start of sweep and lift pen at end of sweep.

9. **INTERLACE DISPLAY**

    Controls display sweep. When set to IN, display address register sweeps memory and CRT continuously at 1 msec/cm, providing continuous flicker-free display, regardless of Logic Plug-in SWEEP TIME setting. When SWEEP VOLTAGE OUTPUT (item 14) if being used, set INTERLACE DISPLAY to OUT. This will make SWEEP VOLTAGE OUTPUT a single ramp for each triggering sync pulse.

10. **NOISE OUTPUT**

    Female BNC connector. Provides noise being removed from data signal. Voltage level is correct for that point during time CRT is unblanked (Z AXIS OUTPUT pulse).

11. **VERT SCOPE OUTPUT**

    Female BNC connector. Provides -5V to +5V ac signal (proportional to amplitude of displayed signal) riding on a dc offset voltage that is proportional to setting of Analog Plug-in Vertical POSITION control.

12. **MCS INPUT**

    Female BNC Connector. Signal input for MCS (multichannel scaling) mode (see Figure 1-14).

13. **EXTERNAL DATA INPUT**

    24-pin connector. Inputs parallel data inputs from Analog Plug-in connector.

14. **SWEEP VOLTAGE OUTPUT**

    Female BNC connector. Provides sawtooth (0V to +1V) signal synchronized with analyzed sweep. Set INTERLACE DISPLAY to OUT when using this connector.

Rear Panel (Cont'd)

15. **VARIANCE OUTPUT**

    Female BNC connector. Provides square of noise only (variance of input signal) when variance option is installed. By observing this output on an unused 5480A input channel, operator can tell if average waveform is made up of more than one input signal locked to the input sync frequency.

16. **POWER**

    Connector accepts flat-connector end of HP Part No. 8120-0078 power cord.

    115/230 slide switch allows operation from either 115V or 230V ac lines. Use a narrow-blade screwdriver to slide switch so numbers indicating operating line voltage are exposed. Be sure correct fuse is installed: For 115V; use 2.5A slow-blow, HP Part No. 2110-0015. For 230V, use 1.25A slow-blow, HP Part No. 2110-0021.

17. **POINT PLOTTER**

    Female BNC connectors. SEEK supplies signal telling point plotter to plot a point.

    PLOT receives signal from plotter, indicating point has been plotted.

    Point plotter X and Y inputs come from HORIZ DAC and VERT DAC OUTPUTs, respectively. Logic Plug-in SWEEP TIME switch (see Figure 3-5) must be set to EXT.

18. **SYSTEM LOGIC INTERCONNECTION**

    For I/O (Input/Output) Coupler. The I/O coupler provides for interface connections between the Model 5480A and computer, teletype, printer, tape reader, or tape punch.

Figure 1-4. Control Plug-In (5486A/B)
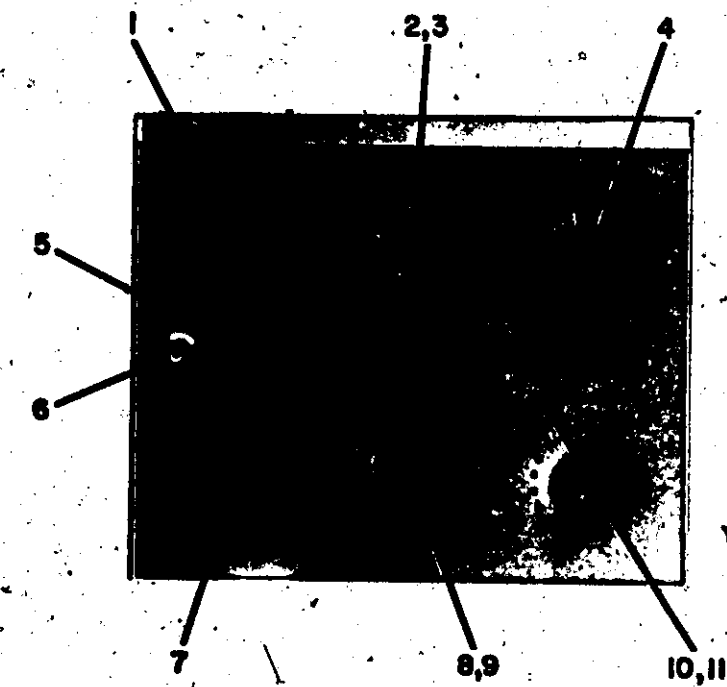
NOTE: Except for identification engraving, 5486B front panel is identical to 5486A front panel shown above.

DESCRIPTION: Selects FUNCTION of Signal Analyzer System. Controls system rate, pre-and post-analysis delays, and amount of processing for each analysis.

N	2^N
0	1
1	2
2	4
3	8
4	16
5	32
6	64
7	128
8	256
9	512
10	1024
11	2048
12	4096
13	8192
14	16384
15	32768
16	65536
17	131072
18	262144
19	524288

CONTROLS, CONNECTORS

1. **FUNCTION**

   SUMMATION: Causes successive repetitions of input signal to be added, keeping a "running total". Vertical display is not calibrated (see Figure 1-12).

   AVERAGE: Keeps "calibrated average" os successive input signal repetitions. Provides continuous calibrated display (see Figure 1-9).

   HISTOGRAM: Creates plot of number-of-occurrances (probability) vertical axis versus frequency or period on horizontal axis. Input signal is applied at input connector (item 6).

   MCS: Creates plot of frequency on vertical axis versus time on horizontal axis. Input signal is applied through 5480A/B rear-panel MCS INPUT connector.

2. **SENSITIVITY MULTIPLIER (2^N)**

   Multiplies vertical display size by 2^N. Dial is calibrated N, thus as control is turned clockwise each position dobules display size. In AUTO, vertical display is automatically calibrated in terms of V/cm on the input attenuator setting when signal was being processed in AVERAGE and SUMMATION.

3. **PRESET TOTALIZER**

   When FUNCTION is set to HISTOGRAM, PRESET TOTALIZER specifies number of values to be counted in memory location having greatest number of counts; highest peak in histogram display will have number of counts indicated by PRESET TOTALIZER, other portions of display will be proportional. When PRESET TOTALIZER is set to OFF, histogramming continues until manually stopped by pressing OUTPUT DISPLAY or PROCESS STOP pushbutton.

4. **SWEEP TIME**

   Determines rate at which address register steps through Memory, (processing rate). In EXT, input signal connected through 5480A/B rear-panel SAMPLE INPUT connector is converted to a train of sampling pulses; nearly any periodic waveform may be used for this application.

   In Frequency HISTOGRAM mode, SWEEP TIME control calibrates Horizontal axis (Figure 1-13).

5. **AC/DC**

   Selects ac coupling or dc coupling of input sync signals in SUMMATION and AVERAGE mode.

6. **INPUT CONNECTOR**

   Female BNC. Sync input for SUMMATION and AVERAGE mode; signal input for HISTOGRAM mode. See Figure 1-9 through Figure 1-13.

7. **TRIGGER SOURCE/LEVEL**

   Operates in SUMMATION, AVERAGE, and HISTOGRAM modes only.

   TRIGGER SOURCE selects triggering signal from: INTERNAL, uses sync signal generated at end of each sweep (5480A/B is free-running). LINE, uses signal derived from power line frequency. EXTERNAL + or -, uses signal at BNC connector (item 6); derives sync pulse from positive or negative signal slope.

CONTROLS, CONNECTORS (Cont'd)

8. **PRE-ANALYSIS DELAY**

   Provides calibrated 20 μsec to 0.5 sec delay between input sync signal and start of sweep. Conserves memory and increases resolution by eliminating unnecessary front portion of data waveform display.

9. **POST-ANALYSIS DELAY**

   Provides uncalibrated zero to 10 second delay of output sync pulse occurring at end of every sweep. If this sync output is used to start each repetition of an experiment, start of experiment will be delayed by this amount.

10. **SWEEP NUMBER (2^N)**

    Selects number of sweeps or repetitions to be averaged. Dial is calibrated in N, number of sweeps is 2^N. (For example, SWEEP NUMBER 3 causes 8 sweeps to be averaged.) Signal-to-noise ratio improvement in dB is approximately 3N. For example, where N is 3, dB improvement is 9 dB.

11. **PRESET/NORMAL**

    PRESET (AVERAGE): 5480A/B averages number of sweeps selected by SWEEP NUMBER, stops and DISPLAYs result (see Figure 1-9). This is stable averaging. *

    NORMAL (AVERAGE): 5480A/B averages number of sweeps selected by SWEEP NUMBER, and continues averaging with this number of sweeps defining the weighting time constant until stopped manually. This is weighted averaging. (See Figure 1-9.)

    PRESET (SUMMATION): 5480A/B adds number of sweeps selected by SWEEP NUMBER control, stops and DISPLAYS result (see Figure 1-12).

    NORMAL (SUMMATION): 5480A/B adds inputs until manually stopped (see Figure 1-12). The AUTO scaling feature of SENSITIVITY MULTIPLIER ceases operation in NORMAL mode during SUMMATION after SWEEP NUMBER has been reached.

    PRESET (MCS): 5480A/B performs Multichannel Scale function for number of sweeps selected by SWEEP NUMBER, stops and DISPLAYs result (see Figure 1-14).

    NORMAL (MCS): 5480A/B performs Multichannel Scale function until stopped by pressing OUTPUT DISPLAY or PROCESS STOP pushbutton.

---

* NOTE: In PRESET (AVERAGE), PRESET (SUMMATION), or PRESET (MCS); and SWEEP NUMBER is 19, 5480A/B will stop and DISPLAY result after 2^{18} sweeps.

Figure 1-5. Two Channel Input Plug-In (5485A)
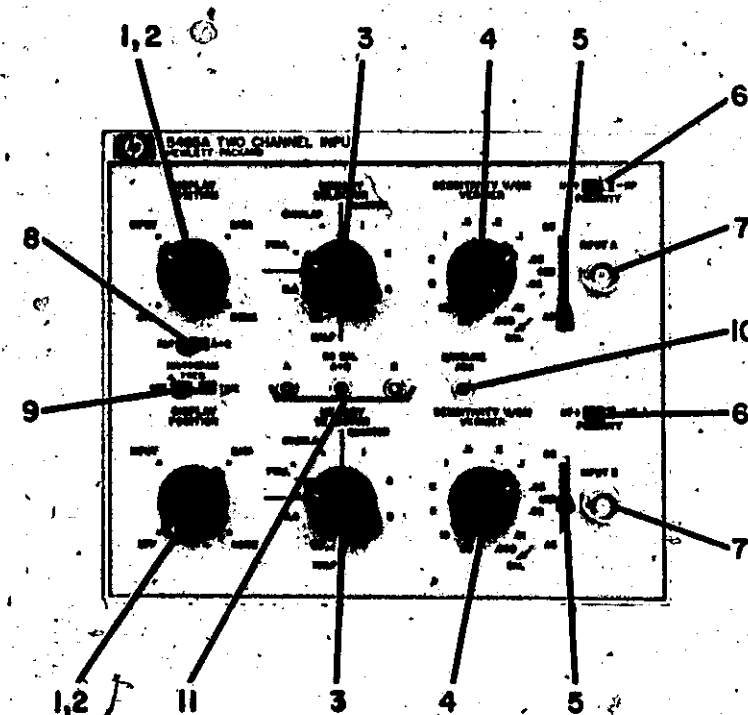
DESCRIPTION: Accepts one or two channels of input data to be presented as vertical portion of Signal Analyzer display.

CONTROLS, CONNECTORS

1. **DISPLAY**

Selects CRT display for its channel. INPUT displays raw, unprocessed input signal. DATA displays averaged signal, MCS, or histogram. NOISE displays difference between INPUT and averaged signal. OFF turns channel off (see item 3).

2. **POSITION**

Adjusts vertical position of its channel on CRT display.

3. **MEMORY SELECTOR**

Selects portion of Memory to be used for storage, display, or processing. Memory is divided into four quarters. Operator may select memory portions for either channel as follows:

    QUARTER 1
    QUARTER 2
    QUARTER 3
    QUARTER 4
    HALF (QUARTERS 1 and 2)
    HALF (QUARTERS 3 and 4)
    FULL (QUARTERS 1, 2, 3, and 4)

In OVERLAP all four quarter sections are displayed simultaneously as separate traces. When MEMORY SELECTOR controls are set to cause both channels to be connected to same Memory quarter, A rules. For example, if MEMORY SELECTOR A is set to FULL, no quarter

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Figure 1-5. Two Channel Input Plug-In (5485A) (Cont'd)

CONTROLS, CONNECTORS (Cont'd)

3. **MEMORY SELECTOR** (Cont'd)

is left for B; or, if MEMORY SELECTOR A is set to QUARTER 1, and MEMORY SELECTOR B set to FULL, Channel A gets QUARTER 1, and Channel B gets QUARTERS 2, 3, and 4.

4. **SENSITIVITY/VERNIER**

SENSITIVITY: Attenuates input signal and provides calibration of vertical display axis from 5 mV/cm to 20 V/cm.

VERNIER: Provides continuous, uncalibrated attenuation of vertical display axis. Does not affect input signal amplitude stored in memory.

5. **DC/GND/AC**

Switches from dc coupling, grounding, or ac coupling of input. In GND, input connector is open-circuited, and input to amplifier is grounded to provide a zero reference.

6. **UP+/-UP**

Inverts input signal, allowing waveform to be displayed with positive voltage above baseline (UP+) or below baseline (-UP).

7. **INPUT**

Female BNC connector. Input for AVERAGE or SUMMATION functions (see Figures 1-9, 1-11, and 1-12).

8. **ALT/A+B**

In ALT, two channels operate separately. In A + B, signal at INPUT B is added to signal at INPUT A and sum is fed through Channel A only. Difference between two signals can be averaged by setting one channel to UP+ and other to -UP.

9. **HISTOGRAM FREQ/TIME**

If Logic Plug-in FUNCTION is set to HISTOGRAM, FREQ displays a frequency histogram (use above 1 kHz), or TIME displays a period (time) histogram (use below 1 kHz). See Figure 1-18.

10. **BASELINE ADJ**

Adjusts baseline to prevent baseline drift during summation.

11. **DC BAL CONTROLS**

Compensate for small dc offsets in the input circuits so dc input will be stored as "0" in the memory.

Section I
General

Model 5480A/B

Model 5480A/B

Section I
General

Figure 1-6. Four Channel Input Plug-In (5487A)
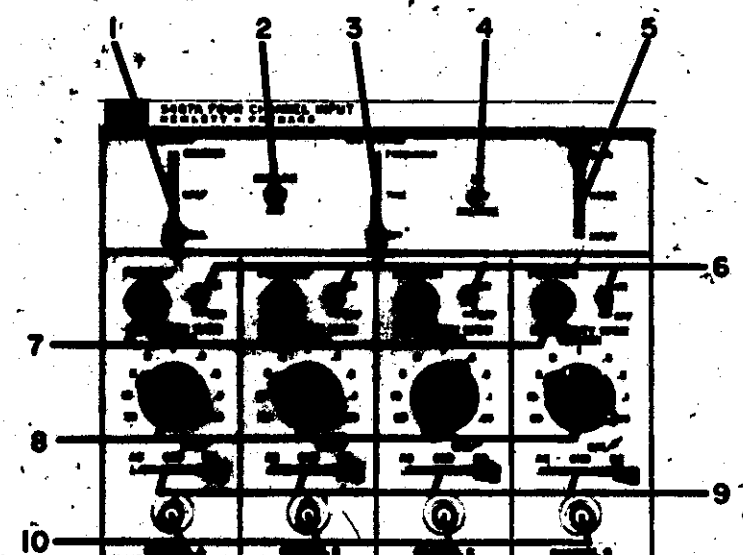
DESCRIPTION: Accepts one to four channels of input data to be presented as vertical portion of Signal Analyzer display.

CONTROLS, CONNECTORS

1. MEMORY

Selects portion of memory to be used for storage, display, or processing by any one channel. The chart below lists memory section-vs-5487A channel allocations.

2. BASELINE ADJ

Adjusts baseline to prevent baseline drift during summation.

3. HISTOGRAM/FREQ/TIME

If Logic Plug-in FUNCTION is set to HISTOGRAM, FREQ displays a frequency histogram (use above 1 kHz), or TIME DISPLAYS A PERIOD (time) histogram (use below 1 kHz). See Figure 1-13.

4. DC BALANCE

Compensates for small dc offsets in the input circuits so dc input will be stored as "0" in the memory.

5. DISPLAY

Selects CRT display for all channels turned ON (see table referenced by MEMORY switch description). INPUT displays sampled input signals. DATA displays averaged signal, MCS, or histogram, NOISE displays difference between INPUT and averaged signal (DATA).

CONTROLS, CONNECTORS (Cont'd)

6. ON/OFF SWITCHES

These switches, plus the MEMORY switch and some internal logic determine which channels will be processed or displayed. The table referenced by the MEMORY switch description provides details. At least one channel must be ON or there will be no processing or display.

7. POSITION

Each control adjusts vertical position of its trace (if channel is being displayed) on CRT screen.

8. SENSITIVITY/VERNIER

SENSITIVITY: Attenuates input signal and provides calibration of vertical display axis from 50 mV/cm to 20 V/cm.

VERNIER: Provides continuous, uncalibrated attenuation of vertical display axis. Does not affect input signal amplitude stored in memory.

9. AC/GND/DC

Switches from ac coupling, grounding, or dc coupling of input. In GND, input connector is open-circuited, and input to amplifier is grounded to provide a zero reference

10. INPUT

Female BNC connector. Input for AVERAGE or SUMMATION functions (see Figures 1-9, 1-11, and 1-12).

Section I
General

Model 5480A/B

Model 5480A/B

Section I
General

Figure 1-7. Correlator Input Plug-In (5488A/H01-5485A)
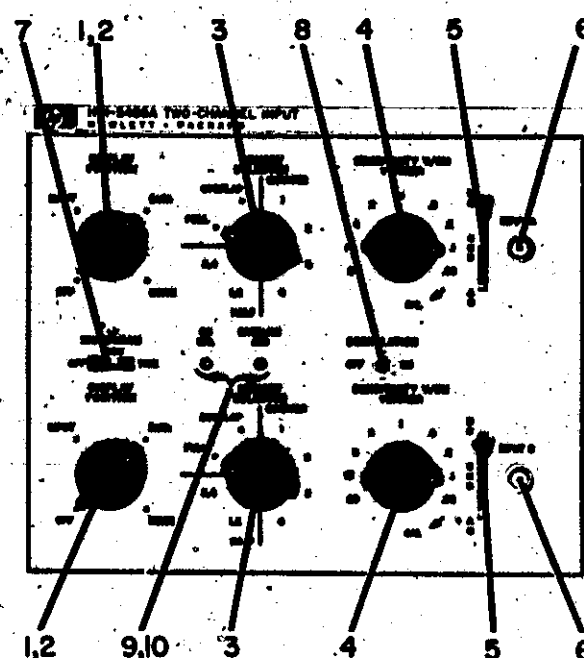
NOTE: Except for minor differences in front-panel engraving and parts numbers, the 5488A and H01-5485A are identical. In general, text will use 5488A terminology and numbers; differences between these and those for the H01-5485A will be described where necessary.

DESCRIPTION: Accepts one or two channels of input data to be presented as vertical portion of Signal Analyzer display. Can perform cross- or auto-correlation function on input signals, in addition to other 5480A/B functions; refer to Figure 1-10 for operating instructions for correlation.

CONTROLS, CONNECTORS

1. DISPLAY

When MODE (see item 8) is set to AVG, selects CRT display for its channel. INPUT displays raw, unprocessed input signal. DATA displays averaged signal, MCS, or histogram, NOISE displays difference between INPUT and averaged signal. OFF turns channel off (see item 3).

When MODE is set to CORR, Channel A DISPLAY switch should be set to DATA; displays for other switch positions are meaningless. Channel B DISPLAY switch may be set to any position, because Channel B will not be displayed.

2. POSITION

Adjusts vertical position of its channel when channel is being displayed on CRT.

3. MEMORY SELECTOR

Selects portion of memory to be used for storage, display, or processing. Memory is divided into four quarters. Operator may select memory portions for either channel as follows:

Figure 1-7. Correlator Input Plug-In (5488A/H01-5485A) (Cont'd)

CONTROLS, CONNECTORS (Cont'd)

3. MEMORY SELECTOR (Cont'd)

QUARTER 1
QUARTER 2
QUARTER 3
QUARTER 4
HALF (QUARTERS 1 and 2)
HALF (QUARTERS 3 and 4)
FULL (QUARTERS 1, 2, 3, and 4)

In overlap, all four quarter sections are displayed simultaneously as separate traces.

When MODE switch is set to AVG and MEMORY SELECTOR controls are set to cause both channels to be connected to the same memory quarter, Channel A has precedence. For example, if MEMORY SELECTOR A is set to FULL, no quarter is left for B; or if MEMORY SELECTOR A is set to QUARTER 1, and MEMORY SELECTOR B is set to FULL, Channel A gets QUARTER 1, and Channel B gets QUARTERs 2, 3, and 4.

When MODE switch is set to CORR, both MEMORY SELECTOR controls must be set to the same position.

4. SENSITIVITY/VERNIER

SENSITIVITY: Attenuates input signal and provides calibration of vertical display axis from 50 mV/cm to 20 V/cm.

VERNIER: Provides continuous, uncalibrated attenuation of vertical display axis. Does not affect input signal stored in memory.

5. DC/GND/AC

Switches from dc coupling, grounding, or ac coupling of input. In GND, Input connector is open-circuited, and input to amplifier is grounded to provide a zero reference.

6. INPUT

Female BNC connector. Input for AVERAGE, SUMMATION, or CORRelation functions (see Figures 1-9 through 1-12).

7. HISTOGRAM FREQ/TIME

If Logic Plug-in FUNCTION is set to HISTOGRAM, FREQ displays a frequency histogram (use about 1 kHz), or TIME displays a period (time) histogram (use below 1 kHz). See Figure 1-13.

8. MODE AVG/CORR (CORRELATION ON/OFF)

When set to AVG (OFF), 5480A system can perform all normal functions (AVERAGE, SUMMATION, HISTOGRAM, MCS). When set to CORR (ON), and Logic Plug-In FUNCTION switch is set to AVERAGE, the 5480A system performs a correlation function on the signals at A and B INPUTS (see Figure 1-10).

9. DC BAL

Compensates for small dc offsets in the input circuits so dc input will be stored as "0" in the memory.

10. BASELINE ADJ: Adjusts baseline to prevent baseline drift during summation.

Section I
General

Model 5480A/B

Model 5480A/B

Section I
General

Figure 1-8. Setup and Turn-on

5487A

5488A
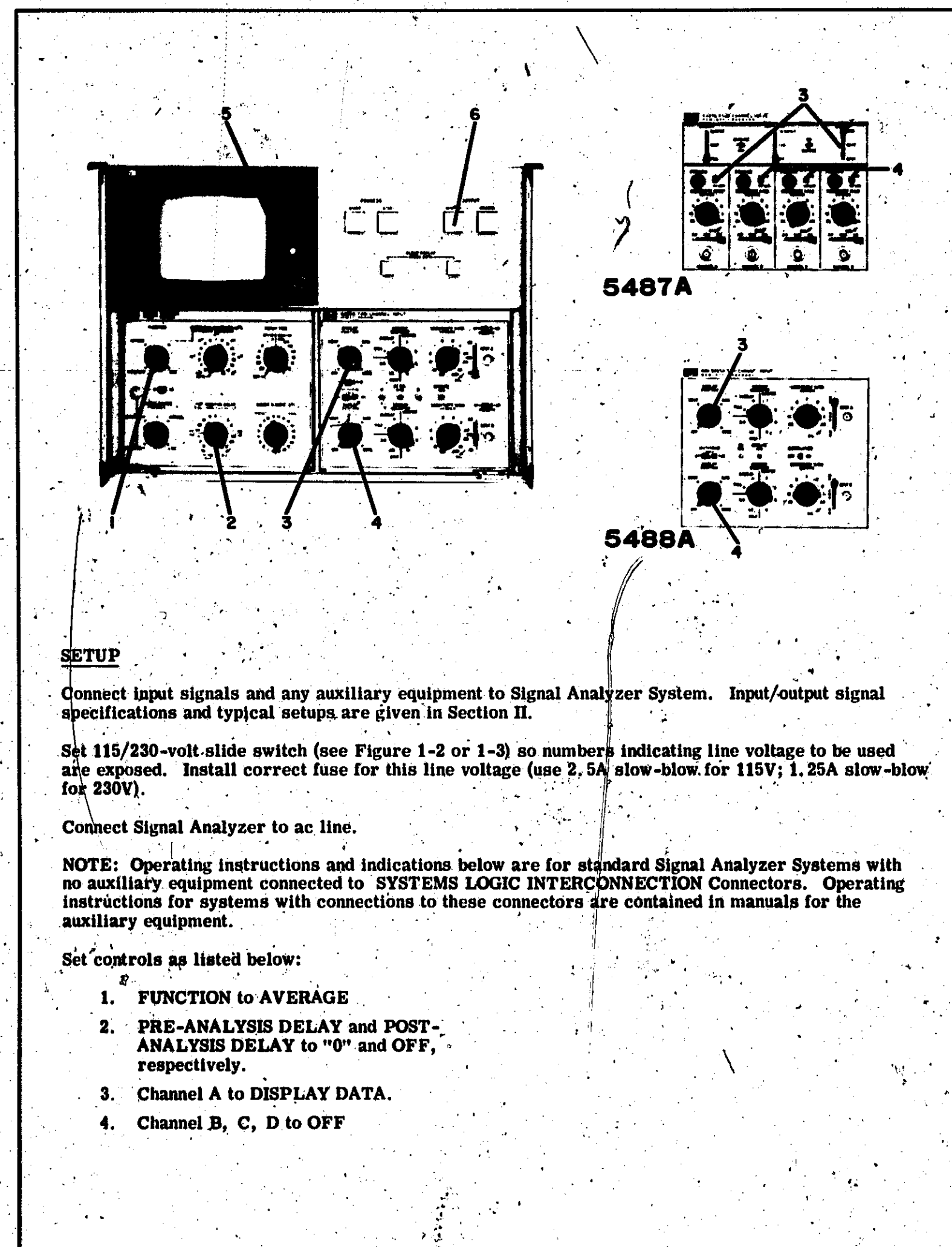
SETUP

Connect input signals and any auxiliary equipment to Signal Analyzer System. Input/output signal specifications and typical setups are given in Section II.

Set 115/230-volt slide switch (see Figure 1-2 or 1-3) so numbers indicating line voltage to be used are exposed. Install correct fuse for this line voltage (use 2.5A slow-blow for 115V; 1.25A slow-blow for 230V).

Connect Signal Analyzer to ac line.

NOTE: Operating instructions and indications below are for standard Signal Analyzer Systems with no auxiliary equipment connected to SYSTEMS LOGIC INTERCONNECTION Connectors. Operating instructions for systems with connections to these connectors are contained in manuals for the auxiliary equipment.

Set controls as listed below:

1. FUNCTION to AVERAGE

2. PRE-ANALYSIS DELAY and POST-ANALYSIS DELAY to "0" and OFF, respectively.

3. Channel A to DISPLAY DATA.

4. Channel B, C, D to OFF

Figure 1-8. Setup and Turn-on (Cont'd)

TURN-ON

5. Turn on ac power. POWER switch (item 5) is near lower right corner of CRT. At turn-on, the white ON and PROCESS STOP switch lamps will light. There will be no CRT display.

6. Press OUTPUT DISPLAY pushbutton. Button will light, and display appear on CRT. Observe CRT display for each of the following Channel A DISPLAY switch positions: FULL; HALF 3, 4; HALF 1, 2; QUARTER 1; QUARTER 2; QUARTER 3; QUARTER 4. Display for each setting represents what was in that portion of memory when Signal Analyzer was turned off. If memory section being displayed is not important, erase it by pressing both CLEAR DISPLAY pushbuttons simultaneously; any quarter or half of the memory being displayed can be erased independently of other memory sections not being displayed.

SELECT OPERATING MODE

Operating mode is selected by FUNCTION switch (item 1). AVERAGE/CORRELATION use AVERAGE (see Figures 1-9 through 1-11), SUMMATION (see Figure 1-12), HISTOGRAM (see Figure 1-13), MULTICHANNEL SCALING (MCS) (see Figure 1-14).

Figure 1-9. Average
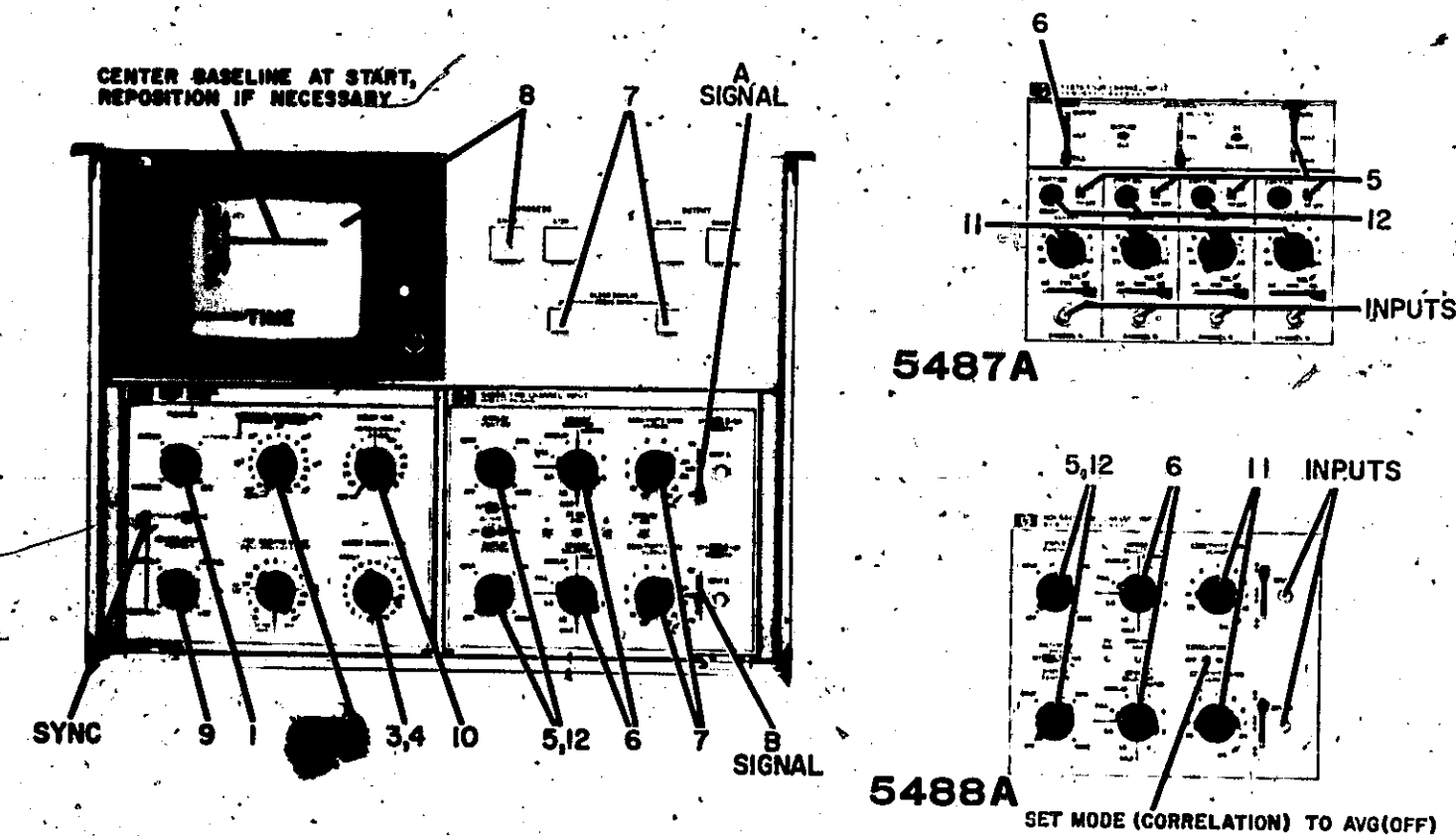
DESCRIPTION: Analyzer performs either weighted or calibrated average. Display resembles oscilloscope presentation of input signal, with noise averaged out. Up to 60 dB of signal-to-noise ratio improvement can be obtained. Vertical display is calibrated in V/cm, noise signal appears at full value and noise is gradually removed, leaving only signal.

Perform SETUP and TURN-ON procedure described in Figure 1-8.

CONTROL SETTINGS

1. FUNCTION to AVERAGE.

2. SENSITIVITY MULTIPLIER fully CCW.

3. SWEEP NUMBER to about 6.

4. PRESET/NORMAL to NORMAL.

5. Each channel to DISPLAY DATA (or OFF, if channel is not used).

6. MEMORY SELECTOR. As desired.

    a. Larger memory sections provide more signal resolution.
    b. A processed signal can be stored in one memory section while processing is done in another section.
    c. (5485A or 5488A) If MEMORY SELECTOR controls are set to feed A and B to same quarter section of memory, A overcomes B. 5487A, refer to Figure 1-6.
    d. Signal processing occurs only 1) in memory sections selected by MEMORY SELECTOR and ON/OFF switches, 2) when PROCESS START button is lighted, and 3) when sync signal is received by Logic plug-in.

Figure 1-9. Average (Cont'd)

Figure 1-9. Average (Cont'd)

CONTROL SETTINGS (Cont'd)

7. Clear display by simultaneously pressing both CLEAR DISPLAY buttons. OUTPUT DISPLAY button will light, and CRT will display a horizontal line.

8. Press PROCESS START pushbutton. Button will light, and OUTPUT DISPLAY light will go out. Display will show signal processing.

9. If necessary, select TRIGGER SOURCE and adjust LEVEL to initiate processing. Ordinarily, best triggering is obtained using an external triggering source locked in frequency to the signal to be observed. See Correlation, Figure 1-10.

10. Adjust SWEEP TIME for best display of input signal. Press both CLEAR DISPLAY buttons and PROCESS START button each time SWEEP TIME is changed.

11. Adjust VERTICAL SENSITIVITY for each displayed input channel to provide best CRT display of input signal.

12. Use POSITION controls to separate or center signals vertically.

The above procedure provides a means for obtaining a rough measurement of signal amplitude and frequency, and positioning display. Information below enables refinement of measurement.

SIGNAL-TO-NOISE RATIO IMPROVEMENT VS RESPONSE TO INPUT SIGNAL CHANGES

Signal-to-noise ratio improvement can be determined approximately by SWEEP NUMBER setting, and can be expressed in dB as 3N, where N is SWEEP NUMBER. For example, where N is 10, signal-to-noise ratio improvement is 30 dB (after 2^N, or 1024 sweeps).

Higher SWEEP NUMBER settings result in greater signal-to-noise ratio improvement.

Lower SWEEP NUMBER settings enable faster response to input signal changes or changes in Signal Analyzer control settings when PRESET/NORMAL switch is on NORMAL.

NOTE: To prevent invalid readings, the following SWEEP NUMBER and SWEEP TIME combinations should not be used:

SWEEP TIME	SWEEP NUMBER
1 msec/cm	19 (PRESET) see NOTE under NORMAL VS PRESET
2 msec/cm	19, 18
5 msec/cm or slower	19, 18, 17, 16

If the above limits are not observed when using a 5486A, data will overflow available memory bits and will be lost. The 5486B automatically limits the SWEEP NUMBER used in averaging to 19 in 1 msec/cm, 17 for 2 msec/cm, and 15 for 5 msec/cm and slower sweep times. The 5485A modification described below will have no effect in systems containing the 5486B logic plug-in.

This SWEEP NUMBER limitation (except 1 msec/cm) is directly related to the 5-, 7-, or 9-bit resolution of the Analog-to-Digital Converter (depending on SWEEP TIME). If input signals are so noisy that 2^{15} sweeps are not sufficient (2^{17} sweeps at 2 msec/cm), the modification below may be made to the 5485A Dual-Channel Input plug-in; the modification restricts resolution to 5 bits for all SWEEP TIMEs.

Modification to 5485A for decreased resolution:

1. Remove connecting wires from SZ3(2) and XA3(3).
2. Tape ends of wires removed in step 1.
3. Ground pins XA3(2) and XA3(3) by connecting them to XA3(17).

NORMAL VS PRESET

NORMAL

Signal Analyzer averages all sweeps until it completes 2^N sweeps. After this number of sweeps is completed, averaging process continues, except that previous information is weighted exponentially with a time constant equal to the time required to take 2^N sweeps. Averaging continues until PROCESS STOP (no CRT display) or OUTPUT DISPLAY pushbutton is pressed.

PRESET

Signal Analyzer averages all sweeps until it completes 2^N sweeps.* After this number of sweeps is completed, averaging process stops and Analyzer automatically switches to OUTPUT DISPLAY. Note that the value of 2^N increases very rapidly (see table in Figure 1-4), and large values of 2^N can result in very long experiment times. As with NORMAL, averaging process can be stopped at any time by pressing PROCESS STOP or OUTPUT DISPLAY button.

CRT DISPLAY

Signal is displayed full-scale and noise is reduced with each sweep.

Vertical calibration for displayed channel is determined by SENSITIVITY V/CM control.

Horizontal calibration is determined by SWEEP TIME and X1/X5 switch.

In OUTPUT DISPLAY, vertical display can be expanded by changing setting of SENSITIVITY MULTIPLIER.

VERNIER of displayed channel provides for uncalibrated reduction in display size (does not affect input SENSITIVITY).

For PRE-ANALYSIS DELAY and POST-ANALYSIS DELAY information, see Figure 1-2.

*NOTE: Except for N = 19. In PRESET, N = 19 and N = 18 are same, 5480A sweeps for 2^{18} sweeps, then DISPLAYs result.

See also correlation, Figure 1-10, and Variance, Figure 1-11.

Figure 1-10. Correlation
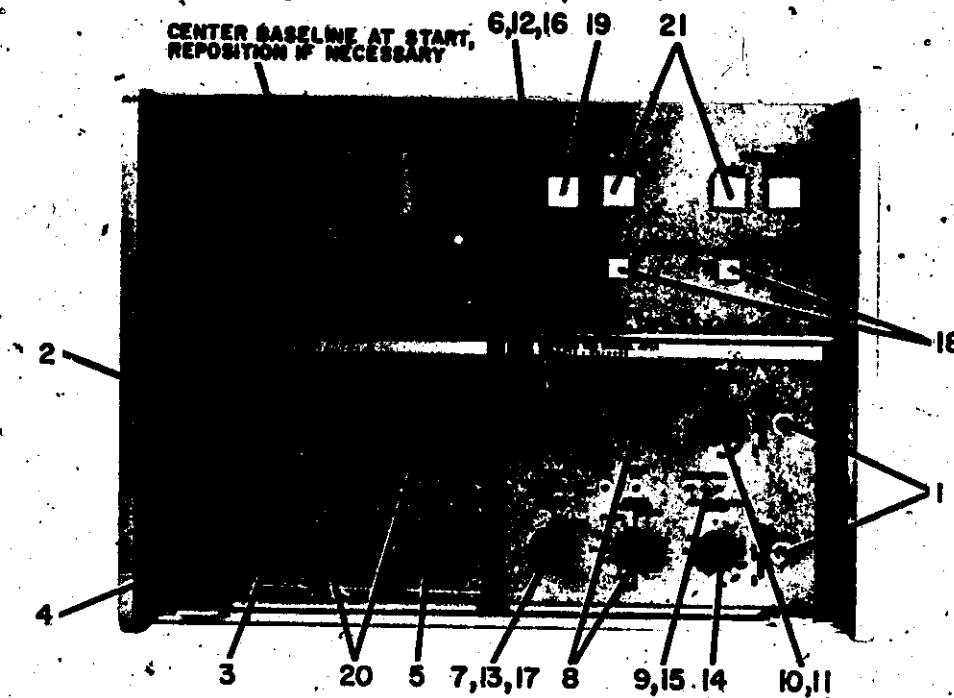
DESCRIPTION: Correlation is a measure of the similarity between two waveforms. The 5488A plug-in allows the 5480A/B system to perform cross- or auto-correlation of signals connected to its A and B INPUTs, when the MODE (CORRELATION) switch is set to CORR (ON). When the plug-in MODE (CORRELATION) switch is set to AVG (OFF), the 5488A allows the 5480A system to function normally, making the measurements described in Figures 1-9, and 1-11 through 1-14.

Correlation is computed by multiplying the two input waveforms ordinate-by-ordinate and finding the average product. Finding the correlation function of two different signals is cross-correlation; finding the correlation function of a signal with itself is auto-correlation. Auto-correlation can be used to determine if there is any periodicity in an unknown input signal; the period of the auto-correlation function is the same as the period of the original input signal. Cross-correlation is used to show how closely two "independent" signals are related.

Some uses of cross- or auto-correlation are:

System Identification: determination of the laws which relate the outputs of a system to its inputs.

Noise Source Direction Finding.

On-site measurement of Acoustic Absorption Coefficients: when designing special acoustic environments

Cross-correlation of Electromyograms: medical research

Measurement of Torsion in rotating shafts

Contactless Velocity measurements

Speech Research

Radio Astronomy

Determination of Noise Transmission Paths

02850-1

Figure 1-10. Correlation (Cont'd)

Figure 1-10. Correlation (Cont'd)

Perform Setup and Turn-on Procedure described in Figure 1-7.

CONTROL SETTINGS

To obtain a valid correlation function, the input signals must not be "clipped". Steps 1 through 14 of this procedure assure proper settings of the Channel A and B attenuators.

1. To perform cross-correlation, connect each of the two input signals to one of the 5488A INPUT connectors. To perform auto-correlation, connect the single input signal through a "T" connector to both inputs.

2. FUNCTION to AVERAGE

3. SENSITIVITY MULTIPLIER fully CCW

4. TRIGGER SOURCE to INTERNAL

   NOTE: Correlation requires that sampling pulses not be sync'ed to either input signal. Random triggering is most easily achieved by setting TRIGGER SOURCE to INTERNAL.

5. PRESET/NORMAL to NORMAL

6. Channel A DISPLAY to INPUT

7. Channel B DISPLAY to OFF

8. MEMORY SELECTOR switches for both channels to the same setting. FULL provides greatest resolution; correlation functions can be stored for comparison by processing in HALF or QUARTER and changing MEMORY SELECTOR settings before next processing.

9. MODE (CORRELATION) to AVG (OFF).

10. Channel A SENSITIVITY VERNIER fully CCW, to display full signal on CRT.

11. Starting from 20 V/cm, rotate Channel A SENSITIVITY control until you see clipping of INPUT signal. Go back one switch position; this is maximum sensitivity for SENSITIVITY that can be tolerated without clipping.

12. Set Channel A DISPLAY to OFF.

13. Set Channel B DISPLAY to INPUT.

14. Repeat steps 10 and 11 for Channel B SENSITIVITY control.

5480A/B system is now set for maximum sensitivity, without clipping.

Do not change SENSITIVITY control settings; Channel A VERNIER can be set for larger picture without disturbing processing.

15. Set MODE (CORRELATION) to CORR (ON).

16. Set Channel A DISPLAY to DATA.

17. Set Channel B DISPLAY to OFF.

18. Press both CLEAR DISPLAY pushbuttons.

19. Press PROCESS START button.

CONTROL SETTINGS (Cont'd)

20. Rotate SWEEP TIME and PRE-ANALYSIS DELAY switches as necessary for best presentation of correlation function. SWEEP TIME determines delay (time offset) between input signals, PRE-ANALYSIS DELAY determines delay in sampling of both input signals. For principles of operation, refer to Section II.

CRT DISPLAY

The signal at INPUT A is sampled once, at the beginning of each process sweep. The value of its amplitude is digitized and stored in a buffer register in the 5488A. Signal B is sampled 1000 (or 500 or 250) times during each processing sweep. Each signal B sample is digitized and stored in another buffer register in the 5488A. The contents of the two buffer registers are multiplied, and the product converted to an analog value. The analog value of the signal product is compared with the analog value of the memory contents for the same point; the analog difference is digitized and added to the memory contents to form the new correlation value for that point.

On the next sweep, the next point of the INPUT A signal is sampled, digitized, stored, and multiplied by all digital values of Signal B, as described above. Thus, after some number of sweeps, the value of the correlation signal is built up in the 5480A/B memory. This is the signal displayed on the 5480A/B CRT.

Amplitude and polarity of the correlation function depend on amplitude and relative phase (polarity) of the two input signals. Positive points of correlation function mean that input signals are in phase (have same polarity), negative points of correlation function mean that input signals are out-of-phase.

The autocorrelation function of a periodic signal will have the same period as the input signal. A sync signal with this period may be used when the input signal is being averaged.

The horizontal display axis is calibrated by the SWEEP TIME and X1/X5 switch settings. The period of the correlation function may be calculated as follows:

$$\text{Period} = \text{distance between corresponding points (cm)} \times \frac{\text{SWEEP TIME}}{\text{X1/X5 setting}}$$

The vertical display axis is calibrated by settings of both Channel A and Channel B SENSITIVITY controls. The vertical signal units are volts-squared, calculated as follows:

20 x Vertical deflection from baseline (cm) x SENSITIVITY A x SENSITIVITY B

Example: Vertical point 2 cm above baseline, when A SENSITIVITY is .1V/cm and B SENSITIVITY is .2V/cm is: 20 x 2 x .1 x .2 = .8V²

Vertical accuracy is ±5%.

Figure 1-11. Variance (Option 001)

DESCRIPTION: This Option, available for the 5485A, 5487A, and 5488A plug-ins, enables use of the 5480A rear-panel VARIANCE OUTPUT signal to check validity of the averaged signal. For the averaged signal to be valid, the input data signal must: 1) repeat itself, 2) always be present, and 3) always be synchronized to the sync pulse. If any of these conditions is not met, an indication can be provided in the Variance Output.

The Variance Option may be ordered installed and tested at the factory, or it may be installed in the field. Field installation consists of replacing the Switching Logic B Assembly (05485-60010) with Switching Logic B Assembly (05485-60020); no wiring changes are required.

As the input waveform is processed, its average pattern is stored in the Signal Analyzer's memory. The Variance-Output signal represents the difference between the sampled input signal and the previous value for the same point, sotred in memory. The variance trace can be used to identify points in the averaged waveform where there is additional activity other than that added simply by random noise.

CONTROL SETTINGS

1. FUNCTION to AVERAGE

2. SENSITIVITY MULTIPLIER fully CCW

3. SWEEP NUMBER to 0 for first sweep, then increase to desired value.

4. PRESET/NORMAL to NORMAL

5. MEMORY SELECTOR, ON/OFF, and DISPLAY switches to display processed input signal and variance signal traces (DATA).
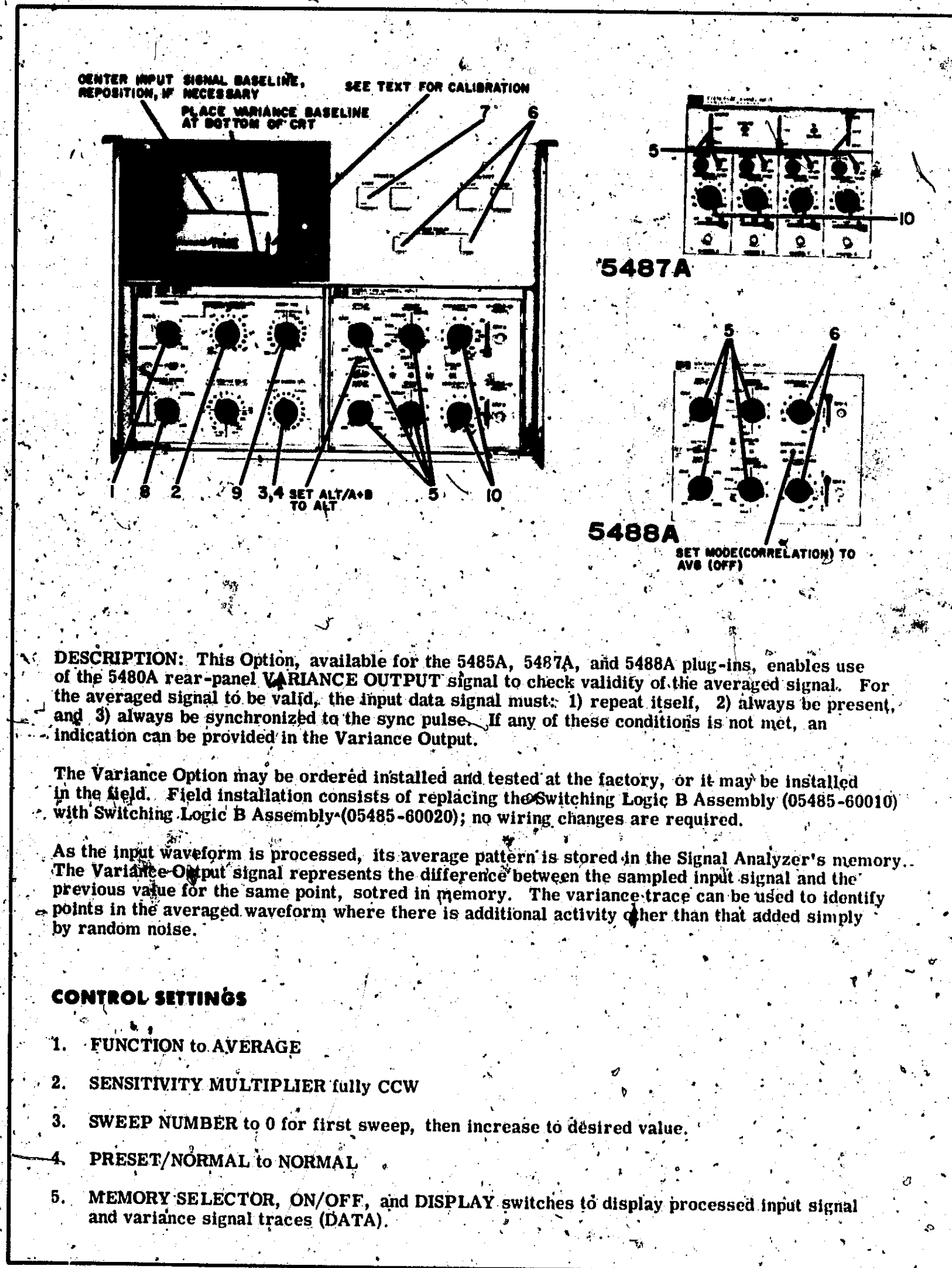
Figure 1-11. Variance (Option 001) (Cont'd)

CONTROL SETTINGS (Cont'd)

NOTE: Because the VARIANCE OUTPUT signal represents the square of the difference between the averaged input signal stored in memory, and the noisy input signal, the variance signal is mostly noise. To obtain meaningful information, the variance signal, too, must be averaged.

The variance signal processed in any memory quarter is the variance of the memory quarter immediately preceding it, as listed below:

Variance processed in Memory Quarter listed below	is always variance of noisy input signal processed in memory quarter listed below
1	4
2	3
3	1
4	2

Where possible, the noisy input signal should be put into memory quarters 1 and/or 2, and the variance signal into memory quarters 3 and/or 4.

6. Clear display by simultaneously pressing both CLEAR DISPLAY buttons. OUTPUT DISPLAY button will light, and CRT will display two horizontal lines.

7. Press PROCESS START pushbutton. Button will light, and OUTPUT DISPLAY light will go out. Display will show signal processing. Several processing sweeps will be required for a good enough average to be stored in memory to get meaningful results from the variance output display.

8. If necessary, select TRIGGER SOURCE and adjust LEVEL to initiate processing. Ordinarily, best triggering is obtained using an external triggering source having the same frequency as noisy signal to be observed. See Correlation, Figure 1-10.

9. Adjust SWEEP TIME for best display of input signal. Press both CLEAR DISPLAY buttons and PROCESS START button each time SWEEP TIME is changed.

10. Adjust VERTICAL SENSITIVITY for each displayed input channel to provide best CRT display of input signal.

CRT DISPLAY

The averaged variance output represents differences in the input signal other than random noise.

If the variance trace is a straight line, then the averaged waveform is the result of a single repetitive input which was time-locked to the synchronizing pulse (except for the special case where the absolute deviation of the spurious input from the average is the same at all points on the waveform).

If the leading or trailing edge of a pulse or square wave is jittering (or appears to be jittering due to a jittering trigger signal), the variance trace will not be a straight line (thus indicating that there was something in the signal besides noise and data).

If the input signal waveform changes, the variance will not be a straight line, but will have a shape of its own, reflecting the signal change.

CRT DISPLAY (Cont'd)

The variance trace is also useful for detecting when a signal is not present during the entire analysis interval, by showing the difference between the averaged signal and the missing input signal. All that would happed to the averaged trace is that its amplitude would shrink slightly (depending on number of sweeps that had been averaged).

The variance signal output is always positive, so the variance channel baseline may be set on the bottom line of the CRT graticule (unless the input is AC coupled).

VARIANCE CALIBRATION

1. Given noise, a voltage is provided at the rear panel:

$$V = \frac{N^2}{32}$$

where
N = value of noise in centimeters, as displayed on CRT

V = voltage provided at rear panel.

example: If the noise causes a ±2 cm deflection on CRT, then the voltage at the rear panel will equal ±125 mV ($2^2/32$).

II. Given a relative variance display on the CRT, an absolute value can be determined as follows:

1) Note deflection D in centimeters of the variance display.

2) V represents the relative variance in volts^2, according to the variance attenuator setting.

$$V = D \cdot A_v \text{ in Volt}^2$$

example: If $D = +3$ cm

and $A_v = 50$ mV/cm

then $V = 50 \cdot 3 = .150$ Volt^2

3) Determine the relative noise N (in cm) that could cause this deflection.

$$N = \sqrt{32V}$$

example: $N = \sqrt{(32)(.150)} = 2.19$ cm

4) Determine the absolute noise, according to the signal attenuator setting:

$$N_A = N \cdot A_s$$

example: If $A_s = 2$V/cm

then $N_A = (2.19)(2)$

$= 4.38$ Vrms

For additional information, refer to Figure 1-9, Average.

Figure 1-12. Summation

INPUTS

SYNC 6 1 11 7 8,9 2 3 10 B A
SIGNAL SIGNAL

10 INPUTS

5488A
SET MODE (CORRELATION) TO AVG (OFF)

DESCRIPTION: Analyzer adds successive repetitions of noisy input waveform, resulting in an uncalibrated display which is proportional to averaged signal. Display resembles oscilloscope presentation of input signal, growing with each successive sweep; display can be halved automatically when it reaches some predetermined size, to keep it on screen.

Perform SETUP and TURN-ON procedure described in Figure 1-8.

CONTROL SETTINGS

1. FUNCTION to SUMMATION.

2. Each channel to DISPLAY DATA (or to OFF, if channel is not used).

3. MEMORY SELECTOR. As desired.

    a. Larger memory sections provide more signal resolution.
    b. A processed signal can be stored in one memory section while processing is done in another section.
    c. (5485A or 5488A) If MEMORY SELECTOR controls are set to feed A and B to same quarter section of memory, A overcomes B. 5487A refer to Figure 1-6.
    d. Signal processing only occurs:
        1) In memory sections selected by MEMORY SELECTOR and ON/OFF switches.
        2) When PROCESS START button is lighted.
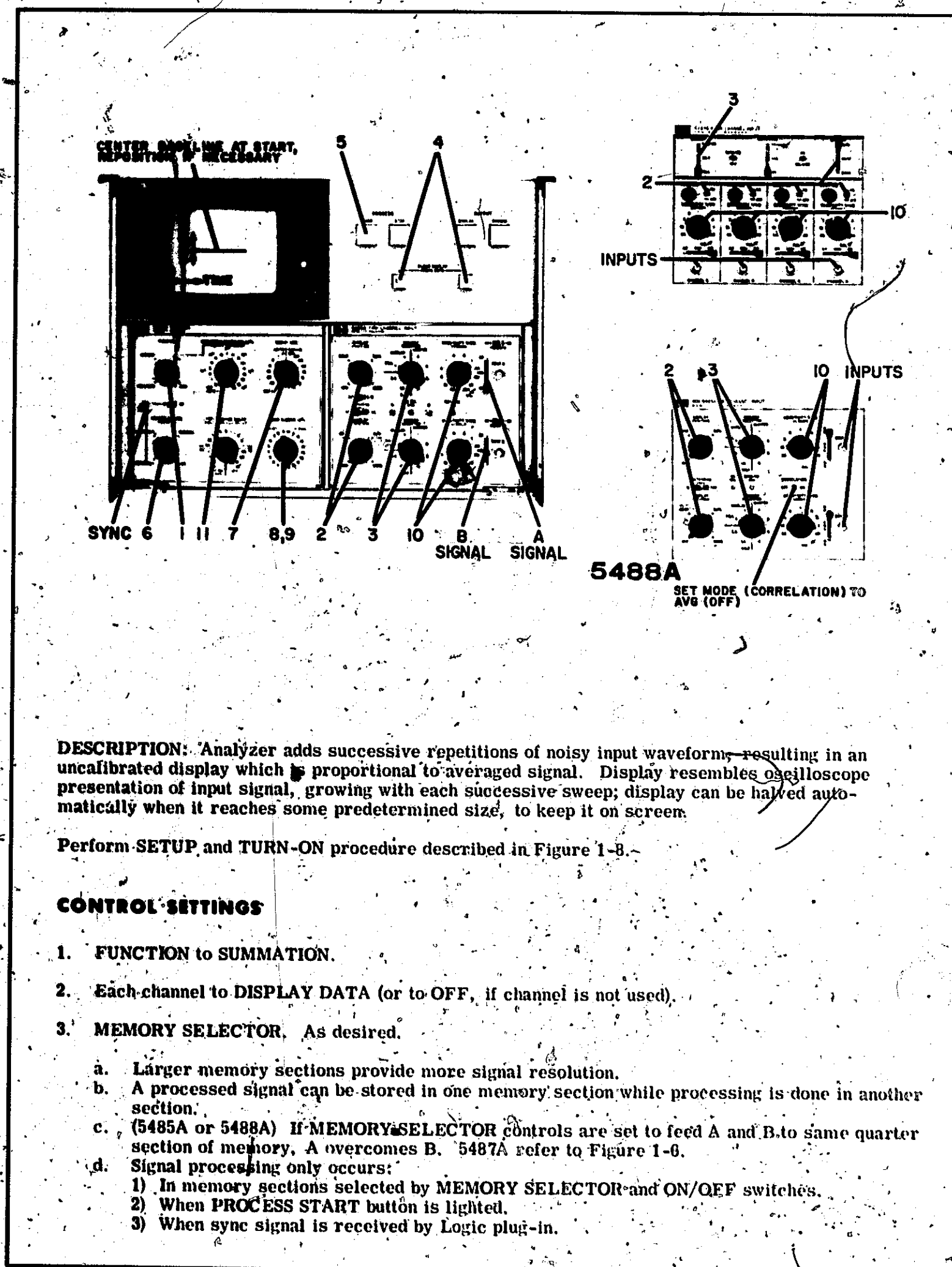   3) When sync signal is received by Logic plug-in.

Figure 1-12. Summation (Cont'd)

CONTROL SETTINGS (Cont'd)

4. Clear display by simultaneously pressing both CLEAR DISPLAY buttons. OUTPUT DISPLAY button will light, and CRT will display a horizontal line.

5. Press PROCESS START pushbutton. Button will light, and OUTPUT DISPLAY light will go out. Display will show signal processing.

6. If necessary, select TRIGGER SOURCE and adjust LEVEL to begin processing. Ordinarily, best triggering is obtained using an external triggering source having same frequency as noisy signal to be observed.

7. Adjust SWEEP TIME for best display of input signal. Press both CLEAR DISPLAY buttons and PROCESS START button each time SWEEP TIME is changed.

8. SWEEP NUMBER to 19.

9. PRESET/NORMAL to PRESET.

10. Adjust SENSITIVITY for good display

CRT DISPLAY

Signal starts at zero amplitude and grows with each sweep as Signal Analyzer totalizes input sweeps. Rate of vertical presentation growth depends on settings of SENSITIVITY for each displayed channel and SENSITIVITY MULTIPLIER. To keep growing display on-screen, SENSITIVITY MULTIPLIER setting is decreased manually or automatically, as described below.

Vertical display is not calibrated.

Horizontal display is calibrated by SWEEP TIME and X1/X5 settings.

SENSITIVITY MULTIPLIER

Controls vertical display size.

When set to AUTO (and SWEEP NUMBER set to 19):

1. Vertical display is cut in half at end of every 2^N (first, second, fourth, eighth, etc.) sweep.

2. Display continues growing at half of previous rate until it is again cut in half.

3. Unless SUMMATION process is stopped (by pressing PROCESS STOP or OUTPUT DISPLAY pushbutton), vertical display is divided in half through the N = 15 sweep; after N = 15, display keeps growing.

NOTES:

1. SENSITIVITY MULTIPLIER and SWEEP NUMBER switches interact, most noticeably at low SWEEP NUMBER settings. When SENSITIVITY MULTIPLIER is set to AUTO, display is cut in half every 2^N sweeps until N is same number set by SWEEP NUMBER switch. From this time on, no additional AUTO scaling is provided, CRT display grows and must be manually scaled using "N" SENSITIVITY MULTIPLIER SETTINGS.

2. SENSITIVITY MULTIPLIER can be set to any N number from 1 to 15. If setting is changed during a sweep, display size will change at end of that sweep.

Figure 1-12. Summation (Cont'd)

SENSITIVITY MULTIPLIER (Cont'd)

NOTES: (Cont'd)

3. SENSITIVITY MULTIPLIER can be switched from AUTO to any N number and back to AUTO, and will resume AUTO scaling without loss of count of sweeps.

4. To prevent invalid readings, the following SWEEP NUMBER and SWEEP TIME combinations should not be used when SENSITIVITY MULTIPLIER is in AUTO:

SWEEP TIME	SWEEP NUMBER
1 msec/cm	19 (PRESET) see NOTE under NORMAL VS PRESET
2 msec/cm	19, 18
5 msec/cm or slower	19, 18, 17, 16

If the above limits are not observed when using a 5486A, data will overflow available memory bits and will be lost. The 5486B automatically limits the SWEEP NUMBER used in summation to 19 in 1 msec/cm, 17 for 2 msec/cm, and 15 for 5 msec/cm and slower sweep times. The 5485A modification described below will have no effect in systems containing the 5486B logic plug-in.

This SWEEP NUMBER limitation (except 1 msec/cm) is directly related to the 5-, 7-, or 9-bit resolution of the Analog-to-Digital Converter (depending on SWEEP TIME). If input signals are so noisy that 2^{15} sweeps are not sufficient (2^{17} sweeps at 2 msec/cm), the modification below may be made to the 5485A Dual Channel Input plug-in; the modification restricts resolution to 5 bits for all SWEEP TIMEs.

Modification to 5485A for decreased resolution:

a. Remove connecting wires from XA3(2) and XA3(3).

b. Tape ends of wires removed in step 1.

c. Ground pins XA3(2) and XA3(3) by connecting them to XA3(17).

5. If power is lost following summation, and instrument is in DISPLAY, automatic scaling associated with AUTO may be lost and display will not be calibrated. (This happens because the AUTO scaling factor is stored in an active register.) Display calibration can be regained, however, by setting SENSITIVITY MULTIPLIER to: 24 - (SWEEP NUMBER + number of bits).

Number of bits = 5 in 1 msec/cm SWEEP TIME

= 7 in 2 msec/cm SWEEP TIME

= 9 in 5 msec/cm or slower SWEEP TIMEs.

Example: SWEEP NUMBER is 6, SWEEP TIME is 10 msec/cm. Set SENSITIVITY MULTIPLIER to 24 - (6 + 9) or 9.

NORMAL VS PRESET

NORMAL: Set SWEEP NUMBER to 19. Analyzer totalizes until manually stopped by pressing PROCESS STOP or OUTPUT DISPLAY pushbutton. AUTO scaling feature of SENSITIVITY MULTIPLIER ceases operation in NORMAL mode during SUMMATION after SWEEP NUMBER has been reached.

PRESET: Set SWEEP NUMBER to number of sweeps to be totalized. Signal Analyzer will totalize this number of sweeps*, stop processing, and display result.

*NOTE: Except for N = 19. In PRESET, N = 19 and N = 18 are same, 5480A sweeps for 2^{18} sweeps, then DISPLAYs result.