Signal	Wire ID	Wire Status	Pattern A	Edge A	Cursor A
Clock1	CLK1	T	X		1
Clock2	CLK2	T	X		1
sn_ready	D4	H	X		0
sn_data	D5	H	X		1
sn_sr_enb	D6	L	X	⤒	0
sn_sr[0]	D7	L	X		0
start_sn	D8	L	X		1

CLK1 Freq: 49,999,380Hz   **D3 Period: 1.00001us**   **Interval T->B: 6.7186ms**   **Interval A->B: 1.9002ms**

Serial Number read 4   **4/11/2016 11:15:32 AM**