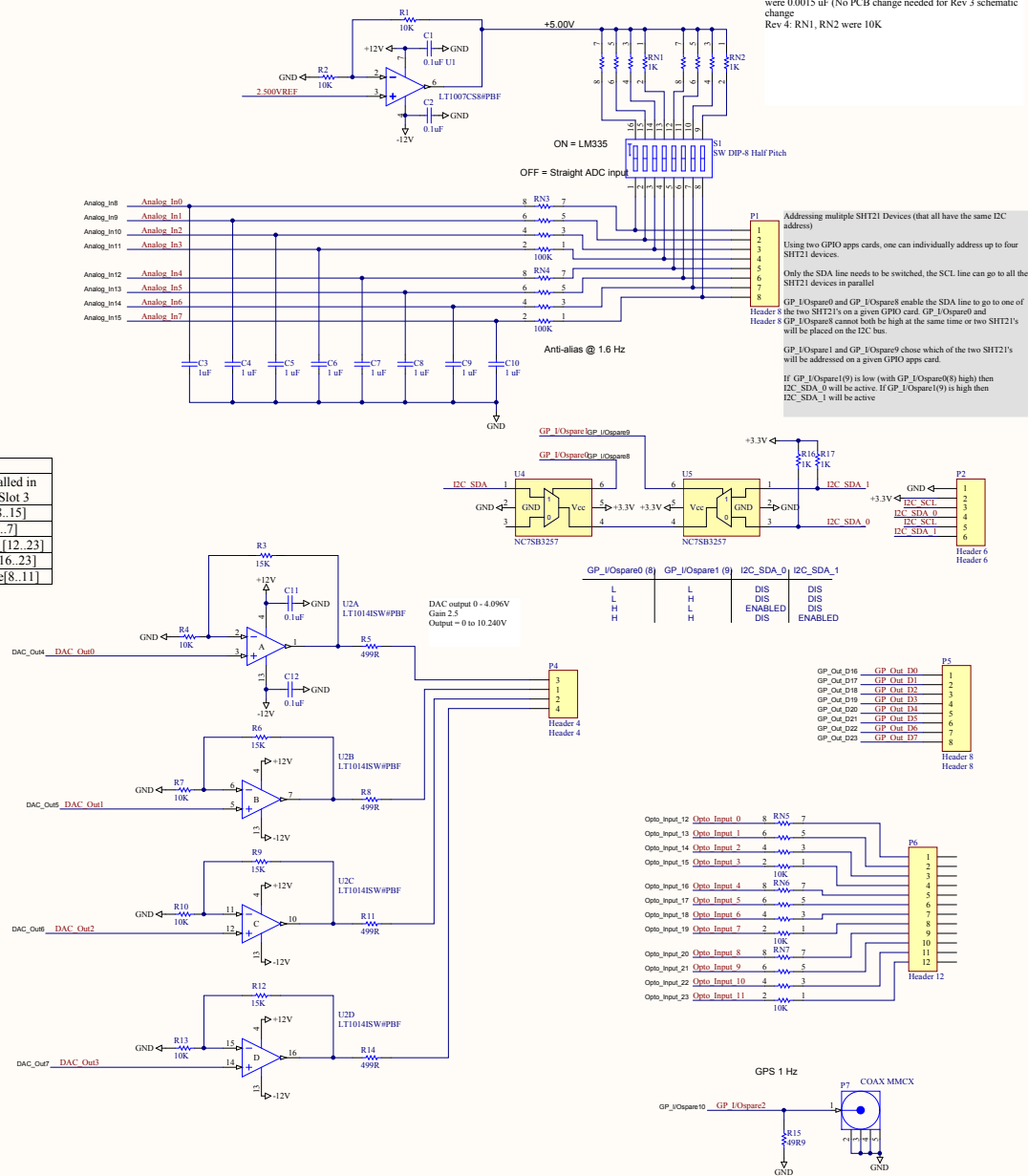
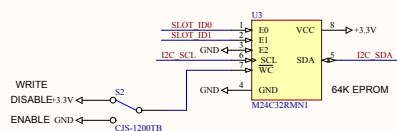
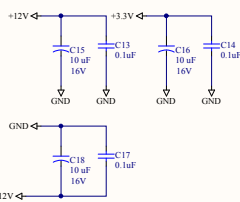


	Signal Mapping to SuperFin Module	
Signal Group	Board installed in Slot 0 or Slot 1	Board installed in Slot 2 or Slot 3
Analog_In	Analog_In[0..7]	Analog_In[8..15]
DAC_Out	DAC_Out[0..3]	DAC_Out[4..7]
Opto_Input	Opto_Input [0..11]	Opto_Input [12..23]
GP_Out_D	GP_Out_D[0..7]	GP_Out_D[16..23]
GP_I/Osarep	GP_I/Osarep(0..3)	GP_I/Osarep(8..11)



Rev 1: Initial Design
Rev 2: Added support for two I2C channels
Rev 3: RN3, RN4 were 10K. R16, R17 were 10K. C3-C10
were 0.0015 uF (No PCB change needed for Rev 3 schematic
change)
Rev 4: RN1, RN2 were 10K

If GP_I/Ospare1(9) is low (with GP_I/Ospare0(8) high) then I2C_SDA_0 will be active. If GP_I/Ospare1(9) is high then I2C_SDA_1 will be active

Diagram showing the connection of pins 5 and 6 to the I2C module. Pin 5 is connected to I2C SCL and pin 6 is connected to I2C SDA 1.

I2C_SDA_0 I2C_SDA_1

Header 6
Header 6

DIS	DIS
DIS	DIS
ENABLED	DIS

DIS | **ENABLED**

GP Out D15 GP Out D0 P5

GP_Out_D16	GP_Out_D16	1
GP_Out_D17	GP_Out_D17	2
GP_Out_D18	GP_Out_D18	3
GP_Out_D19	GP_Out_D19	

GP_Out_D20	GP Out D4	4
GP_Out_D21	GP Out D5	5
GP_Out_D22	GP Out D6	6

GP_Out_D23 GP Out D7

Header 8

Header 8

8 RN5 7

The circuit diagram for Problem 6 shows a network of resistors and a power source. The resistors are labeled with their values in ohms (Ω):

- Resistor 1: $4\ \Omega$ (top left)
- Resistor 2: $6\ \Omega$ (middle left)
- Resistor 3: $4\ \Omega$ (bottom left)
- Resistor 4: $1\ \Omega$ (top right)
- Resistor 5: $5\ \Omega$ (middle right)
- Resistor 6: $3\ \Omega$ (bottom right)

The power source is labeled **P6** and is represented by a yellow rectangle. The circuit is connected in a way that allows for the calculation of the power dissipated in the $3\ \Omega$ resistor.

A circuit diagram showing a 10K resistor connected between pins 1 and 2 of a 3-pin component. The component has pins labeled 1, 2, and 3. The resistor is labeled '10K'.

Diagram for Example 1: A circuit with a 10V DC source. The circuit consists of a 4 ohm resistor in series with a parallel combination of a 3 ohm resistor and a series combination of a 2 ohm resistor and a 1 ohm resistor. The output terminals are labeled 7, 8, and 9.

Header 12



10K

GPS 1 Hz

	6
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