

Solutions PDF Generated from: solutions-openai-generated/labs/lab-01-solutions-set- 03.json

Question A

Complete the truth table for the full adder circuit.

A	B	Cin	S	Cout
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Question B

Derive Boolean equations for Co and Sum (using whatever method you like). It is possible to complete the equations using only NAND and XOR logical gates (Hint: use some of what you already have from Sum for Co, and that Co is always asserted for AB.)

$$\text{Co (A,B)} = \text{NAND (NAND (A, B), Sum (A,B))}$$

$$\text{Sum (A,B)} = \text{XOR (A, B)}$$

Question C

Draw the schematic of the full adder circuit according to the equations you have derived. Note that you sometimes use the output of one logical gate for BOTH expressions, to minimize the number of gates used.

Execution Time

0:00:15.421329

OpenAI Parameters

Model: text-davinci-003, Max. Tokens: 1024, Temperature: 1, N: 1