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Question A

Simplify f(x,y,z) = (x(y+z)+z)y into sum-of-products form. Follow example from lecture 3 slides, however, at each step of the simplification process write down the Boolean algebra theorem used in this step. Note lecture slides sometimes used multiple theorems in each step in the slides, your solutions should use only one theorem at each step and may have more steps than shown in the slides. List of Boolean algebra theorems can be found in the supplementary materials on Canvas site.

Question B

Simplify the following Boolean equation into sum-of-products form. Again, justify each simplification step with Boolean algebra theorem used: f(a,b,c,d,e) = (a+b+c)(a+b+d)(a+b+e)(a+d+e)(a+c).

```
f(a,b,c,d,e) = (a + b + c')(a + b + d)(a + b + e)(a + d' + e)(a' + c)

Step 1: Using the Distributive Law:
f(a,b,c,d,e) = (a+b)(a+d)(a+e)(a+d'+e)(a'+c) + (a+b)(a+d)(a+e)(a+d'+e)(b+c') + (a+b)(a+d)(a+e)(a'+c)(b+d')

Step 2: Using the Associative and Commutative Laws:
f(a,b,c,d,e) = (a+d'+e)(a+b+c')(a+d)(a+e)(a'+c) + (a+d'+e)(a+b+d)(a+e)(a'+c)(b+c')

Step 3: Using the Distributive Law:
f(a,b,c,d,e) = (a+d'+e)(a'+c)(a + b + c')(a + b + d)(a + e) + (a+d'+e)(a'+c)(a + b + d)(a+e)(b + c')

Step 4: Using the Associative Law:
f(a,b,c,d,e) = (a+d'+e)(a'+c)(a + b)(a+e)(b+c'+d)

Answer: f(a,b,c,d,e) = (a+d'+e)(a'+c)(a + b)(a+e)(b+c'+d)
```

Question C

Simplify the following Boolean equation into sum-of-products form. Again, justify each simplification step with Boolean algebra theorem used: f(a,b,c,d,e) = (a+b+c)(a+b+d)(a+b+e)(a+d+e)(a+c).

```
f(a, b, c, d, e) = (a + b)(a+d'+e)(a + c) ----> Distributive Property

f(a, b, c, d, e) = (a + b)(a + d')(a + e)(a + c) ----> Complements Theorem

f(a, b, c, d, e) = a(a + b + d' + e) + b(a+d' + e) + c(a' + d' + e) ----> Distributive Property

f(a, b, c, d, e) = a'bd'e + abd'e + ac'd'e ----> Sum of Products Form
```

Question D

Simplify f(x,y,z) = (x(y+z)+z)y into most simple form, which may not be sum-of-products. Again, justify each simplification step with Boolean algebra theorem used.

```
= x'yz' + x'y + z'y

By the associative law,
= x'yz' + (x'+z')y

By the distributive law,
= (x'+z')y + x'yz'

By the commutative law,
= (x'+z')y + (x'z'+z'y)

By the absorption law,
= (x'+z')y
```

Question E

Simplify f(x,y,z) = (x(y+z)+z)y into most simple form, which may not be sum-of-products. Again, justify each simplification step with Boolean algebra theorem used.

The most simple form of the equation is y(x'+z'), which is in product-of-sums form.

Question F

Expand f(x,y,z) = xy + yz into canonical sum-of-products. What are the minterms that compose the canonical sum-of-products? Which rows of the truth table do the minterms correspond to? Write the truth table.

The canonical sum-of-products form of f(x,y,z) = xy + yz is xyz + xy'z' + x'y'z. The minterms that compose the canonical sum-of-products are xyz, xy'z', and x'y'z. These minterms correspond to rows 6, 3, and 4 of the truth table, respectively.

Truth Table:

Question G, Part A

Draw the symbol, Boolean equation, and truth table for a four-input OR gate.

Symbol:

```
|--|
А--о-В
 1__1
 C--o--D
Boolean Equation:
A + B + C + D
Truth Table:
A | B | C | D | Output
0 | 0 | 0 | 0 | 0
0 | 0 | 0 | 1 | 1
0 | 0 | 1 | 0 | 1
0 | 0 | 1 | 1 | 1
0 | 1 | 0 | 0 | 1
0 | 1 | 0 | 1 | 1
0 | 1 | 1 | 0 | 1
0 | 1 | 1 | 1 | 1
1 | 0 | 0 | 0 | 1
1 | 0 | 0 | 1 | 1
1 | 0 | 1 | 0 | 1
1 | 0 | 1 | 1 | 1
1 | 1 | 0 | 0 | 1
1 | 1 | 0 | 1 | 1
1 | 1 | 1 | 0 | 1
1 | 1 | 1 | 1 | 1
```

Question G, Part B

Draw the symbol, Boolean equation, and truth table for a three-input XNOR gate.

Symbol:

Visual representation of a 3 input XNOR gate



Boolean Equation:

Y= x1 XNOR x2 XNOR x3

Truth Table:

x1	1	x2	-	x.	3	۱ ۱	Y
0	1	0	I	0	1	1	
0	-	0	l	1	-	0	
0	-	1		0	-	0	
0	-	1		1	-	1	
1	-	0	I	0	-	0	
1	-	0		1	-	1	
1	-	1		0	-	1	
1	1	1	I	1	1	0	

Question G, Part C

Draw the symbol, Boolean equation, and truth table for a five-input NAND gate.

Symbol:



Boolean Equation:

(A B C D E) 0

Truth Table:

```
0 | 1 | 1 | 0 | 1 | 1
0 | 1 | 1 | 1 | 0 | 1
0 | 1 | 1 | 1 | 1 | 1
1 | 0 | 0 | 0 | 0 | 1
1 | 0 | 0 | 0 | 1 | 1
1 | 0 | 0 | 1 | 0 | 1
1 | 0 | 0 | 1 | 1 | 1
1 | 0 | 1 | 0 | 0 | 1
1 | 0 | 1 | 0 | 1 | 1
1 | 0 | 1 | 1 | 0 | 1
1 | 0 | 1 | 1 | 1 | 1
1 | 1 | 0 | 0 | 0 | 1
1 | 1 | 0 | 0 | 1 | 1
1 | 1 | 0 | 1 | 0 | 1
1 | 1 | 0 | 1 | 1 | 1
1 | 1 | 1 | 0 | 0 | 1
1 | 1 | 1 | 0 | 1 | 0
1 | 1 | 1 | 1 | 0 | 0
1 | 1 | 1 | 1 | 1 | 0
```

Question I

There are 16 different truth tables for Boolean functions of two variables. List each truth table. Give each one a short descriptive name (such as OR, NAND, and so on).

```
1. AND - Output is 1 only if both inputs are 1
        В
               Output
0
        1
               0
        0
               0
1
        1
               1
2. OR - Output is 1 if either of the inputs are 1
       В
               Output
0
        0
               0
0
        1
               1
1
        0
               1
        1
               1
3. NAND - Output is 0 unless both inputs are 1
       В
               Output
Α
0
       0
               1
0
        1
               1
1
        0
4. NOR - Output is 1 only if both inputs are 0
       В
               Output
Α
        0
0
0
        1
               0
1
        0
               0
```

5. XOR - Output is 1 if one of the inputs is 1 but not both

```
Α
       В
               Output
0
       1
              1
       0
              1
1
               0
1
       1
6. XNOR - Output is 1 only if both inputs are the same
       В
               Output
Α
0
       0
0
              0
       1
       0
1
              0
       1
              1
7. Implication - Output is 1 unless first input is 1 and second input is 0
       В
               Output
0
       0
              1
0
       1
              1
       0
               0
8. Converse Implication - Output is 1 unless first input is 0 and second input is 1
       В
               Output
0
       0
               1
0
       1
1
       0
              1
9. Negation - Output is 0 when input is 1 and output is 1 when input is 0
Α
       Output
       1
1
10. Double Negation - Output is 0 when input is 0 and output is 1 when input is 1
Α
       Output
       1
11. Exclusive OR - Output is 1 only when inputs are different
       В
               Output
A
0
       0
       1
1
       0
              1
1
       1
              0
12. Exclusive NOR - Output is 0 only when inputs are different
Α
       В
               Output
       0
               1
0
       1
              0
1
       0
               0
```

```
13. Contrapositive - Output is 1 unless first input is 0 and second input is 1
Α
       В
               Output
0
       0
               1
0
       1
               1
1
       0
14. Biconditional - Output is 1 only if both inputs are the same
       В
               Output
0
       0
0
       1
               0
1
       0
               0
15. Biconditional Implication - Output is 1 unless first input is 1 and second input is 0
       В
               Output
0
       0
0
       1
               1
       1
```

16. Negation Implication - Output is 0 unless first input is 0 and second input is 1

```
Output
A
         В
0
         0
                  1
0
         1
1
         0
                  1
         1
```

Question J

Write a truth table for the function performed by the gate shown in the figure. The truth table should have two inputs, A and B. What is the name of this function?

Truth Table:

```
Output
     В
Α
0
     0
           0
     1
           1
           0
     0
     1
           0
```

Name of Function: NOT Gate

Question K

Write a truth table for the function performed by the gate in the figure. The truth table should have three inputs, A, B, and C.

```
A | B | C | Output
---|---|----
0 | 0 | 0 | 0
0 | 0 | 1 | 1
0 | 1 | 0 | 0
0 | 1 | 1 | 0
```

```
1 | 0 | 0 | 0
1 | 0 | 1 | 0
1 | 1 | 0 | 1
1 | 1 | 1 | 0
```

Question L

Sketch a transistor-level circuit for a CMOS four-input NOR gate.

The following is an example of a transistor-level circuit for a CMOS four-input NOR gate:

[Insert Diagram]

In this circuit, the fourinputs (A-D) control the gates of transistors M1-M4, which connect to a pull-up network (PM1 and PM2). The output (Q) is connected to the common node of the pull-up network. When at least one of the four inputs (A-D) is high (1), transistors M1-M4 turn on, which sources current from PM1 and PM2, resulting in a low (0) output. When all inputs (A-D) are low (0), transistors M1-M4 are off, and the pull-up network pulls the output (Q) high (1).

Execution Time

0:03:19.891064

OpenAI Parameters

Model: text-davinci-003, Max. Tokens: 1024, Temperature: 1, N: 1