Register Test Report				
Date	**19/10/26**	**12:39:58**		
Register Name	**Accesor Function**	**Test Result**		**Git Commit Hash**
		Pass	**Fail**	
gmac_reg_phy_control_h	XGMAC_WritePHYControlH()	Yes		74941d5
	XGMAC_ReadPHYControlH()	Yes		74941d5
gmac_reg_phy_control_l	XGMAC_WritePHYControlL()	Yes		74941d5
	XGMAC_ReadPHYControlL()	Yes		74941d5
gmac_reg_mac_address	XGMAC_SetMACAddress()	Yes		74941d5
	XGMAC_GetMACAddress()	Yes		74941d5
gmac_reg_local_ip_address	XGMAC_SetLocalIPAddress()	Yes		74941d5
	XGMAC_GetLocalIPAddress()	Yes		74941d5
gmac_reg_gateway_ip_address	XGMAC_SetGatewayIPAddress()	Yes		74941d5
	XGMAC_GetGatewayIPAddress()	Yes		74941d5
gmac_reg_multicast_ip_address	XGMAC_SetMulticastIPAddress()	Yes		74941d5
	XGMAC_GetMulticastIPAddress()	Yes		74941d5
gmac_reg_multicast_ip_mask	XGMAC_SetMulticastIPMask()	Yes		74941d5
	XGMAC_GetMulticastIPMask()	Yes		74941d5
gmac_reg_udp_port	XGMAC_SetUDPPort()	Yes		74941d5
	XGMAC_GetUDPPort()	Yes		74941d5
gmac_reg_udp_port_mask	XGMAC_SetUDPPortMask()	Yes		74941d5
	XGMAC_GetUDPPortMask()	Yes		74941d5
gmac_reg_mac_enable	XGMAC_EnableXGMAC()	Yes		74941d5
	XGMAC_DisableXGMAC()	Yes		74941d5
gmac_reg_mac_promiscous_mode	XGMAC_EnableXGMACPromiscuousMode()	Yes		74941d5
	XGMAC_DisableXGMACPromiscuousMode()	Yes		74941d5
gmac_reg_counters_reset	XGMAC_ResetStatisticsCounters()	Yes		74941d5
gmac_reg_core_type	XGMAC_GetCoreType()	Yes		74941d5
gmac_reg_phy_status_h	XGMAC_GetPHYStatusH()	Yes		74941d5
gmac_reg_phy_status_l	XGMAC_GetPHYStatusL()	Yes		74941d5
gmac_reg_tx_packet_rate	XGMAC_GetTXPacketRate()	Yes		74941d5
gmac_reg_tx_packet_count	XGMAC_GetTXPacketCount()	Yes		74941d5
gmac_reg_tx_valid_rate	XGMAC_GetTXValidRate()	Yes		74941d5
gmac_reg_tx_valid_count	XGMAC_GetTXValidCount()	Yes		74941d5
gmac_reg_tx_overflow_count	XGMAC_GetTXOverflowCount()	Yes		74941d5
gmac_reg_tx_afull_count	XGMAC_GetTXAFullCount()	Yes		74941d5
gmac_reg_rx_packet_rate	XGMAC_GetRXPacketRate()	Yes		74941d5
gmac_reg_rx_packet_count	XGMAC_GetRXPacketCount()	Yes		74941d5
gmac_reg_rx_valid_rate	XGMAC_GetRXValidRate()	Yes		74941d5
gmac_reg_rx_valid_count	XGMAC_GetRXValidCount()	Yes		74941d5
gmac_reg_rx_overflow_count	XGMAC_GetRXOverflowCount()	Yes		74941d5
gmac_reg_rx_almost_full_count	XGMAC_GetRXAFullCount()	Yes		74941d5
gmac_reg_rx_bad_packet_count	XGMAC_GetRXBadPacketCount()	Yes		74941d5
gmac_reg_arp_size	XGMAC_GetARPSize()	Yes		74941d5
gmac_reg_tx_word_size	XGMAC_GetTXWordSize()	Yes		74941d5
gmac_reg_rx_word_size	XGMAC_GetRXWordSize()	Yes		74941d5
gmac_reg_tx_buffer_max_size	XGMAC_GetTXBufferMaximumSize()	Yes		74941d5
gmac_reg_rx_buffer_max_size	XGMAC_GetRXBufferMaximumSize()	Yes		74941d5

ARP Interface Test Report			
Date	**19/10/26**	**12:39:58**	
Write Interface	**Accesor Function**	**Test Result**	**Git Commit Hash**
		Pass	**Fail**	
gmac_arp_cache_write_enable	XGMAC_WriteARPCacheEntry()	Yes		74941d5
gmac_arp_cache_write_data		Yes		74941d5
gmac_arp_cache_write_address		Yes		74941d5
Read Interface				
gmac_arp_cache_read_enable	XGMAC_ReadARPCacheEntry()	Yes		74941d5
gmac_arp_cache_read_data		Yes		74941d5
gmac_arp_cache_read_address		Yes		74941d5

CPU Receive Interface Test Report				
Date	**19/10/26**	**12:39:58**		
Ringbuffer Interface	**Accesor Function**	**Test Result**		**Git Commit Hash**
		Pass	**Fail**	
gmac_rx_ringbuffer_slot_id	XGMAC_SetRXSlotID()	Yes		74941d5
	XGMAC_GetRXSlotID()	Yes		74941d5
gmac_rx_ringbuffer_slot_clear	XGMAC_RXSlotClear()	Yes		74941d5
gmac_rx_ringbuffer_slot_status	XGMAC_GetRXSlotStatus()	Yes		74941d5
gmac_rx_ringbuffer_number_slots_filled	XGMAC_GetRXNumberOfSlotsFilled()	Yes		74941d5
Read Interface				
gmac_rx_data_read_enable	XGMAC_ReceiveEthernetFrame()			74941d5
gmac_rx_data_read_byte_enable				74941d5
gmac_rx_data_read_data				74941d5
gmac_rx_data_read_address				74941d5

CPU Transmit Interface Test Report				
Date	**19/10/26**	**12:39:58**		**Git Commit Hash**
Ringbuffer Interface	**Accesor Function**	**Test Result**		
		Pass	**Fail**	
gmac_tx_ringbuffer_slot_id	XGMAC_SetTXSlotID()	Yes		74941d5
	XGMAC_GetTXSlotID()	Yes		74941d5
gmac_tx_ringbuffer_slot_set	XGMAC_TXSlotSet()	Yes		74941d5
gmac_tx_ringbuffer_slot_status	XGMAC_GetTXSlotStatus()	Yes		82fc18b
gmac_tx_ringbuffer_number_slots_filled	XGMAC_GetTXNumberOfSlotsFilled()	Yes		82fc18b
Read Interface				
gmac_tx_data_read_enable				82fc18b
gmac_tx_data_read_byte_enable				82fc18b
gmac_tx_data_read_data				82fc18b
gmac_tx_data_read_address				82fc18b
Write Interface	XGMAC_TransmitEthernetFrame()			
gmac_tx_data_write_enable				82fc18b
gmac_tx_data_write_byte_enable				82fc18b
gmac_tx_data_write_data				82fc18b
gmac_tx_data_write_address				82fc18b