

Measurement and Analysis of GPU-accelerated Applications with HPC Toolkit

Jonathon Anderson, Yuning Xia, John Mellor-Crummey
Rice University

Consortium for the Advancement of Scientific Software
Birds of a Feather Days
February 11, 2026



Office of
Science



DOE's GPU-Accelerated Exascale Platforms



- Frontier compute nodes (OLCF)
 - 1 AMD EPYC “Trento” CPU
 - 4 MI250X AMD Radeon Instinct GPUs
 - 4 Slingshot 11 endpoints
 - Unified memory architecture



- Aurora compute nodes (ALCF)
 - 2 Intel Xeon “Sapphire Rapids” processors
 - 6 Intel Data Center GPU Max 1500
 - 8 Slingshot 11 endpoints
 - Unified memory architecture



- El Capitan compute nodes (LLNL)
 - 4 AMD MI300A APU
 - 4 Slingshot 11 endpoints
 - Unified memory architecture

Tuning HPC Applications

Profile the Application

Collect detailed performance data on an application execution

04

Analyze Performance Data

Aggregate performance data from all application threads and generate a database of "analysis results"

01

03

02

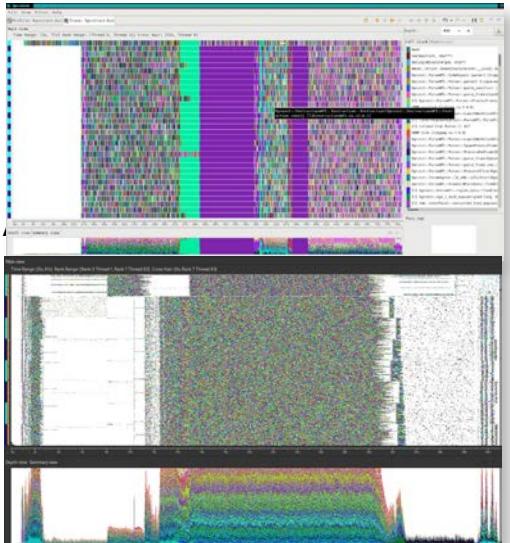
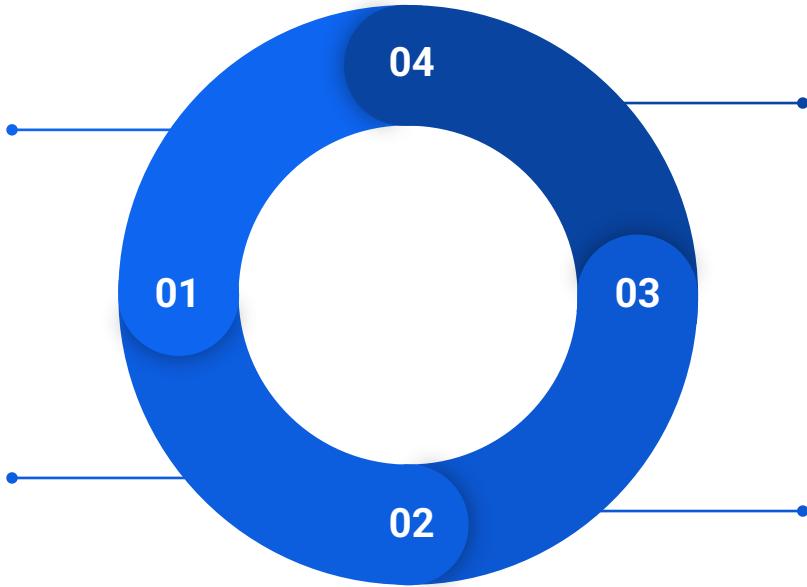
Optimize and Improve

Remove performance bottlenecks and further improve the application

Inspect and Identify

Inspect locations with poor performance and identify root causes

Tuning HPC Applications



```

Measurements: /opt/intel/kernels/libalgos/REDUCE SIMD/mkl_poclkit/rj-ra/jra-perf exec database
Measurements: /opt/intel/kernels/libalgos/REDUCE SIMD/mkl_poclkit/rj-ra/jra-perf exec measurements

PROGRAM TOTALS:
-----+
Total Execution Time: 8.000000
Total Stall Cycles: 1,473,985,912
Total GPU Occupancy: 38% (0.07,056)
Overall Stall Ratio: 96.3K
Kernels Analyzed: 1
-----+
UP TO KERNELS BY STALL CYCLES
-----+
# Kernel           Time (s)    Stall Cycles   Stall N   Occupancy   Sample Rate
l_text            0.0059    343,932,928    97.0K    38% (vgqr)  181.3G/s / 28K
-----+
-----+
Kernel: l_text
GPU Usage: CSDFB554d847f4096712f209e313.gpuinfo
(Occupancy: 38% (12/40 moves/CU, Limited by vgqr) [VGPR=48, SGPR=10, LO=64])
-----+
          len GPU Performance Analysis
-----+
WARNING: Very low profile coverage: 66/15559 (0.4%)
```

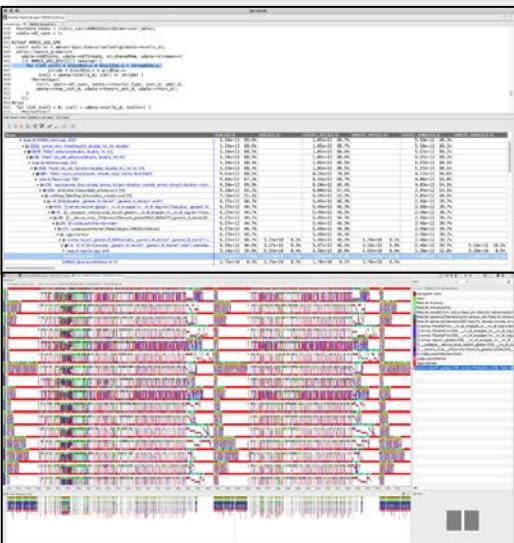
Kernel: l_text

Architecture: AMD ME380

Stall Cycles: 423,624,784

STALL ANALYSIS (FC Sampling + Back-slicing = Root Cause)

Root Cause Location	Stall Opcode	Root Cause Location	Stall Opcode	Cycles N Total Speedup
global_func.mpp:25	__m256i_mm256_sub_ps	global_func.mpp:25	__m256i_mm256_sub_ps	177,433,720 71.7%
and_device_Functions.h:778 r�� barrier	__r��_and_device_Functions_h_778_r��_barrier	and_device_Functions.h:778 r�� barrier	__r��_and_device_Functions_h_778_r��_barrier	64,048,592 18.4K 1.1x
functional.h:242	__wcontext_wcontext_m	block_LockFunc.h:258	global_func.mpp:25	16,777,216 4.0K 1.0x
functional.h:242	__wcontext_wcontext_m	block_LockFunc.h:258	global_func.mpp:25	16,777,216 4.0K 1.0x
functional.h:242	__wcontext_wcontext_m	block_LockFunc.h:258	__addc_le32_u32_u32	16,065,495 3.6K 1.0x
global_func.mpp:150	__m256i_mm256_sub_ps	global_func.mpp:150	__m256i_mm256_sub_ps	16,065,495 3.6K 1.0x



Why HPCToolkit?

- **Widely applicable:** many parallel programming models within & across nodes
- **Easy:** profiles unmodified application binaries
- **Fast:** low-overhead measurement; parallel analysis of large performance data
- **Scalable:** measure and analyze GPU-accelerated executions at large scale
- **Informative:** learn where an application spends its time and why
 - call path profiles associate metrics with application source code contexts
 - hierarchical traces reveal execution dynamics
 - LEO identifies and quantifies root causes of GPU performance losses
- **Broad audience:** developers of applications, frameworks, runtimes & tools
- **Multiplatform:** unlike vendor tools, works with a wide range of CPUs and GPUs

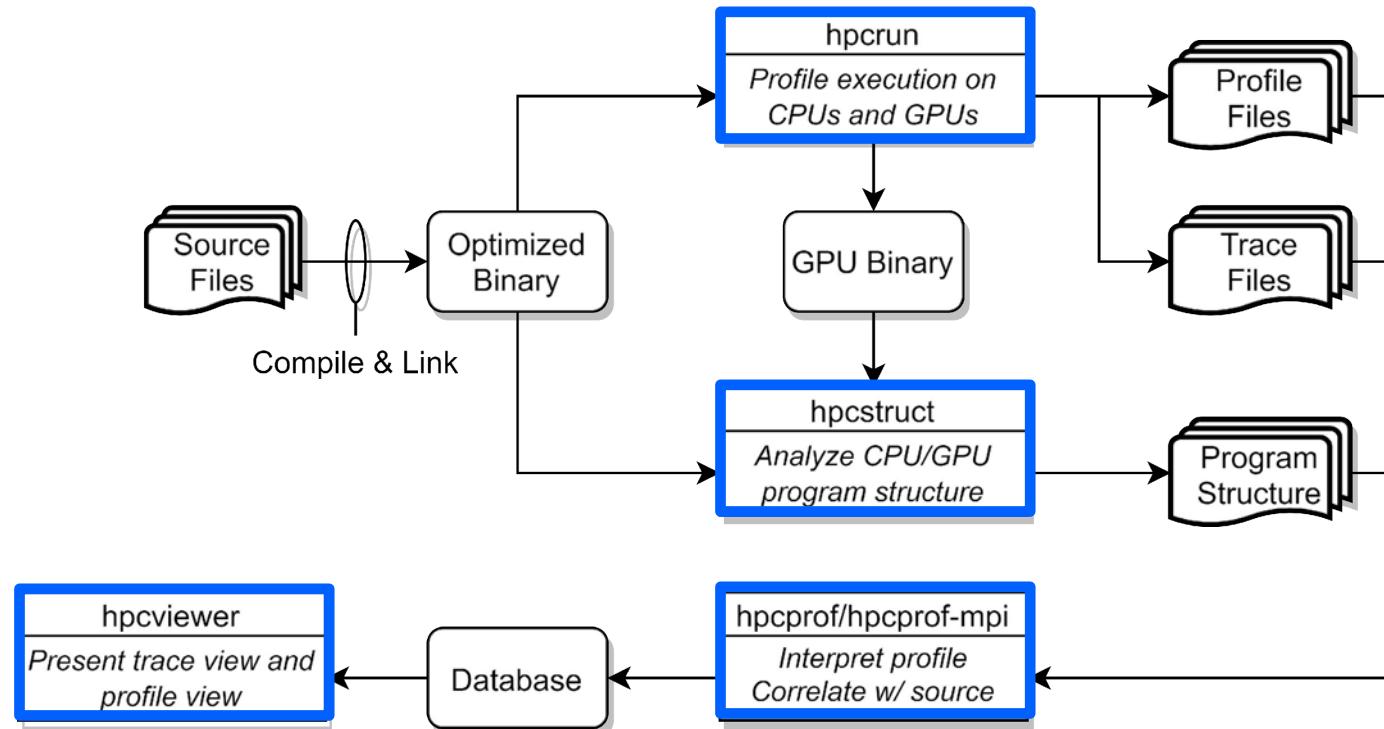
How is HPCToolkit Different from Vendor Tools?

- More scalable tracing than vendor tools
 - measure exascale executions across many nodes and GPUs
 - GUI supports interactive exploration of TB of performance data
- Scalable, parallel post-mortem analysis vs. non-scalable in-GUI analysis
- Detailed reconstruction of calling context profiles within GPU kernels
- Identifies and quantifies root causes of GPU performance losses

Today's Agenda

- Introduce HPCToolkit tools and workflow
- Illustrate HPCToolkit's use with some case studies
- Live demos
 - profiling and tracing
 - instruction-based performance metrics
- Instruction-level performance monitoring
 - explaining instruction-level performance
 - improving analysis and attribution of PC samples
 - automating analysis of GPU bottlenecks
- Discussion about needs, problems, and suggestions

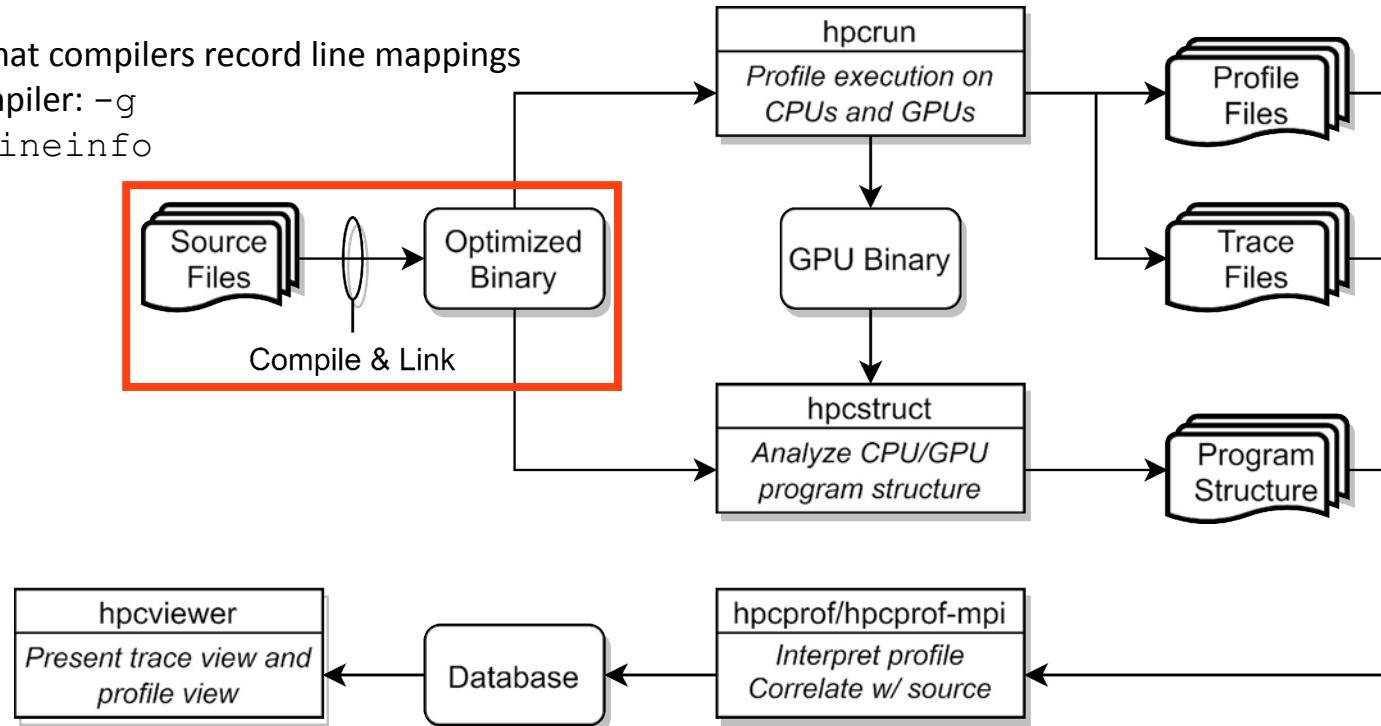
HPCToolkit's Workflow for GPU-accelerated Applications



HPCToolkit's Workflow for GPU-accelerated Applications

Step 1:

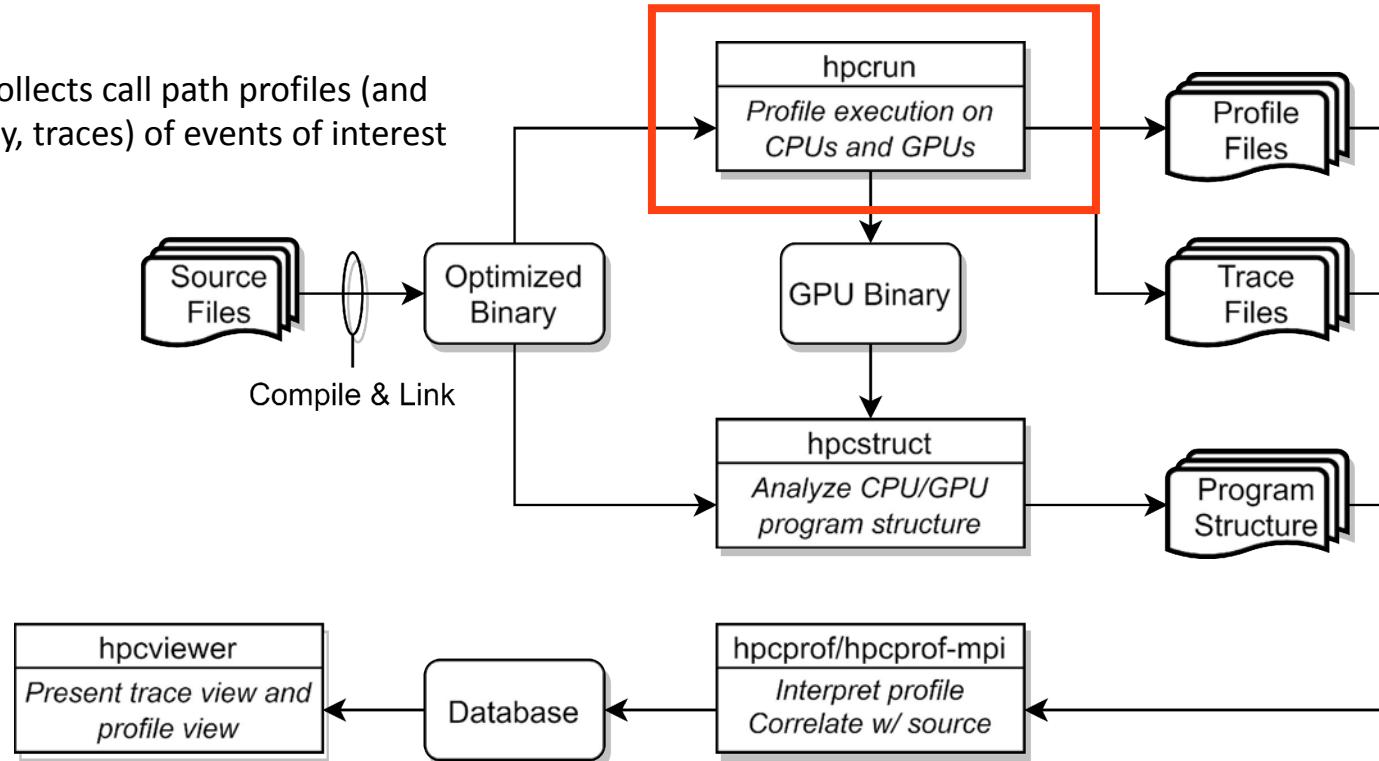
- Ensure that compilers record line mappings
- host compiler: -g
- nvcc: -lineinfo



HPCToolkit's Workflow for GPU-accelerated Applications

Step 2:

- *hpcrun* collects call path profiles (and optionally, traces) of events of interest



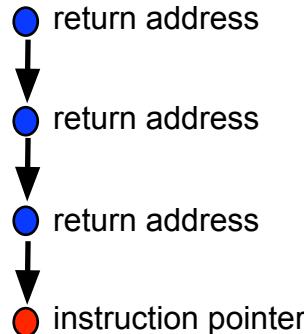
Measurement of CPU and GPU-accelerated Applications

- Sampling using Linux timers and hardware counters on the CPU
- Callbacks when GPU operations are launched
- Event stream or callbacks for GPU operation completion
- PC Samples: AMD, NVIDIA, Intel
- Binary instrumentation of GPU kernels: Intel

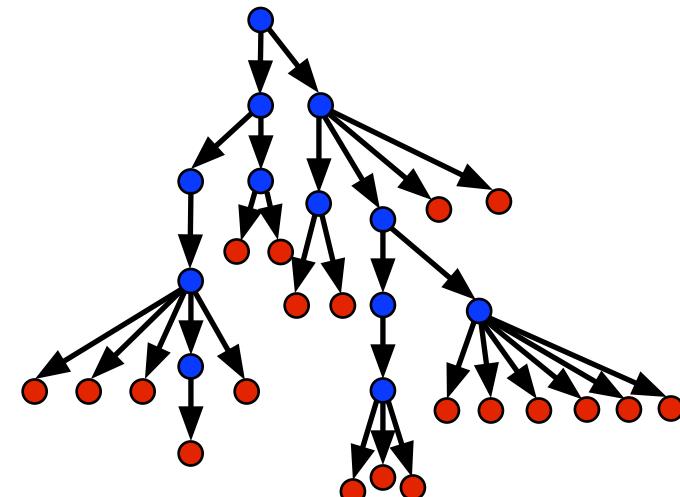
Call Stack Unwinding to Attribute Costs in Context

- Unwind when timer or hardware counter overflows
 - measurement overhead \propto sampling frequency rather than call frequency
- Unwind to capture context for events such as GPU kernel launches

Call path sample



Calling context tree



hpcrun: Measure CPU and/or GPU activity

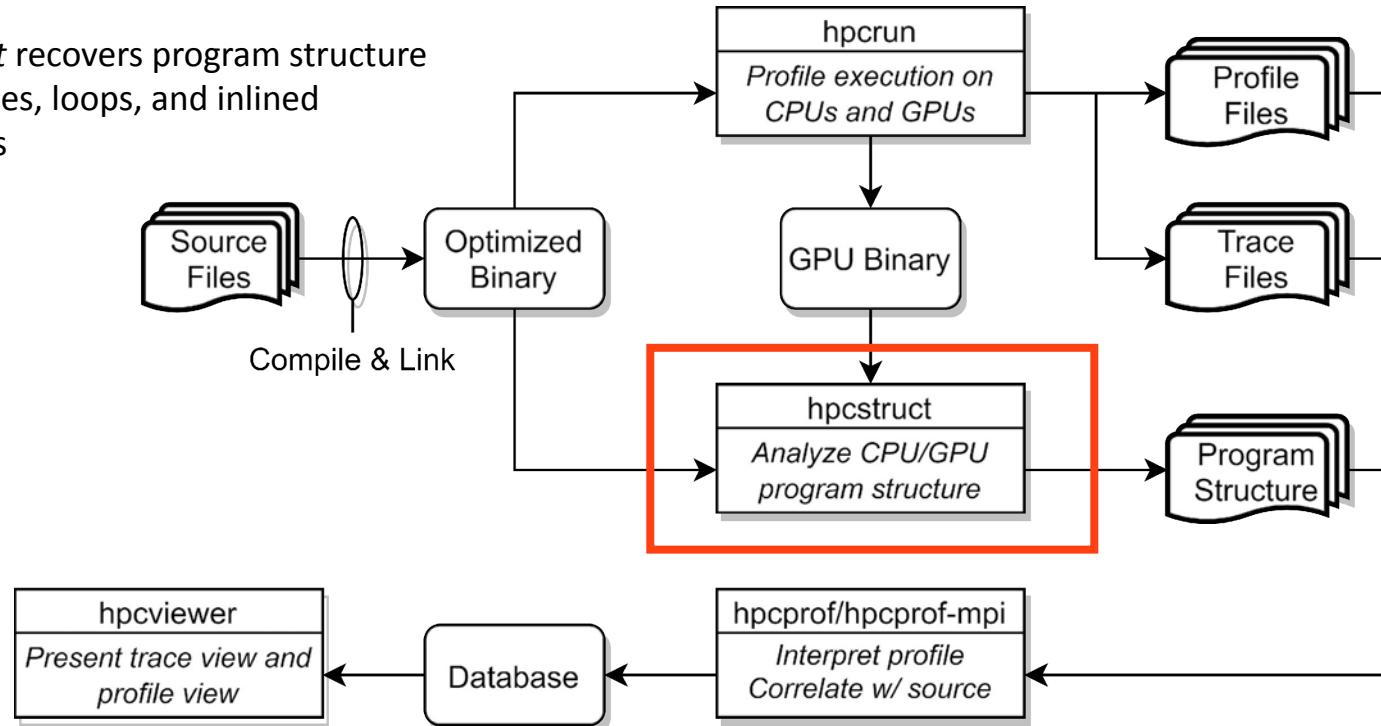
- CPU and GPU profiling
 - hpcrun -e CPUTIME -e gpu=**XXX** <app> ...
- CPU and GPU profiling and tracing
 - hpcrun -e CPUTIME -e gpu=**xxx** -**tt** <app>
- GPU PC sampling
 - hpcrun -e gpu=**YYY**,pc <app>
- Measuring MPI programs
 - srun -n <ranks> ... hpcrun -e CPUTIME -e gpu=**XXX** <app>

xxx $\in \{cuda, rocm, opencl, level0\}$
yyy $\in \{cuda, rocm, level0\}$

HPCToolkit's Workflow for GPU-accelerated Applications

Step 3:

- *hpcstruct* recovers program structure about lines, loops, and inlined functions



hpcstruct: Analyze CPU and GPU Binaries Using Multiple Threads

- Usage

```
hpcstruct [--gpucfg yes] <measurement-directory>
```

- What it does

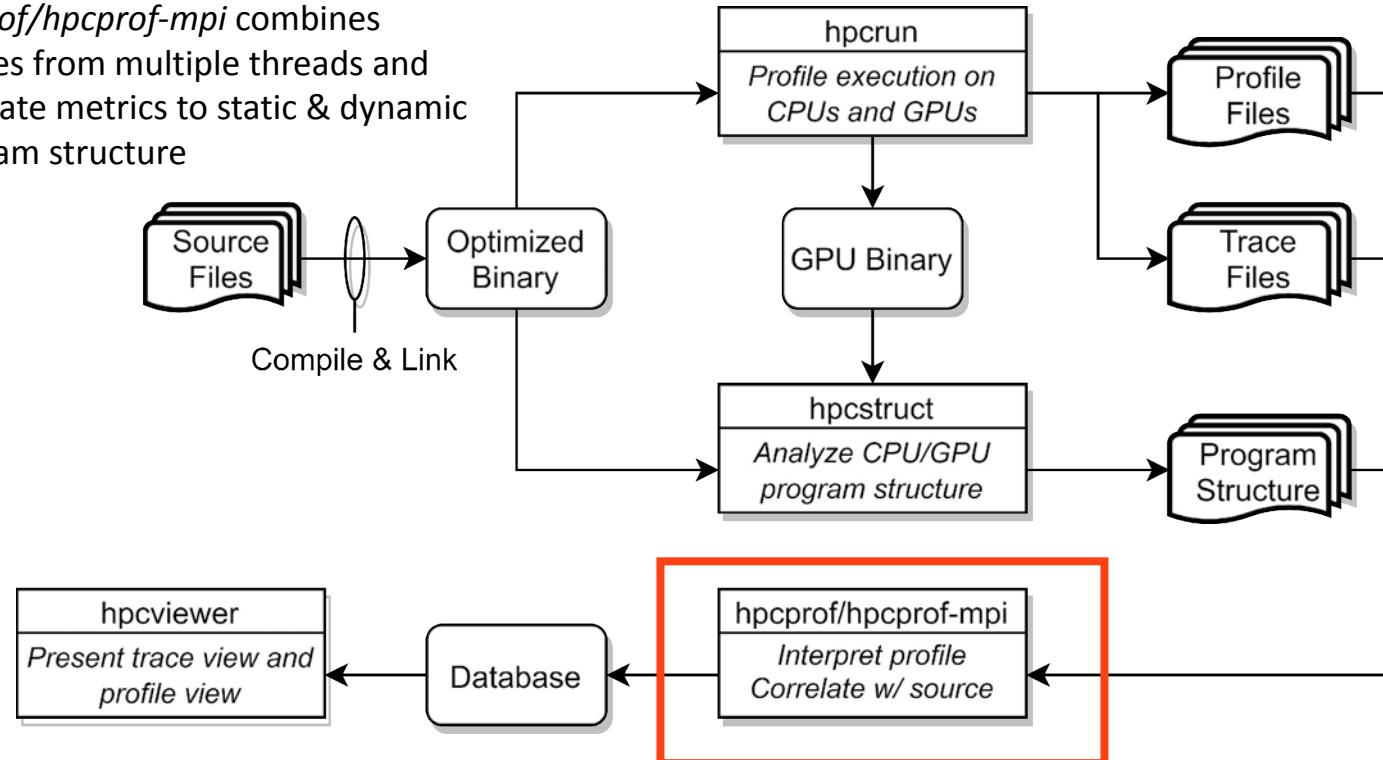
- Recover program structure information
 - Files, functions, inlined templates or functions, loops, source lines
- In parallel, analyze all CPU and GPU binaries that were measured by HPCToolkit
 - typically analyze large application binaries with 16 threads
 - typically analyze multiple small application binaries concurrently with 2 threads each
- Cache binary analysis results for reuse when analyzing other executions

NOTE: **--gpucfg yes** needed only for detailed analysis of PC samples

HPCToolkit's Workflow for GPU-accelerated Applications

Step 4:

- *hpcprof/hpcprof-mpi* combines profiles from multiple threads and correlate metrics to static & dynamic program structure



hpcprof/hpcprof-mpi: Associate Measurements with Program Structure

- Analyze data from modest-scale executions with multithreading

```
hpcprof <measurement-directory>
```

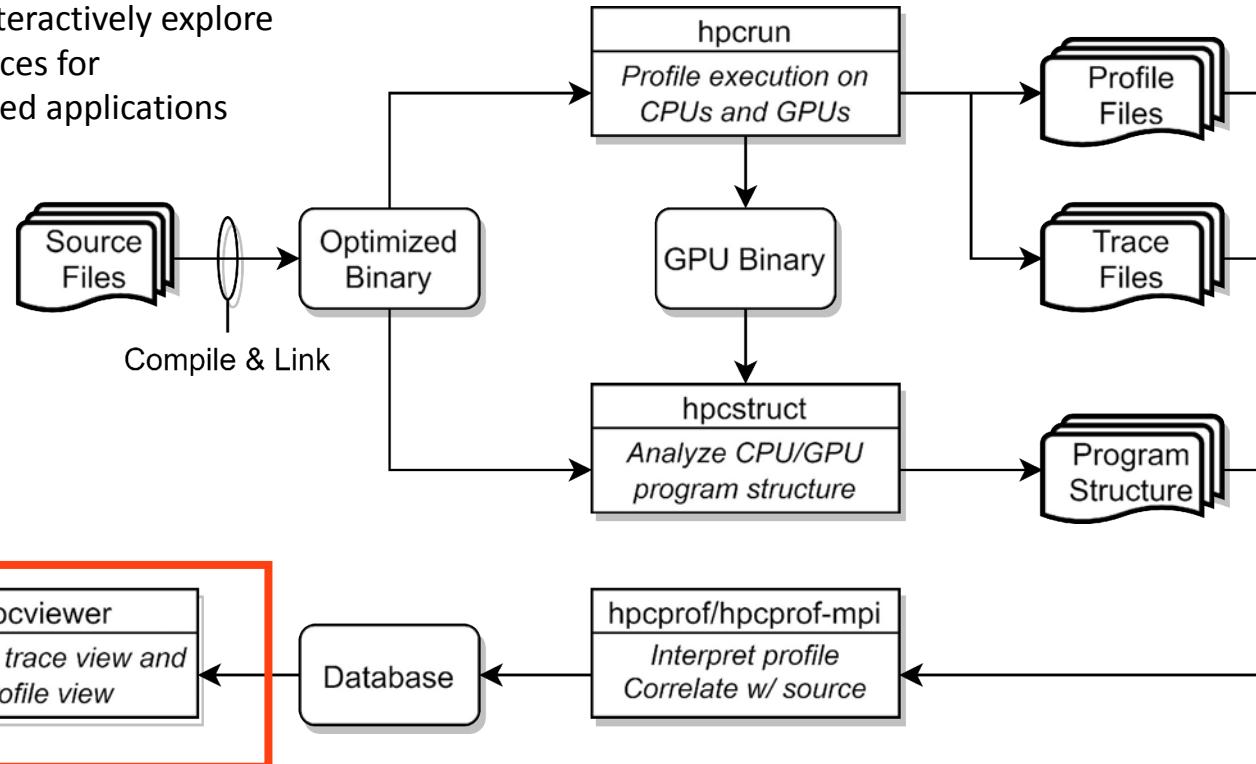
- Analyze data from large-scale executions with distributed-memory parallelism + multithreading

```
srun -n ${NODES} --ppn 1 --depth=128 \
hpcprof-mpi <measurement-directory>
```

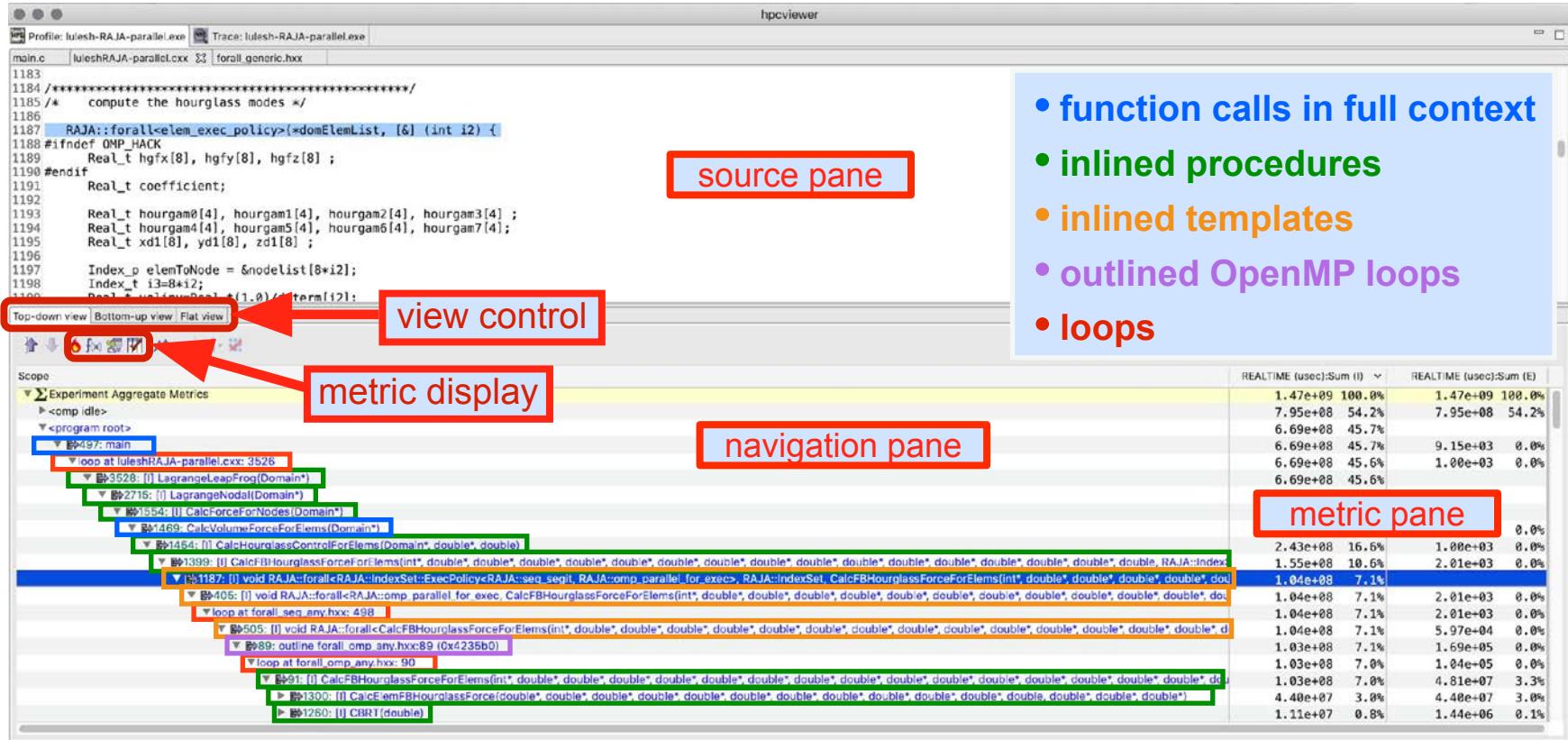
HPCToolkit's Workflow for GPU-accelerated Applications

Step 4:

- *hpcviewer* - interactively explore profile and traces for GPU-accelerated applications

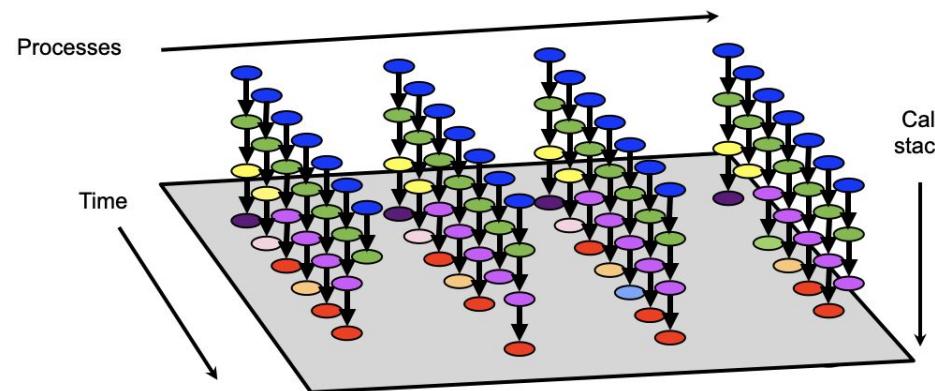


Code-centric Analysis with hpcviewer



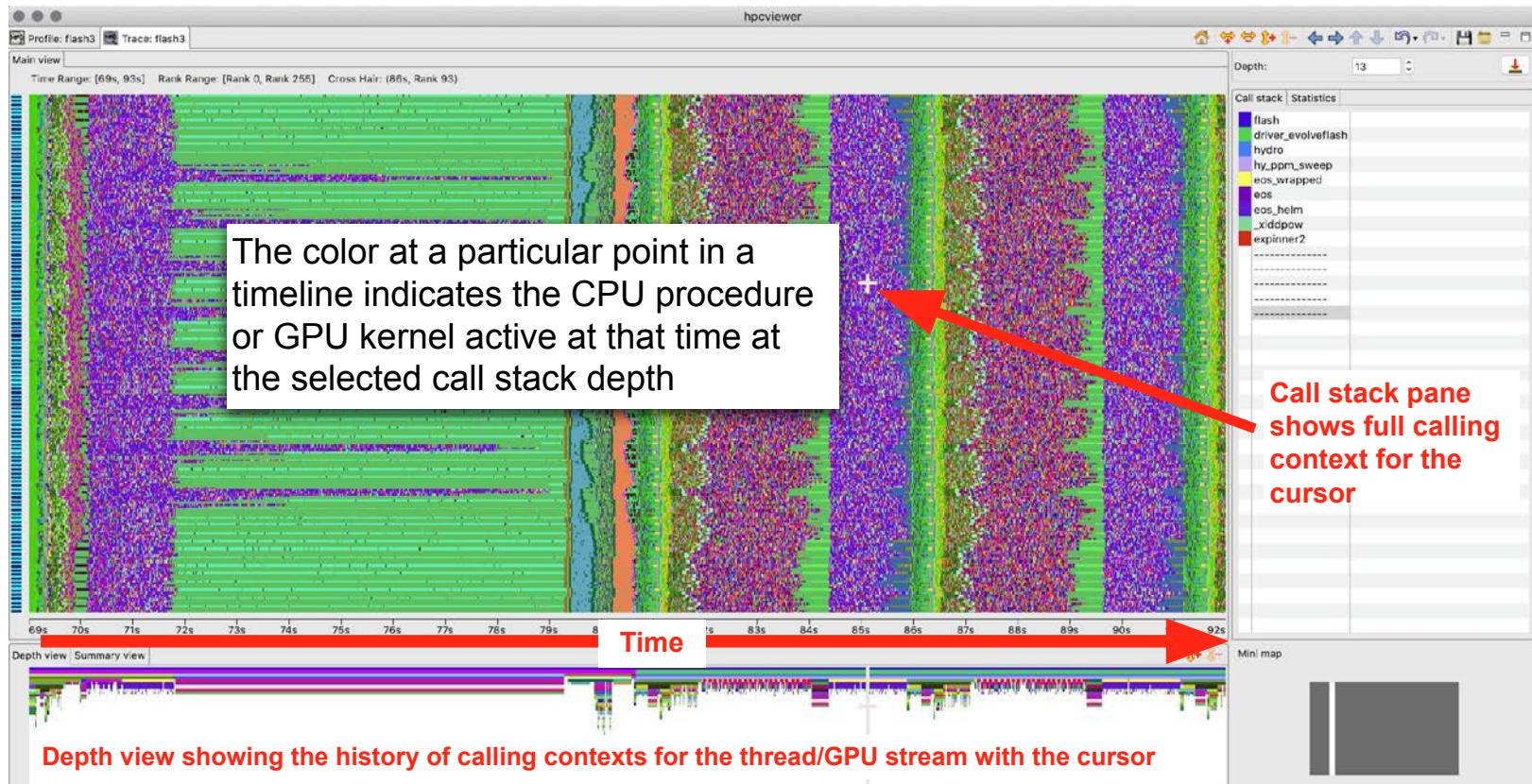
Understanding Temporal Behavior

- Profiling compresses out the temporal dimension
 - Temporal patterns, e.g. serial sections and dynamic load imbalance are invisible in profiles
- What can we do? Trace call path samples
 - N times per second, take a call path sample of each thread
 - Organize the samples for each thread along a time line
 - View how the execution evolves left to right
 - What do we view? assign each procedure a color; view a depth slice of an execution



Understanding hpcviewer's Trace View

MPI ranks,OpenMP Threads, GPU streams



Today's Agenda

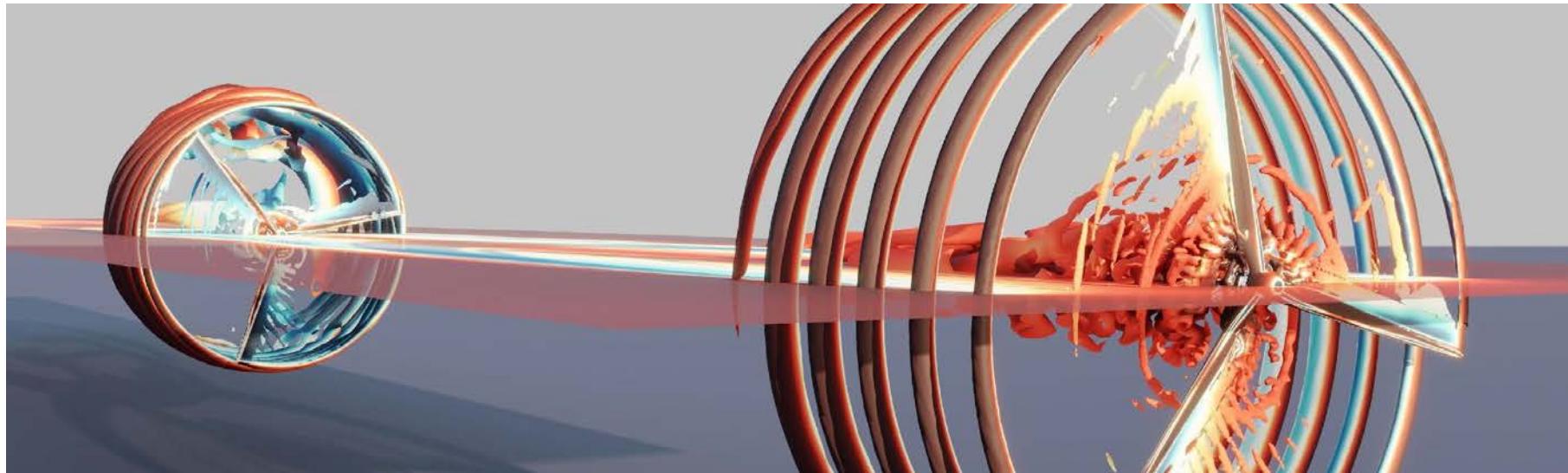
- Introduce HPCToolkit tools and workflow
- Illustrate HPCToolkit's use with some case studies
- Live demos
 - profiling and tracing
 - instruction-based performance metrics
- Instruction-level performance monitoring
 - explaining instruction-level performance
 - improving analysis and attribution of PC samples
 - automating analysis of GPU bottlenecks
- Discussion about needs, problems, and suggestions

Case Studies

- ExaWind (Nalu-Wind + AMRWind) - Wind turbine and wind farm simulation
- PeleLMeX - Adaptive mesh hydrodynamics code for low mach number reacting flows
- GAMESS (OpenMP) - general ab initio quantum chemistry package
- LAMMPS (Kokkos) - classical molecular dynamics code with a focus on materials modeling

At Exascale!

ExaWind: Modeling Turbine Wake Formation



ExaWind: Wakes from Three Turbines over Time

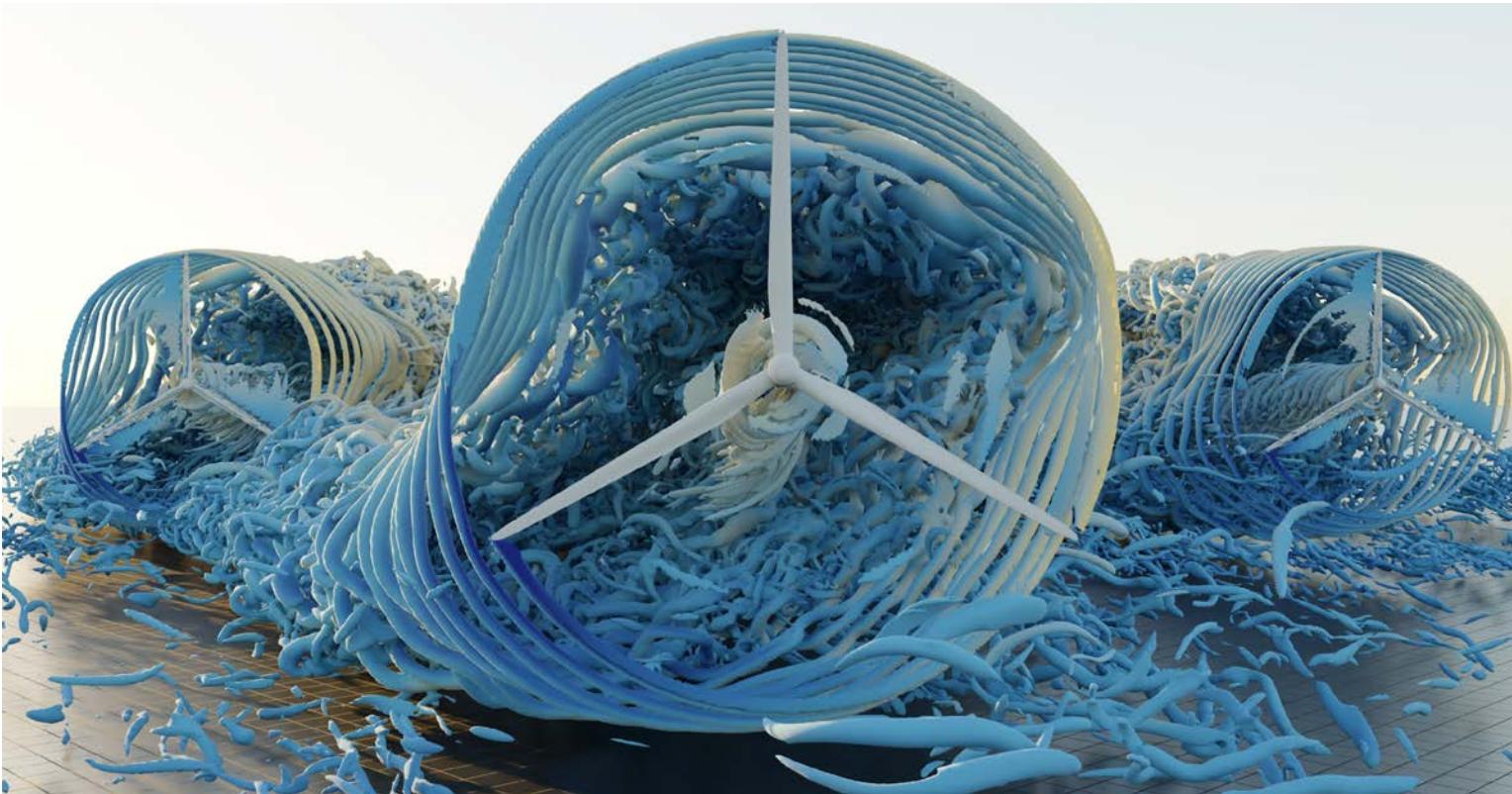


Figure credit: Jon Rood, NREL

ExaWind: Visualization of a Wind Farm Simulation

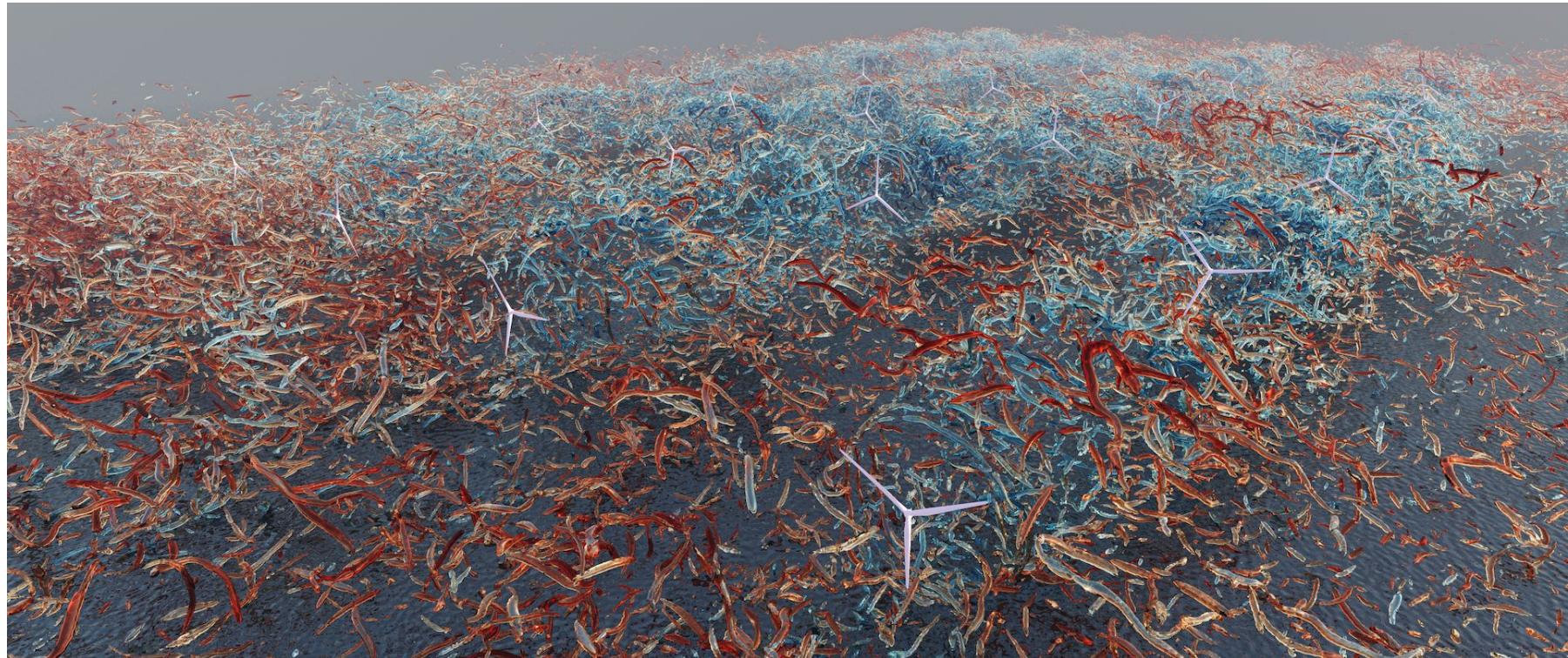


Figure credit: Jon Rood, NREL

ExaWind: Execution Traces on Frontier Collected with HPCToolkit

Traces on roughly 64K MPI ranks + 8K GPUs for ~17minutes

Before: MPI waiting (bad), shown in red

After: MPI overhead negligible*

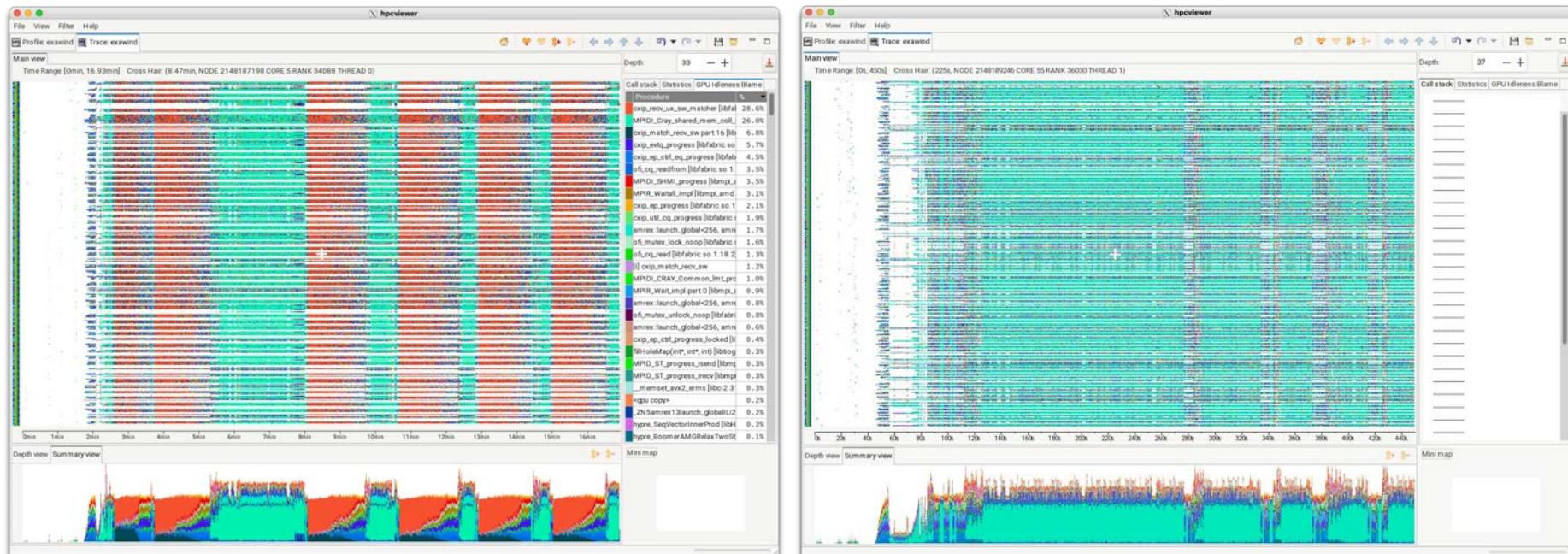


Figure credits: Jon Rood, NREL

*replaced non-blocking send/recv with ialltoally

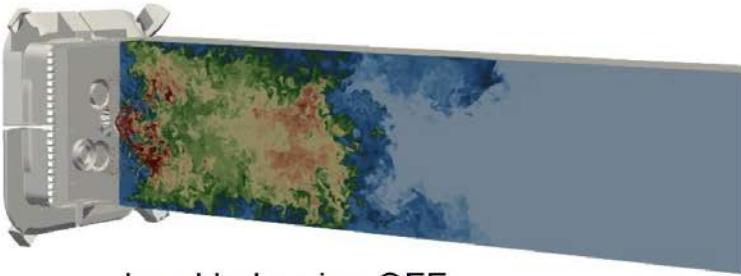
ExaWind Testimonials for HPCToolkit

*I just wanted to mention we've been using HPCToolkit a lot for our ExaWind application on Frontier, which is a **hugely complicated code**, and **your profiler is one of the only ones** we've found that really lets us easily instrument and then browse what our application is doing at runtime including GPUs. As an example, during a recent hackathon we had, we **improved our large scale performance by 24x** by understanding our code better with HPCToolkit and **running it on 1000s of nodes while profiling**. We also recently improved upon this by 10% for our total runtime.*

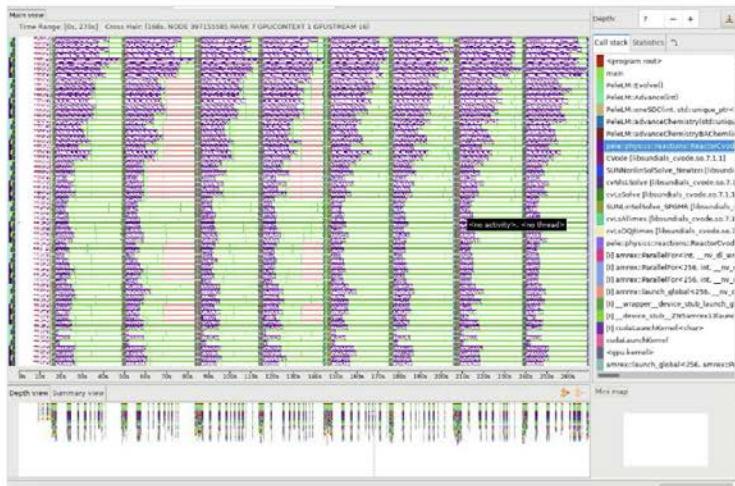
- Jon Rood NREL (5/31/2024)

*One big thing for us is that we can't overstate how complicated ExaWind is in general, and how complicated it is to build, so finding out that **HPCToolkit could easily profile our entire application without a ton of instrumentation during the build process, and be able to profile it on a huge amount of Frontier with line numbers and visualizing the trace was really amazing to us.***

- Jon Rood NREL (6/3/2024)



Load balancing OFF
(pink-> Cvode calls)



Full SAF Case: Load Balancing Issues

Confirmed on
Frontier
production runs:
60% speedup

Load balancing ON

Grid from checkpoint file
before regridding, Avg.
Time/dt = 92.3 s

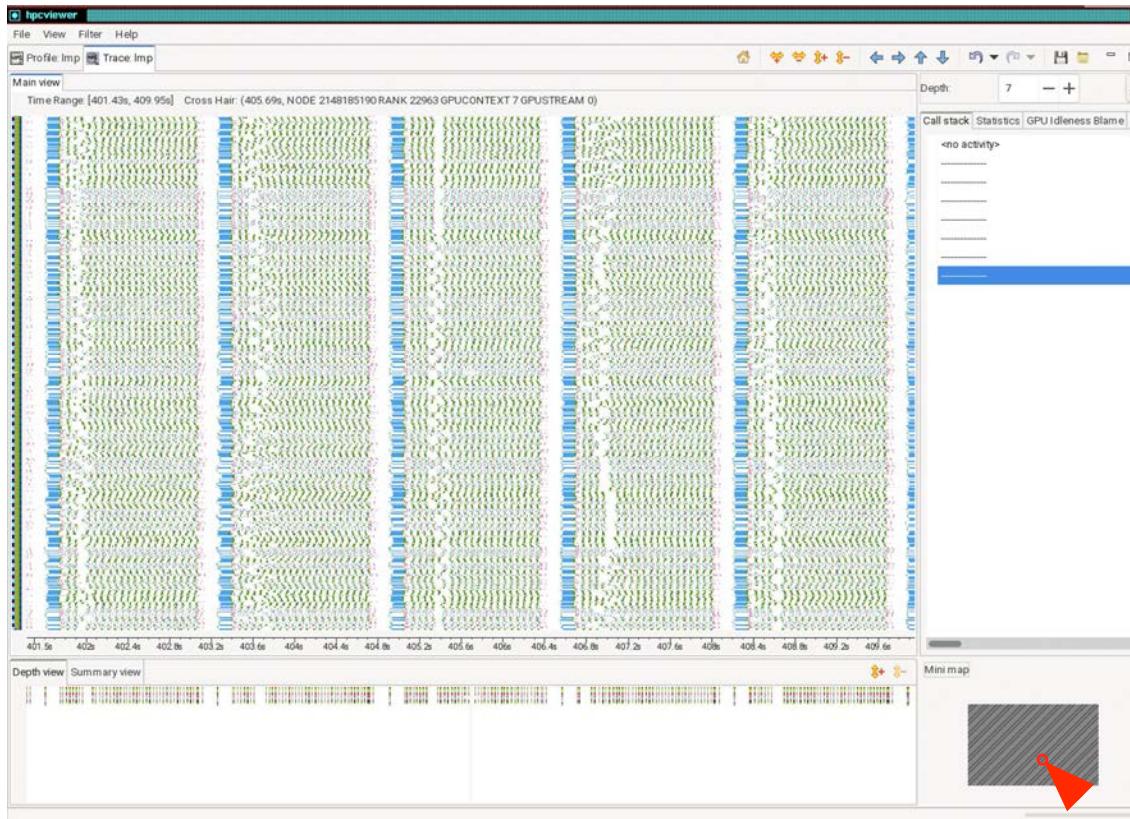
After regridding, Avg. Time/dt = 62.5s
- Down to 40s when setting
amr.max_grid_size=32



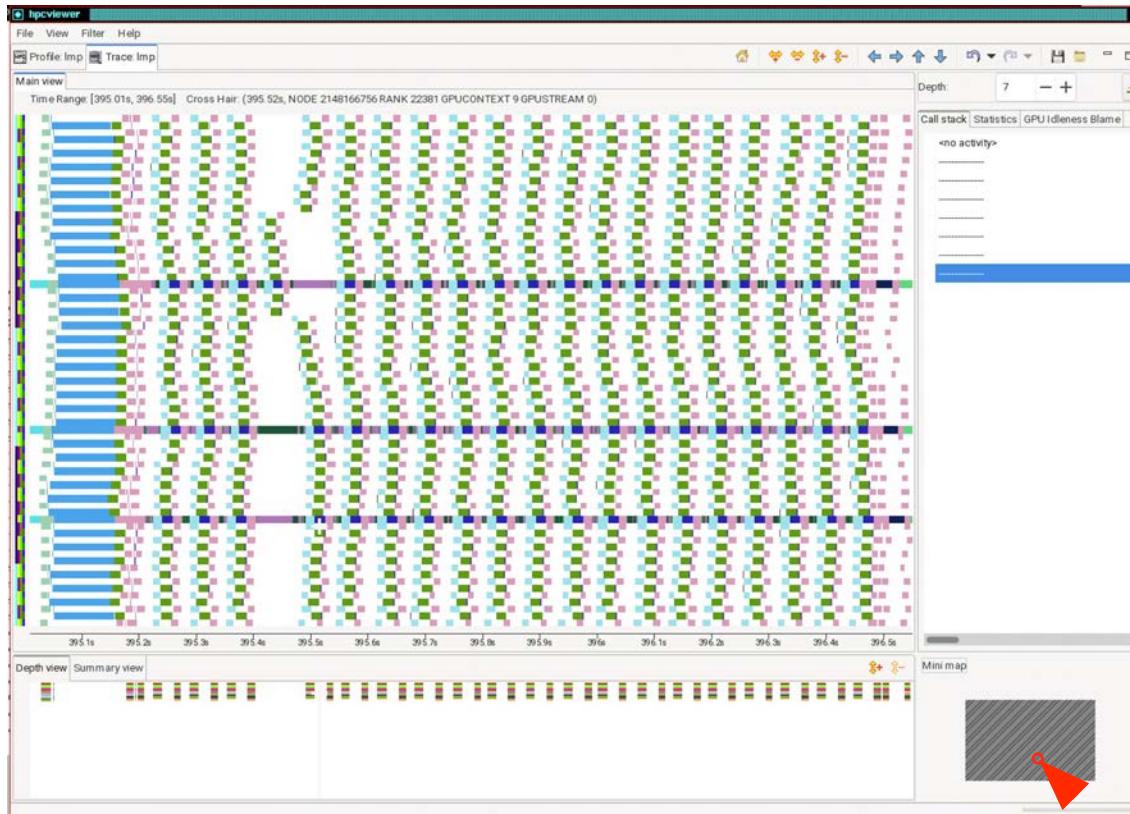
LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



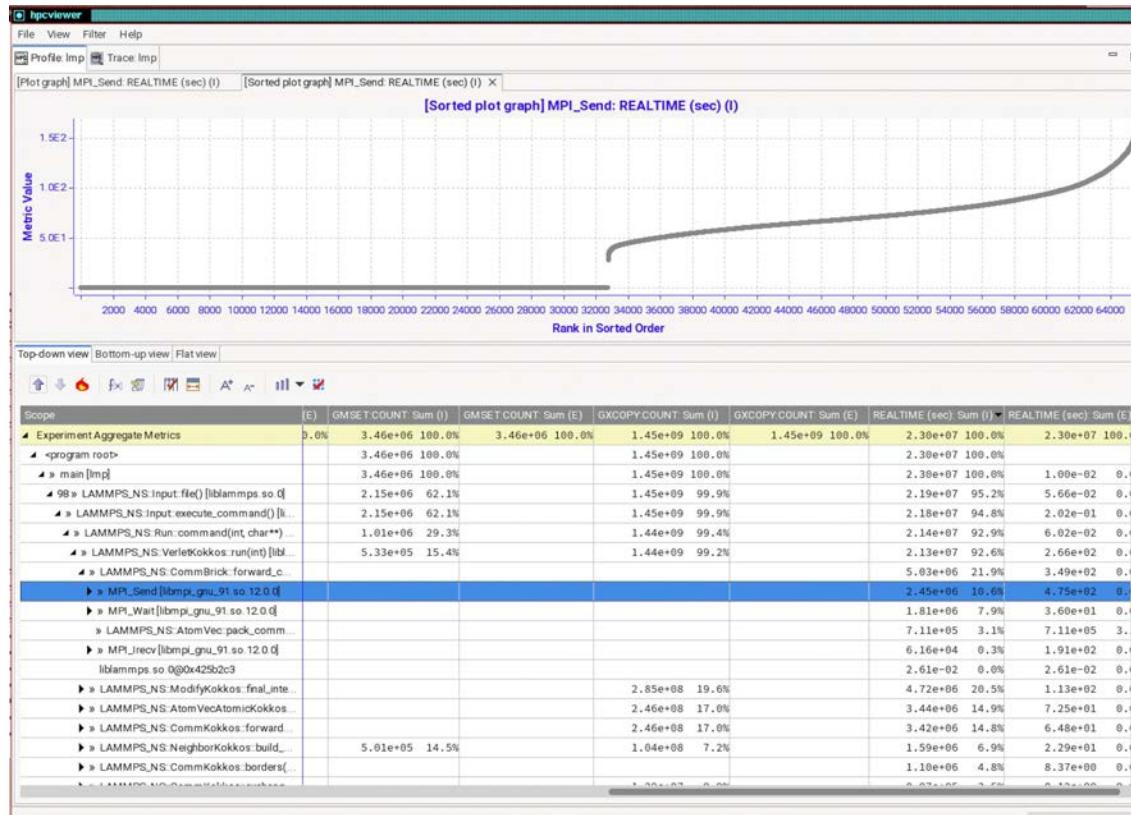
LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



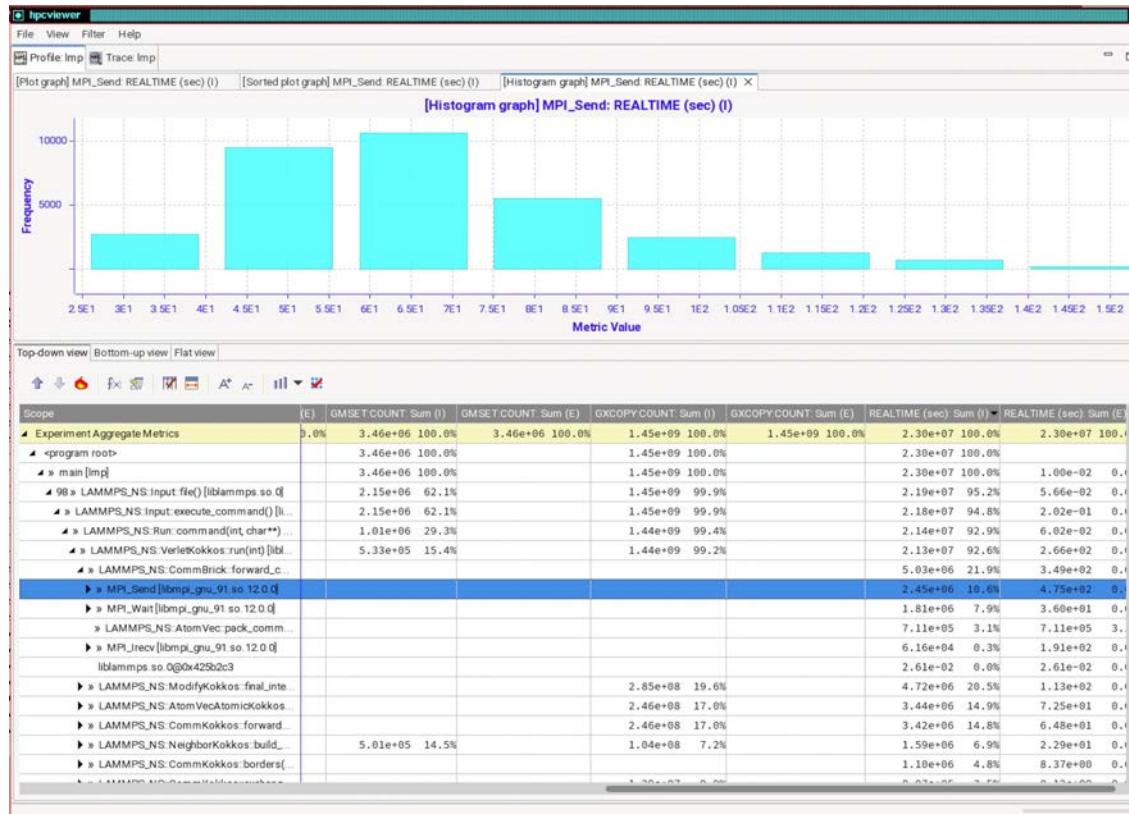
LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds

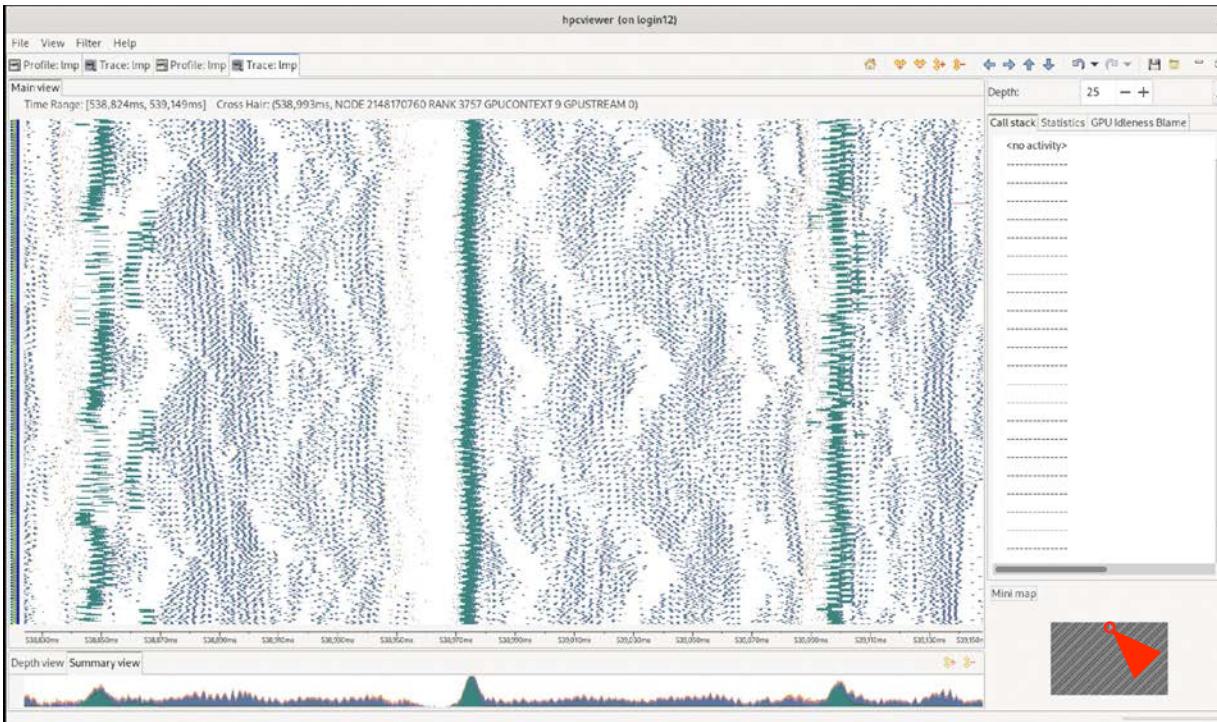


LAMMPS on Frontier: Executions with Kernel Duration of Milliseconds



LAMMPS on Frontier: 8K nodes, 64K MPI ranks + 64K GPU tiles

Kernel duration of microseconds

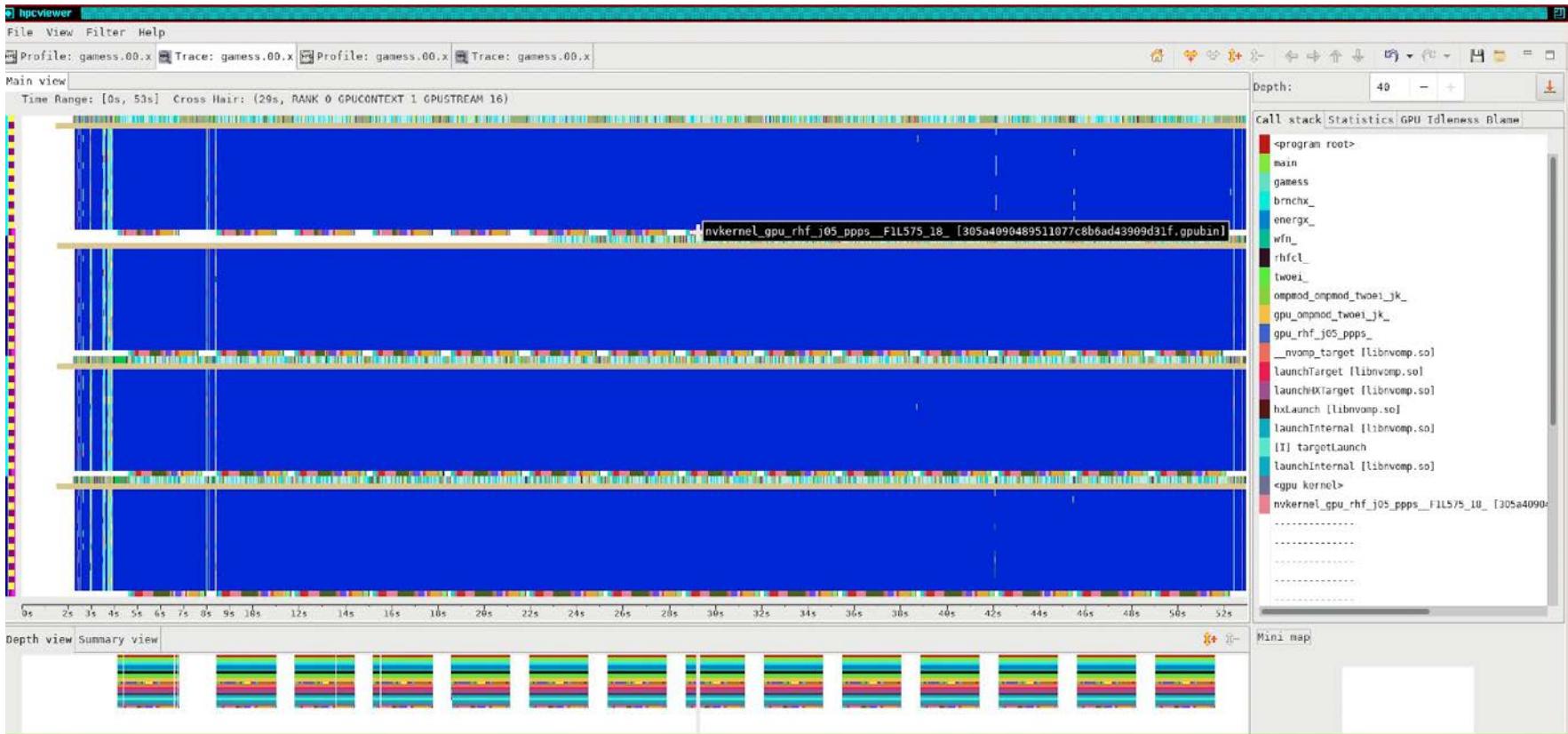


Case Study: GAMESS

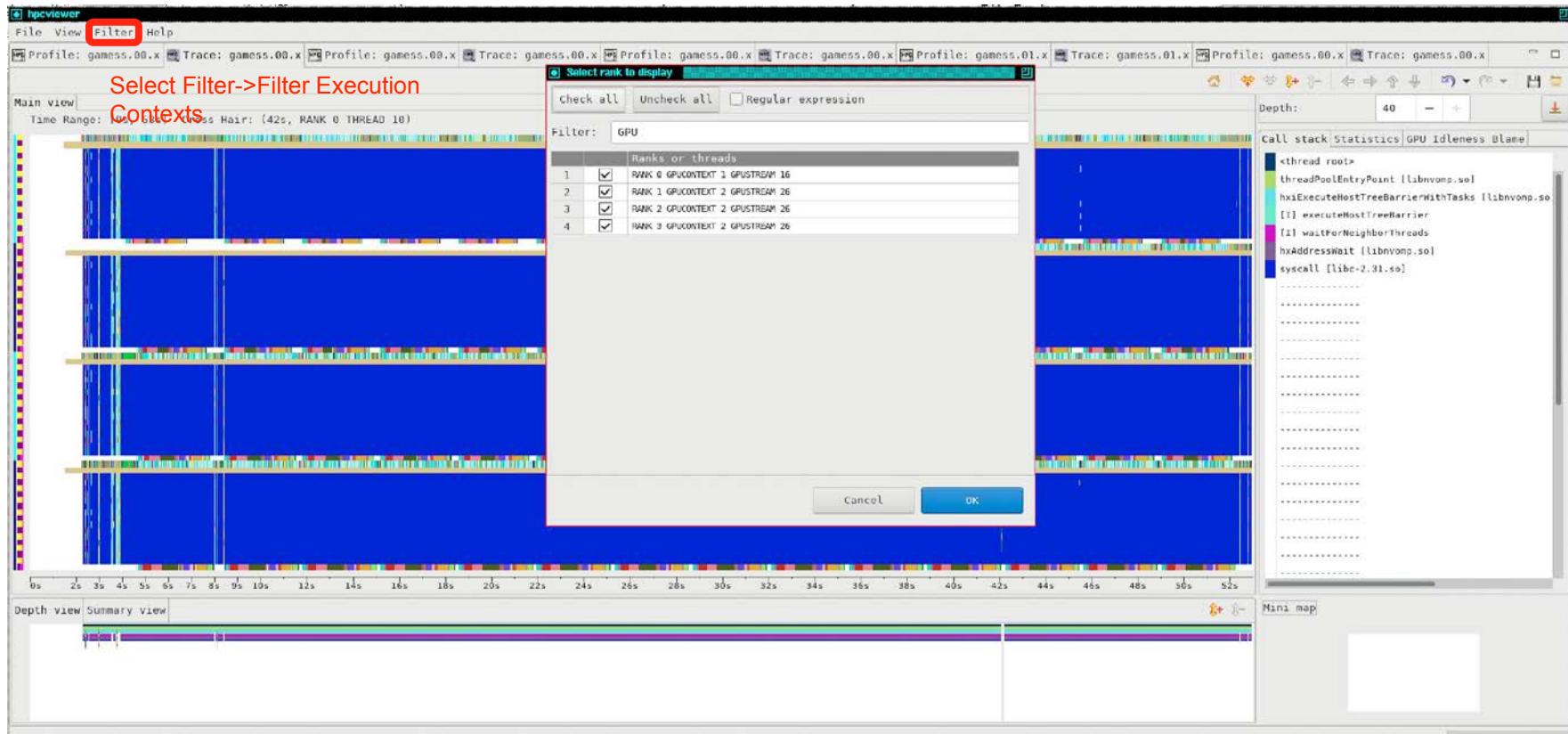
- General Atomic and Molecular Electronic Structure System (GAMESS)
 - general *ab initio* quantum chemistry package
- Calculates the energies, structures, and properties of a wide range of chemical systems
- Experiments
 - GPU-accelerated nodes at a prior Perlmutter hackathon
 - Single node with 4 GPUs
 - Five nodes with 20 GPUs

Perlmutter node at a glance
AMD Milan CPU
4 NVIDIA A100 GPUs
256 GB memory

Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



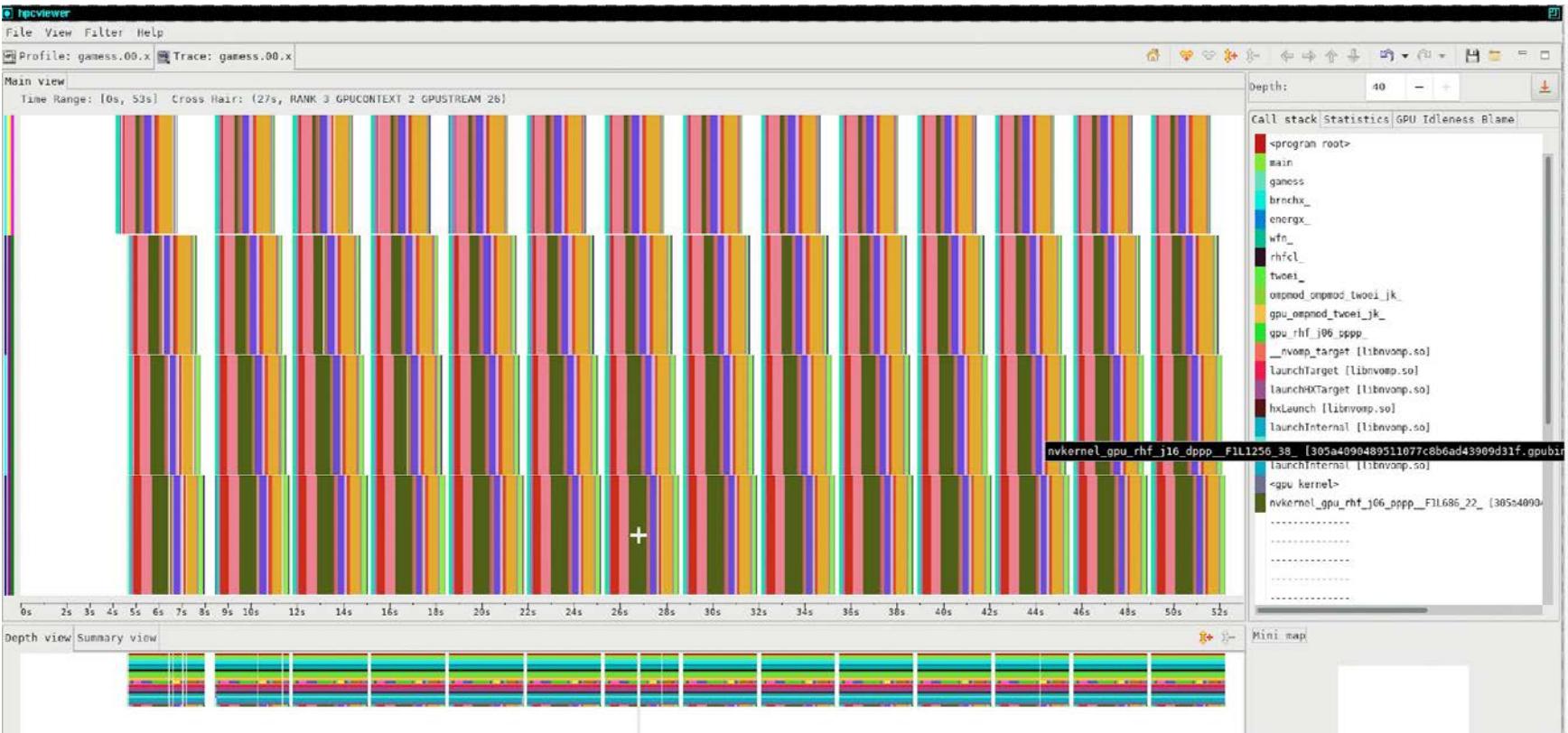
Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



GAMESS original

All CPU threads and GPU streams

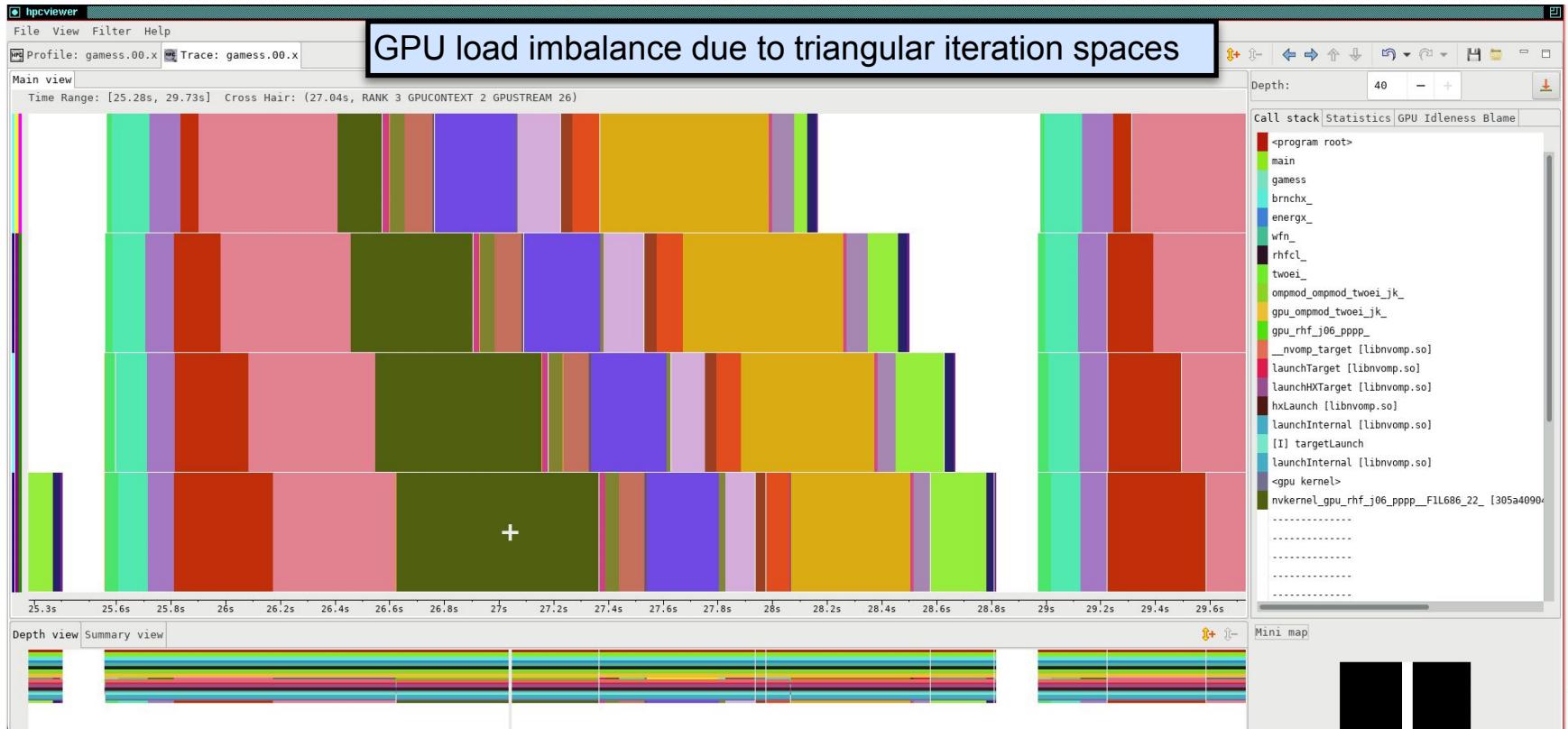
Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



GAMESS original

All GPU streams; whole execution

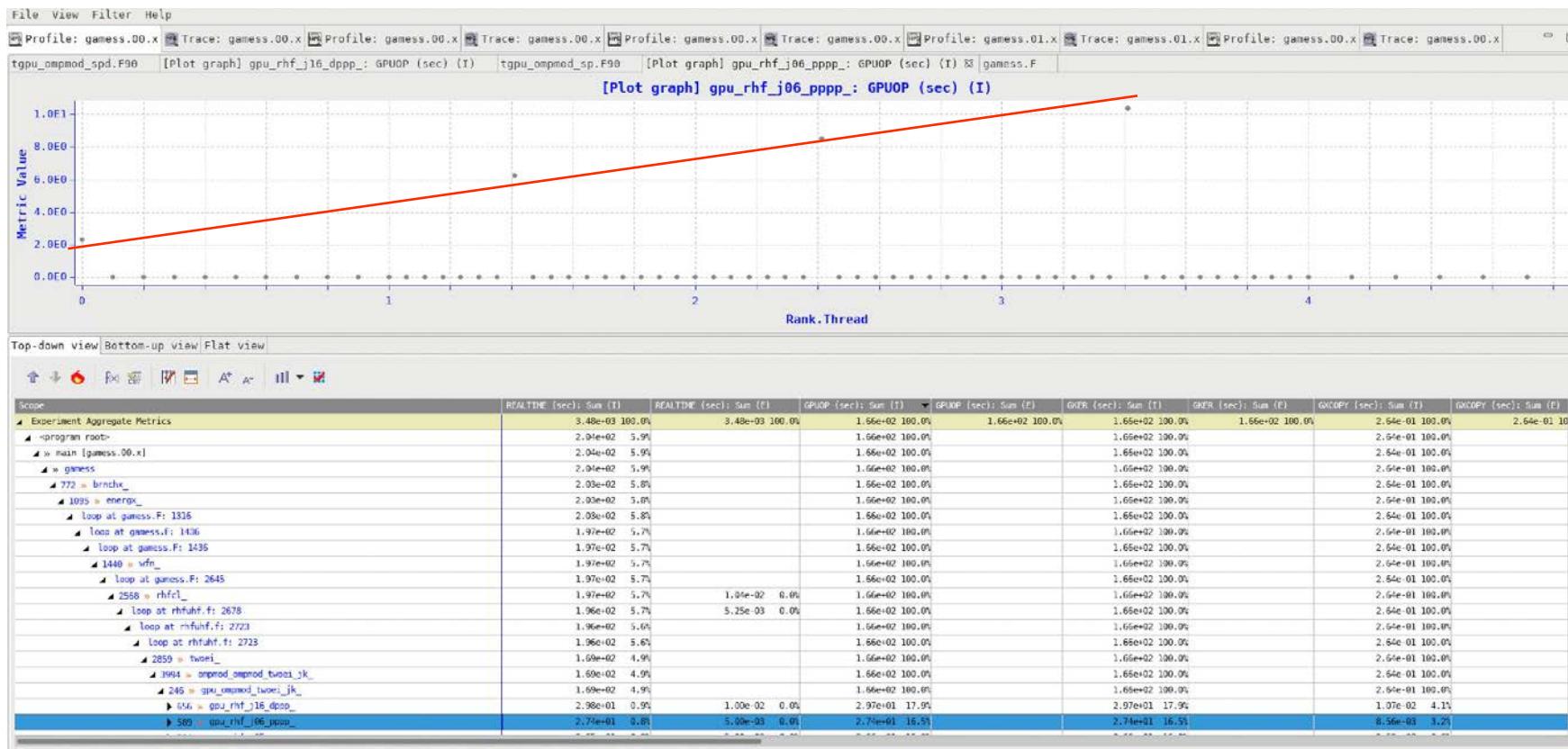
Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter



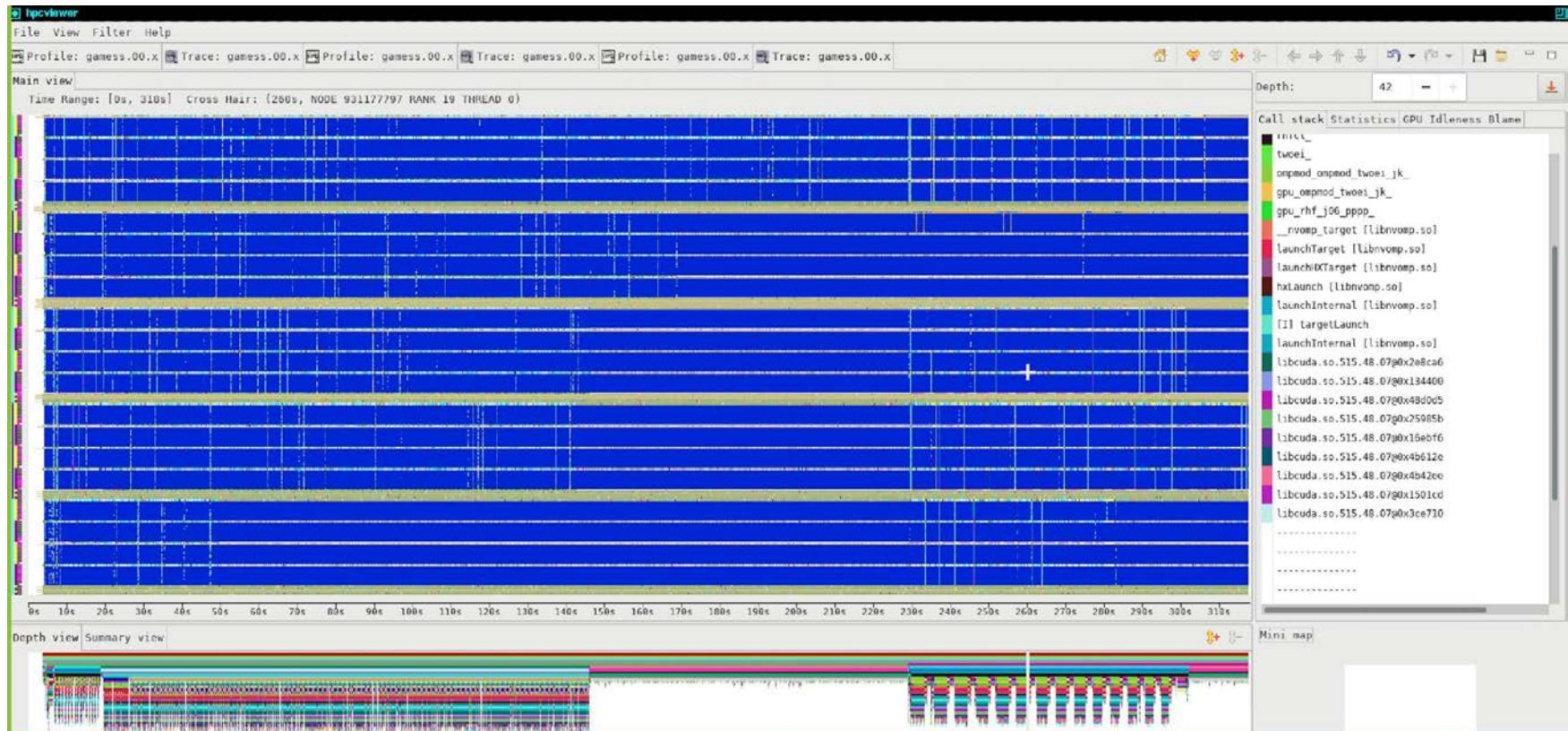
GAMESS original

GPU streams: 1 iteration

Time-centric Analysis: GAMESS 4 ranks, 4 GPUs on Perlmutter

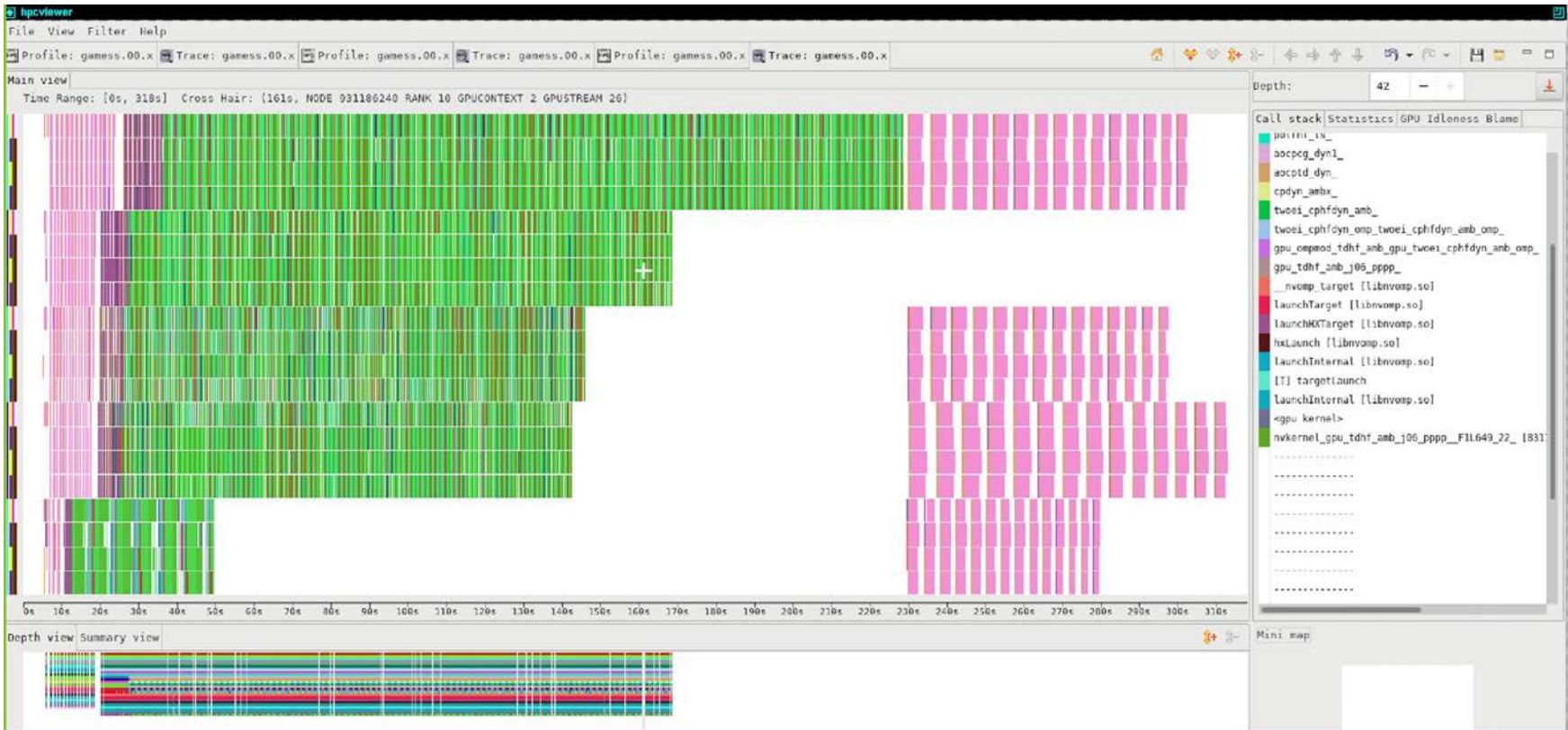


Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

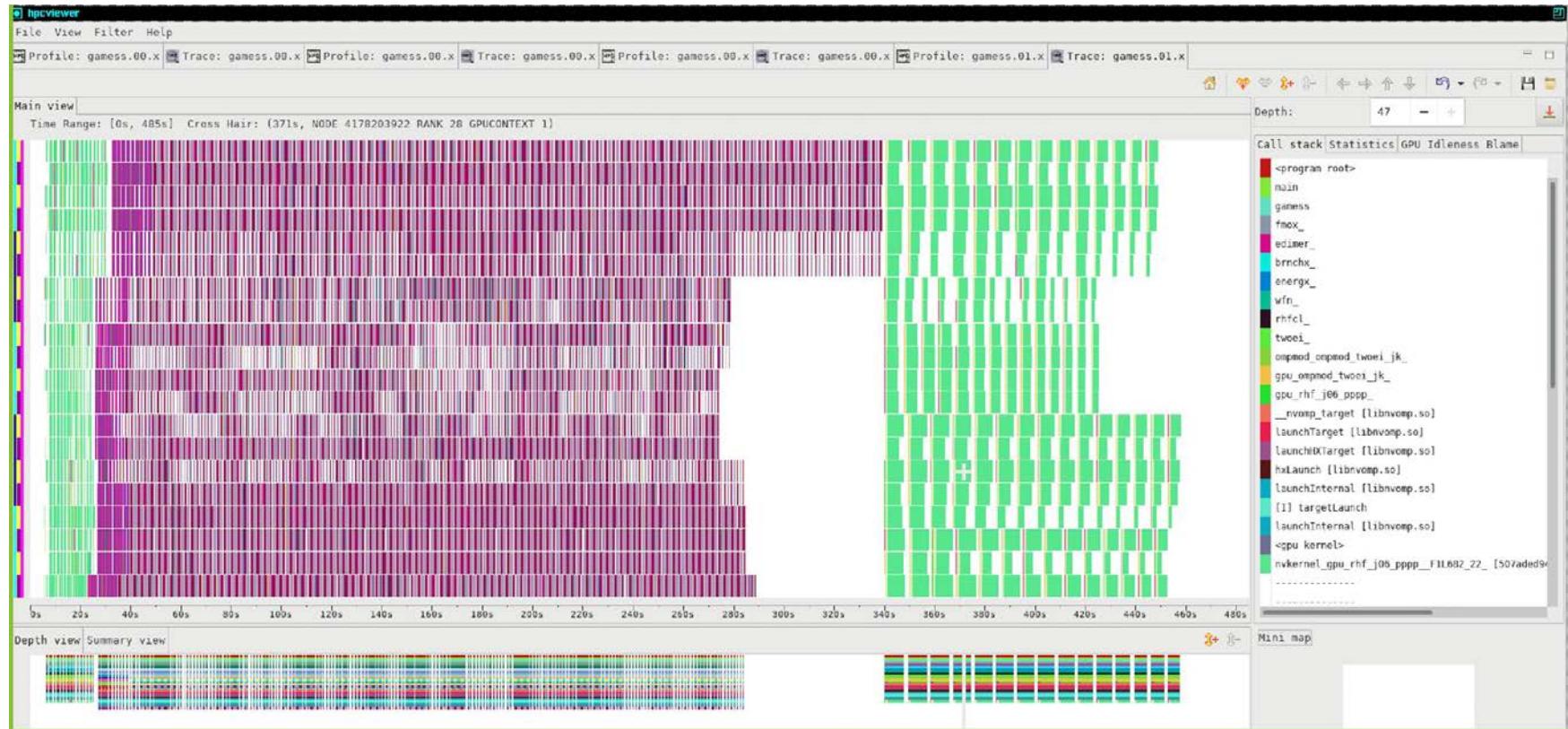


CPU Threads and GPU Streams

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

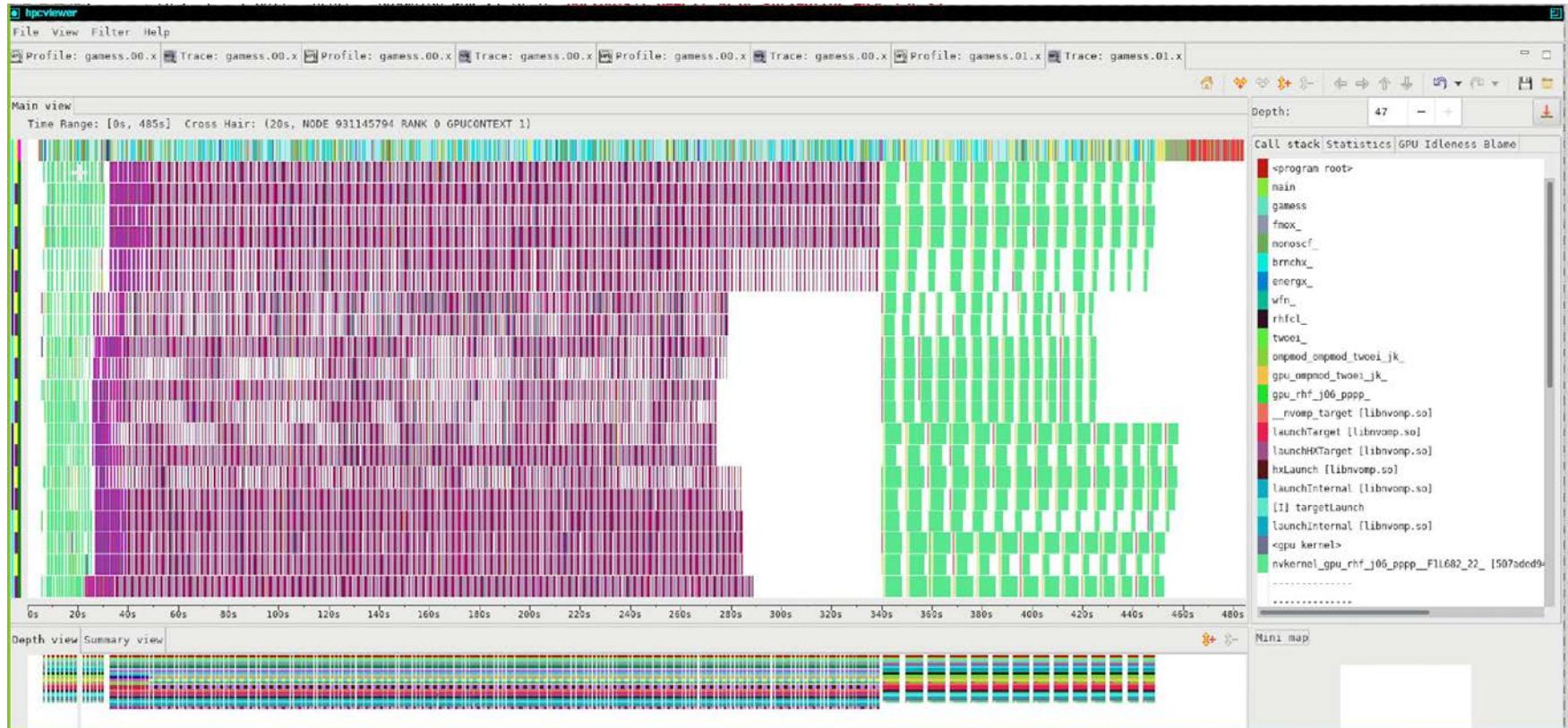


Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



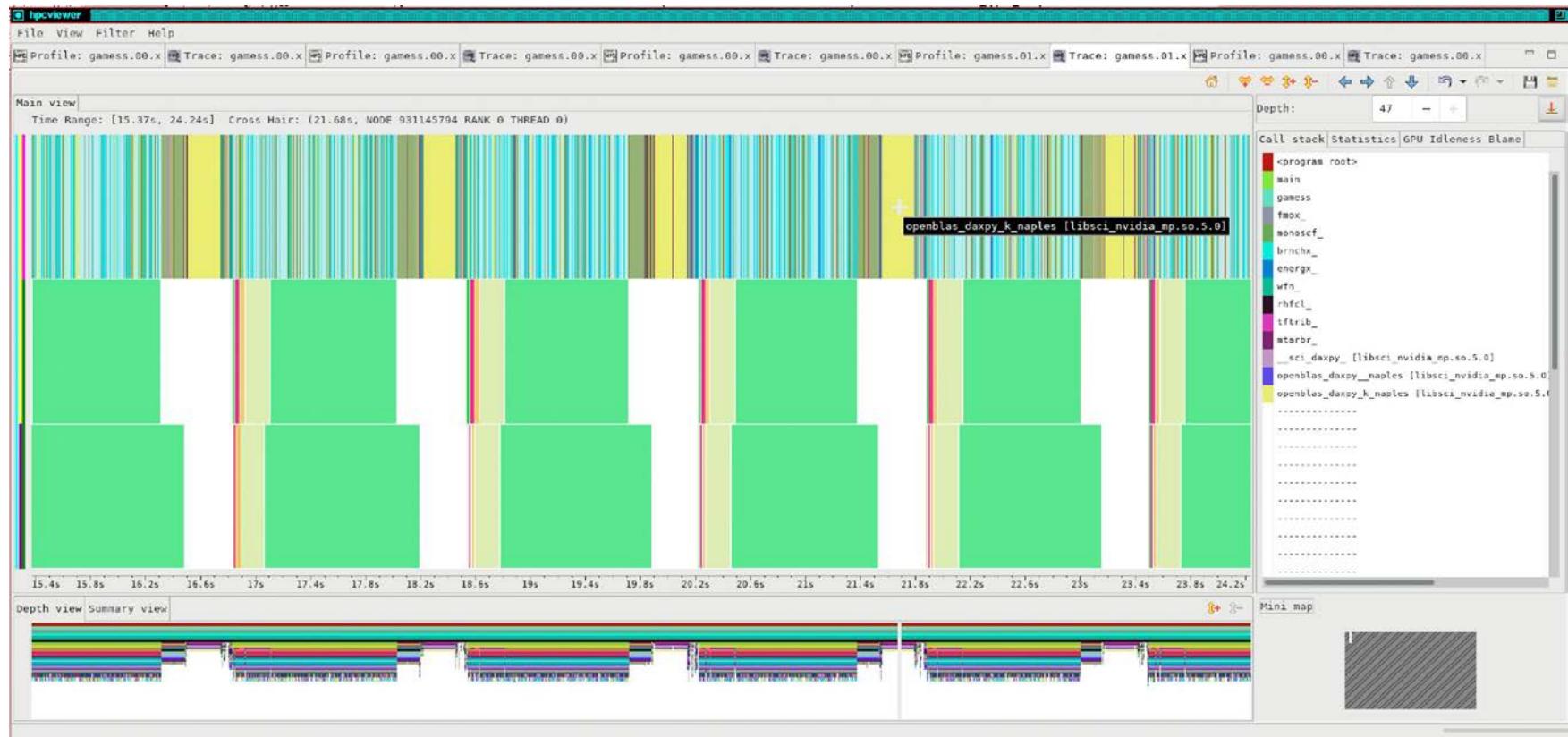
GAMESS improved with better manual distribution of work in input

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



GAMESS improved adding Rank 0 Thread 0 to GPU streams

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter



1 CPU Stream, 2 GPU Streams: 6 Iterations

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

hpviewer

File View Filter Help

Profile: gamess.00.x Trace: gamess.00.x Profile: gamess.00.x Trace: gamess.00.x Profile: gamess.00.x Trace: gamess.00.x Profile: gamess.01.x Trace: gamess.01.x

```
mthlib.f 33
1054 C
1055 C      END
1056 C*MODULE MTHLIB *DECK MTARBR
1056 SUBROUTINE MTARBR(A,NA,0,MB,AB,NAB,INCA)
1057 C
1058 USE MPI_LIB
1059 C
1060 IMPLICIT DOUBLE PRECISION(A-N,O-Z)
1061 C
1062 DIMENSION A(*),B(NA,NB),AB(NAB,MB)
1063 C
1064 PARAMETER (ZERO=0.0D+00)
1065 C
1066 C* 31 OCT 1979
1067 C*
1068 C*FUNCTION - TO MULTIPLY SYMMETRIC MATRIX A
1069 C*           TIMES RECTANGULAR MATRIX B AND GET RECTANGULAR MATRIX AB
1070 C*
1071 C*PARAMETERS
1072 C*   A - THE INPUT REAL SYMMETRIC MATRIX OF ORDER NA
1073 C*         STORED IN SYMMETRIC STAGE MODE.
1074 C*   B - THE INPUT REAL NA BY MB RECTANGULAR MATRIX
1075 C*   NA - THE ORDER OF MATRIX A
1076 C*   NB - THE COLUMN DIMENSION OF MATRIX B
1077 C*   MB - THE COLUMN DIMENSION OF MATRICES B AND AB
1078 C*   NAB - THE INPUT ROW DIMENSION OF MATRIX AB
1079 C*   INCA - ADDRESS DIFFERENCE OF TWO ADJACENT ELEMENTS OF A
1080 C*
1081 INC=INCA
1082 C
1083 C PROCESS DIAGONAL ELEMENTS OF INPUT MATRIX A
1084 C
1085 IJ=1-INC
1086 DO 120 I=1,NA
1087   IJ=IJ+INC
1088   AIJ=A(IJ)
1089   DO 110 K=1,MB
1090     AB(I,K)=AIJ+B(I,K)
1091 110 CONTINUE
1092 120 CONTINUE
1093 IF(NA.EQ.1) RETURN
1094 C
1095 C PROCESS OFF-DIAGONAL ELEMENTS OF INPUT MATRIX A
1096 C
1097 IJ=1-INC
1098 DO 130 I=2,NA
1099   IJ=IJ-INC
1100   IM1=I-1
1101   DO 140 J=1,IM1
1102     IJ=IJ+INC
1103     AIJ=A(IJ)
1104     IF(AIJ.EQ.ZERO) GO TO 140
1105     CALL DAXPY(MB,AIJ,B(I,J),NA,AB(J,1),NAB)
1106     CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)
1107 140 CONTINUE
1108 150 CONTINUE
1109 RETURN
1110 END
```

Top down view|Bottom up view|Flat view

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

```
hpviewer
File View Filter Help
Profile: gamess.00.x Trace: gamess.00.x Profile: gamess.00.x Trace: gamess.00.x Profile: gamess.00.x Trace: gamess.00.x Profile: gamess.01.x Trace: gamess.01.x
mthlib.f 0x
1054 C
1055 END
1056 C*MODULE MTHLIB *DECK MTARBR
1056 SUBROUTINE MTARBR(A,NA,0,MB,AB,NAB,INC)
1057 C
1058 USE MPI_LIB
1059 C
1060 IMPLICIT DOUBLE PRECISION(A-N,O-Z)
1061 C
1062 DIMENSION A(*),B(NA,NB),AB(NAB,MB)
1063 C
1064 PARAMETER (ZERO=0.0D+00)
1065 C
1066 C* 31 OCT 1979
1067 C*
1068 C*FUNCTION - TO MULTIPLY SYMMETRIC MATRIX A
1069 C*           TIMES RECTANGULAR MATRIX B AND GET RECTANGULAR MATRIX AB
1070 C*
1071 C*PARAMETERS
1072 C*   A - THE INPUT REAL SYMMETRIC MATRIX OF ORDER NA
1073 C*         STORED IN SYMMETRIC STAGE MODE.
1074 C*   B - THE INPUT REAL NA BY MB RECTANGULAR MATRIX
1075 C*   NA - THE ORDER OF MATRIX A
1076 C*   NB - THE COLUMN DIMENSION OF MATRICES B AND AB
1077 C*   AB - THE OUTPUT PRODUCT NA BY MB MATRIX
1078 C*   NAB - THE INPUT ROW DIMENSION OF MATRIX AB
1079 C*   INC - ADDRESS DIFFERENCE OF TWO ADJACENT ELEMENTS OF A
1080 C*
1081 INC=INC
1082 C
1083 C PROCESS DIAGONAL ELEMENTS OF INPUT MATRIX A
1084 C
1085 IJ=1-INC
1086 DO 120 I=1,NA
1087   IJ=IJ+INC
1088   AIJ=A(IJ)
1089   DO 110 K=1,MB
1090     AB(I,K)=AIJ*B(I,K)
1091 110 CONTINUE
1092 120 CONTINUE
1093  IF(NA.EQ.1) RETURN
1094 C
1095 C
1096 IJ=1-INC
1097 DO 130 I=2,NA
1098   IJ=IJ+INC
1099   IM1=I-1
1100   IM1=IM1-INC
1101   DO 140 J=1,IM1
1102     IJ=IJ+INC
1103     AIJ=A(IJ)
1104     IF(AIJ.EQ.ZERO) GO TO 140
1105     CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB)
1106     CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)
1107 140 CONTINUE
1108 150 CONTINUE
1109  RETURN
1110 END
```

Time-centric Analysis: GAMESS 5 nodes, 40 ranks, 20 GPUs on Perlmutter

```
hpviewer
1096 C
1097      IJ=1-INC
1098      DO 150 I=2,NA
1099          IJ=IJ+INC
1100          IM1=I-1
1101          DO 140 J=1,IM1
1102              IJ=IJ+INC
1103              AIJ=A(IJ)
1104              IF(AIJ.EQ.ZERO) GO TO 140
1105                  CALL DAXPY(MB,AIJ,B(I,1),NA,AB(J,1),NAB)
1106                  CALL DAXPY(MB,AIJ,B(J,1),NA,AB(I,1),NAB)
1107 140      CONTINUE
1108 150      CONTINUE
1109      RETURN
1110      END
```

Top down view | Bottom up view | Flat view



Leadership
Computing Facility

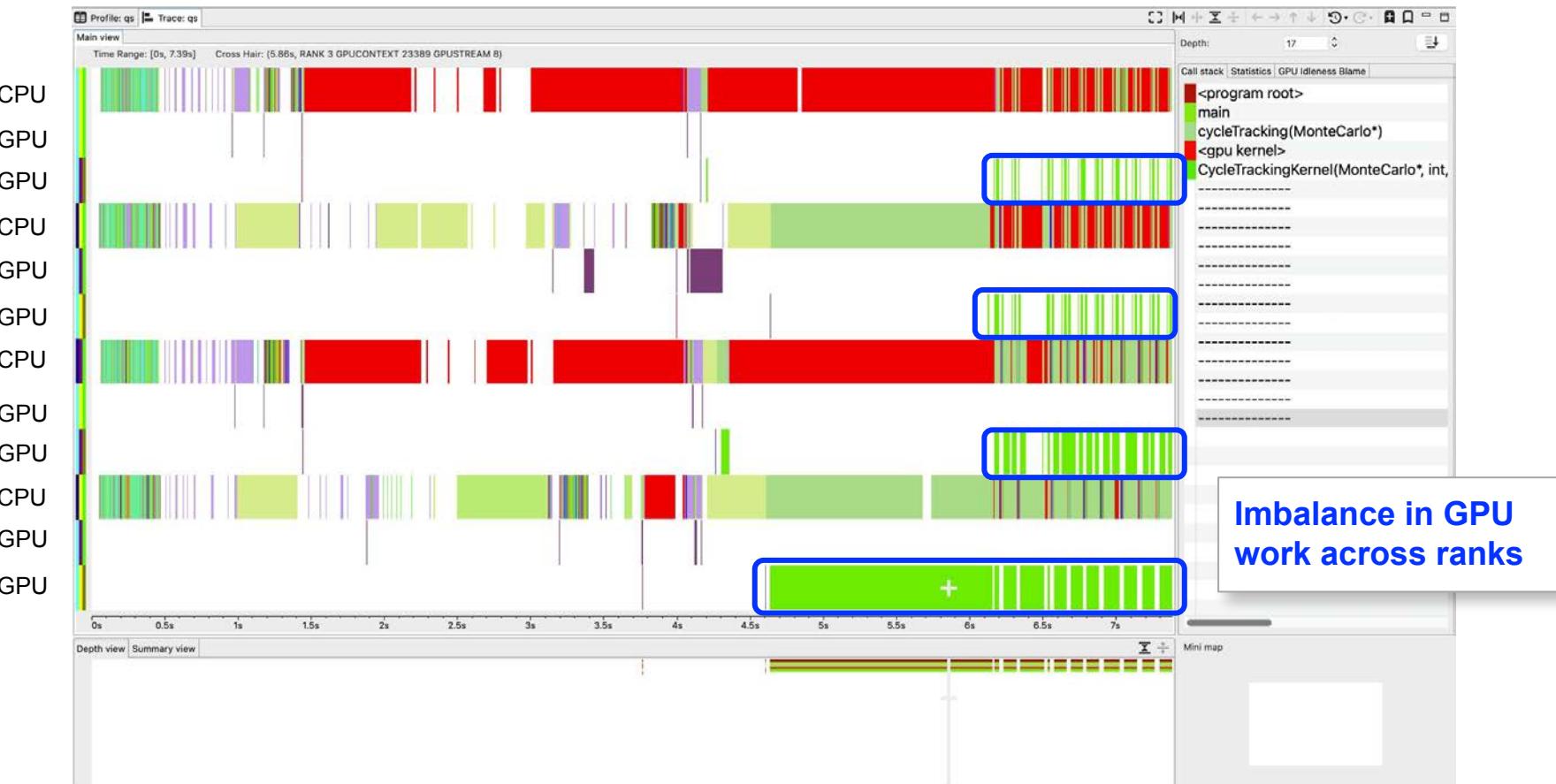
Today's Agenda

- Introduce HPCToolkit tools and workflow
- Illustrate HPCToolkit's use with some case studies
- Live demos
 - profiling and tracing
 - instruction-based performance metrics
- Instruction-level performance monitoring
 - explaining instruction-level performance
 - improving analysis and attribution of PC samples
 - automating analysis of GPU bottlenecks
- Discussion about needs, problems, and suggestions

Case Study: Quicksilver

- Proxy application that represents some elements of LLNL's Mercury code
- Solves a simplified dynamic Monte Carlo particle transport problem
 - Attempts to replicate memory access patterns, communication patterns, and branching or divergence of Mercury for problems using multigroup cross sections
- Parallelization: MPI, OpenMP, CUDA, and HIP
- Performance Issues
 - load imbalance (for canned example)
 - latency bound table look-ups
 - a highly branchy/divergent code path
 - poor vectorization potential

Quicksilver: Trace view



Quicksilver: Detailed analysis within a Kernel using PC Sampling

The screenshot displays the hpcviewer application interface, which integrates a code editor and a performance analysis tool.

Code Editor: The top half shows the source code for `CollisionEvent.cc`. The code implements a function to find a reaction cross-section based on a unique number and energy group. It includes assertions to ensure the selected isotope is valid.

```
main.cc CollisionEvent.cc X
69 int uniqueNumber = monteCarlo-> materialDatabase-> mat[globalMatIndex].iso[isoIndex].gid;
70 int numReacts = monteCarlo->_nuclearData->getNumberOfReactions(uniqueNumber);
71 for (int reactIndex = 0; reactIndex < numReacts; reactIndex++)
72 {
73     currentCrossSection = macroscopicCrossSection(monteCarlo, reactIndex, mc_particle.domain, mc_particle.cell,
74             isoIndex, mc_particle.energy_group);
75     if (currentCrossSection < 0)
76     {
77         selectedIso = isoIndex;
78         selectedUniqueNumber = uniqueNumber;
79         selectedReact = reactIndex;
80         break;
81     }
82 }
83 }
84 assert(selectedIso != -1);
85
```

Performance Analysis: The bottom half of the interface is a table showing performance metrics for various code scopes. The columns include:

Scope	INS: Sum (I)	GINS: Sum (E)	GINS:STL_ANY: Sum (I)	GINS:STL_ANY: Sum (E)	GINS:STL_IFET: Sum (I)	GINS:STL_IFET: Sum (E)	GINS:STL_IDEP:
14 » [I] cudaLaunchKernel<char>	1.30e+11 100.0%		1.19e+11 100.0%		5.27e+09 100.0%		9.34e+04
211 » cudaLaunchKernel [qs]	1.30e+11 100.0%		1.19e+11 100.0%		5.27e+09 100.0%		9.34e+04
»gpu kernel»			1.19e+11 100.0%		5.27e+09 100.0%		9.34e+04
» CycleTrackingKernel(MonteCarlo*, int, ParticleVault*, ParticleVault*, MC_Particle&, int, P...	1.30e+11 100.0% 4.08e+07 0.0%		1.19e+11 100.0% 3.62e+07 0.0%		5.27e+09 100.0% 2.11e+07 0.4%		9.34e+04
132 » CycleTrackingGuts(MonteCarlo*, int, ParticleVault*, ParticleVault*, MC_Particle&, int, P...	1.30e+11 100.0% 9.03e+09 7.0%		1.19e+11 100.0% 9.01e+09 7.6%		5.24e+09 99.5% 8.98e+06 0.2%		9.32e+04
26 » [I] CycleTrackingFunction(MonteCarlo*, MC_Particle&, int, P...	1.30e+10 64.4% 4.12e+08 0.3%		7.25e+10 61.1% 3.65e+08 0.3%		5.21e+09 98.9% 1.02e+08 1.9%		9.25e+04
loop at CycleTracking.cc: 118	1.35e+10 64.3% 3.76e+08 0.3%		7.25e+10 61.1% 3.34e+08 0.3%		5.21e+09 98.8% 9.90e+07 1.9%		9.24e+04
63 » CollisionEvent(MonteCarlo*, MC_Particle&, unsigned int) [...]	1.20e+10 40.1% 4.99e+09 3.8%		4.44e+10 37.4% 4.02e+09 3.4%		3.05e+09 73.1% 4.89e+08 9.3%		6.37e+04
loop at CollisionEvent.cc: 67	1.09e+10 31.5% 8.15e+08 0.6%		3.42e+10 28.8% 6.54e+08 0.6%		3.54e+09 67.1% 1.27e+08 2.4%		5.67e+04
loop at CollisionEvent.cc: 71	1.05e+10 29.6% 2.70e+09 2.1%		3.22e+10 27.1% 2.06e+09 1.7%		3.27e+09 62.0% 2.28e+08 4.3%		5.33e+04
73 » macroscopicCrossSection(MonteCarlo*, int, int, int, i...	1.58e+10 27.5% 1.22e+10 9.4%		3.01e+10 25.4% 9.85e+09 8.3%		3.04e+09 57.7% 1.79e+09 33.9%		4.80e+04
41 » NuclearData::getReactionCrossSection(unsigned int, u...	1.09e+10 16.1% 1.09e+10 8.4%		1.79e+10 15.1% 9.42e+09 7.9%		1.26e+09 23.8% 6.68e+08 12.7%		2.19e+04
253 » [I] NuclearDataReaction::getCrossSection(unsigned ...	1.09e+09 5.3% 3.77e+09 2.9%		5.86e+09 4.9% 3.32e+09 2.8%		2.25e+08 4.3% 8.24e+07 1.6%		8.06e+04
NuclearData.cc: 253	1.28e+09 4.8% 6.28e+09 4.8%		5.66e+09 4.8% 5.66e+09 4.8%		4.76e+08 9.0% 4.76e+08 9.0%		6.11e+04
NuclearData.cc: 251	1.85e+09 1.4% 1.85e+09 1.4%		1.64e+09 1.4% 1.64e+09 1.4%		4.76e+08 9.0% 4.76e+08 9.0%		2.47e+04
NuclearData.cc: 248	1.61e+09 1.2% 1.61e+09 1.2%		1.18e+09 1.0% 1.18e+09 1.0%		8.12e+07 1.5% 8.12e+07 1.5%		3.62e+04
252 » [I] qs_vector<NuclearDataSpecies>::operator[](int)	1.29e+09 1.0% 1.29e+09 1.0%		1.14e+09 1.0% 1.14e+09 1.0%		7.37e+04 0.0% 7.37e+04 0.0%		1.24e+04
NuclearData.cc: 250	1.12e+09 0.9% 1.12e+09 0.9%		9.48e+08 0.8% 9.48e+08 0.8%		3.44e+05 0.0% 3.44e+05 0.0%		2.50e+04
252 » [I] qs_vector<NuclearDataReaction>::size() const	1.41e+08 0.7% 9.41e+08 0.7%		8.17e+08 0.7% 8.17e+08 0.7%				4.63e+04
73 » [I] ...	1.41e+08 0.3% 2.15e+08 0.3%		7.42e+08 0.3% 7.42e+08 0.3%				3.27e+04

Quicksilver: Attribution to Code within a Kernel

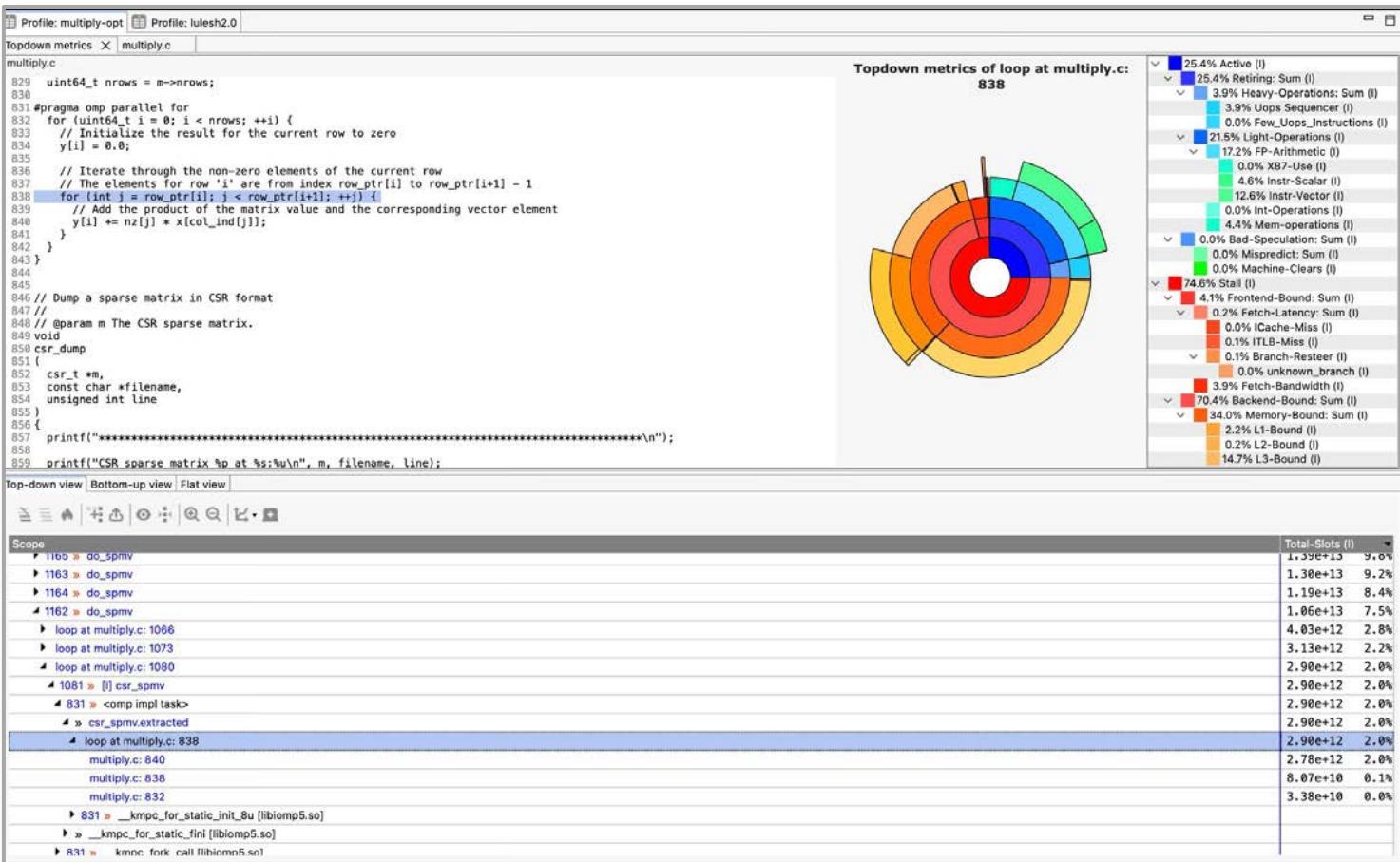
```
Scope
  ▾ 14 » [I] cudaLaunchKernel<char>
    ▾ 211 » cudaLaunchKernel [qs]
      ▾ » <gpu kernel>
        ▾ » CycleTrackingKernel(MonteCarlo*, int, ParticleVault*, ParticleVau...
        ▾ 132 » CycleTrackingGuts(MonteCarlo*, int, ParticleVault*, Particle...
        ▾ 26 » [I] CycleTrackingFunction(MonteCarlo*, MC_Particle&, int, P...
          ▾ loop at CycleTracking.cc: 118
          ▾ 63 » CollisionEvent(MonteCarlo*, MC_Particle&, unsigned int) [...]
            ▾ loop at CollisionEvent.cc: 67
            ▾ loop at CollisionEvent.cc: 71
              ▾ 73 » macroscopicCrossSection(MonteCarlo*, int, int, int, i...
                ▾ 41 » NuclearData::getReactionCrossSection(unsigned int, u...
                  ▾ 253 » [I] NuclearDataReaction::getCrossSection(unsigned ...
                    NuclearData.cc: 253
                    NuclearData.cc: 251
                    NuclearData.cc: 248
                  ▾ 252 » [I] qs_vector<NuclearDataSpecies>::operator[](int)
                    NuclearData.cc: 252
                  ▾ 252 » [I] qs_vector<NuclearDataReaction>::size() const
                  ▾ 252 » [I] ...
```

Today's Agenda

- Introduce HPCToolkit tools and workflow
- Illustrate HPCToolkit's use with some case studies
- Live demos
 - profiling and tracing
 - instruction-based performance metrics
- Instruction-level performance monitoring
 - explaining instruction-level performance
 - improving analysis and attribution of PC samples
 - automating analysis of GPU bottlenecks
- Discussion about needs, problems, and suggestions

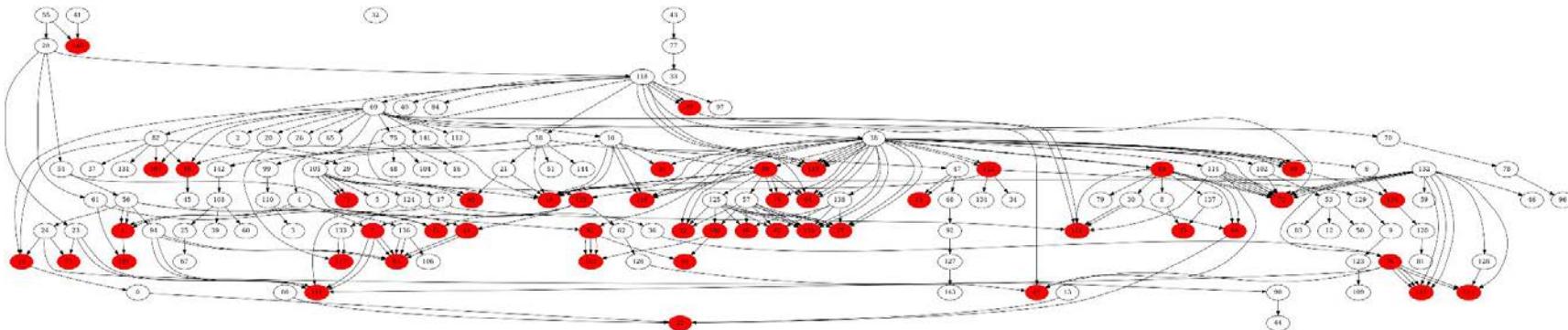
Work in Progress: Present GPU Metrics using a Donut Graph

- Figure shows forthcoming top-down display of CPU metrics
- Plan similar top-down display of GPU metrics
 - Issues
 - Exposed stalls
 - Memory
 - Pipeline
 - Ifetch
 - ...
 - Hidden stalls



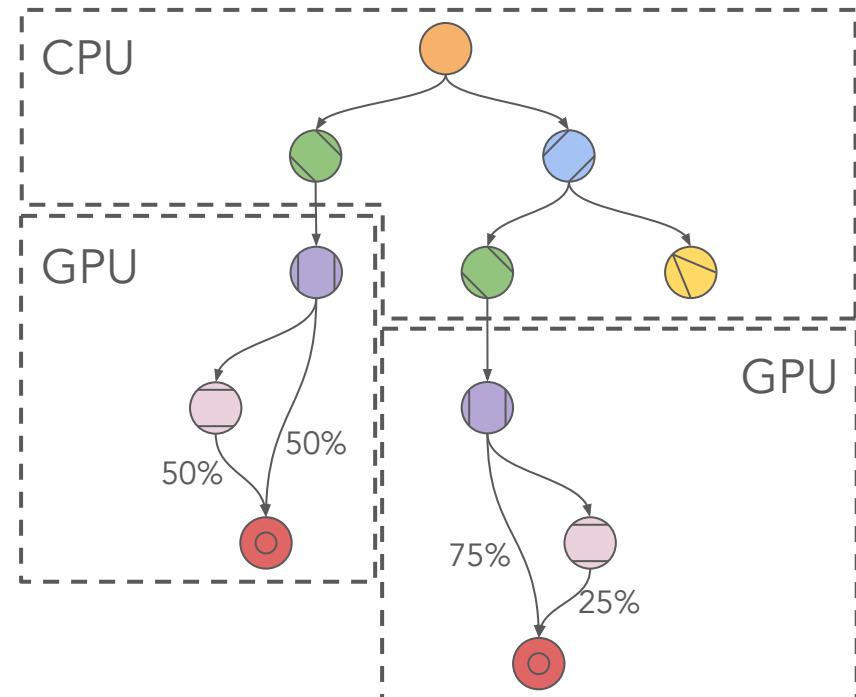
WIP: Enabling Instruction-level Metrics for Complex Kernels

- Calling context is great for visualization, but slow to analyze for complex GPU kernels!
 - Every plausible calling context must be expanded to attribute performance
 - Example: Quicksilver has one kernel with 145 separate GPU functions
 - Call graph below, **red** nodes have multiple plausible calling contexts
 - Sometimes >100,000 calling contexts for a single function, e.g. `cuda_div`



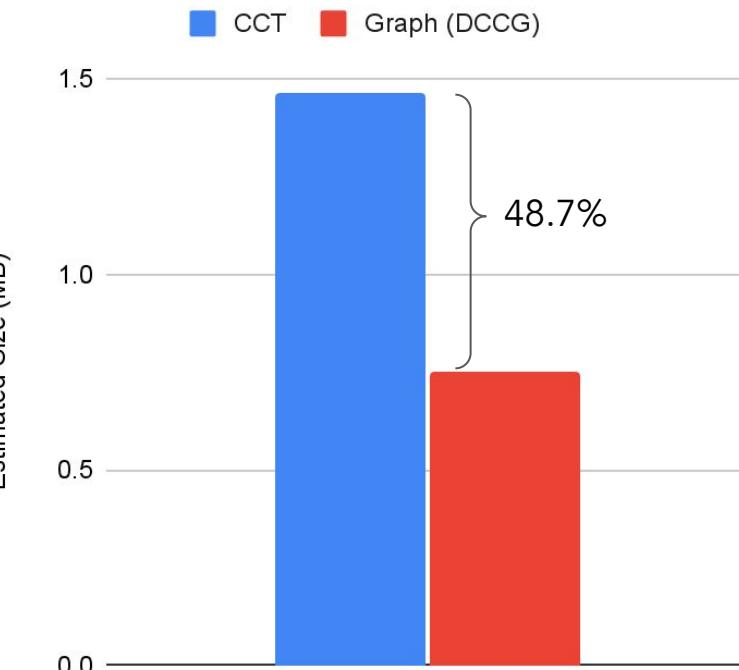
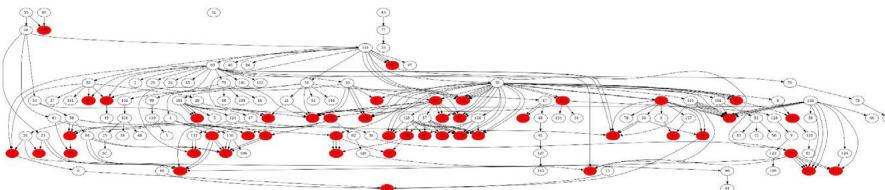
WIP: Enabling Instruction-level Metrics for Complex Kernels

- Next generation: Graph-based calling contexts
 - Performance is attributed to nodes
 - Distribution factors recorded on edges
- Efficient representation for GPU code
 - Avoid reconstruction of duplicate contexts
 - Match GPU measurement capabilities
- Clean model for both CPU and GPU performance
 - Not a hybrid model, one graph handles all

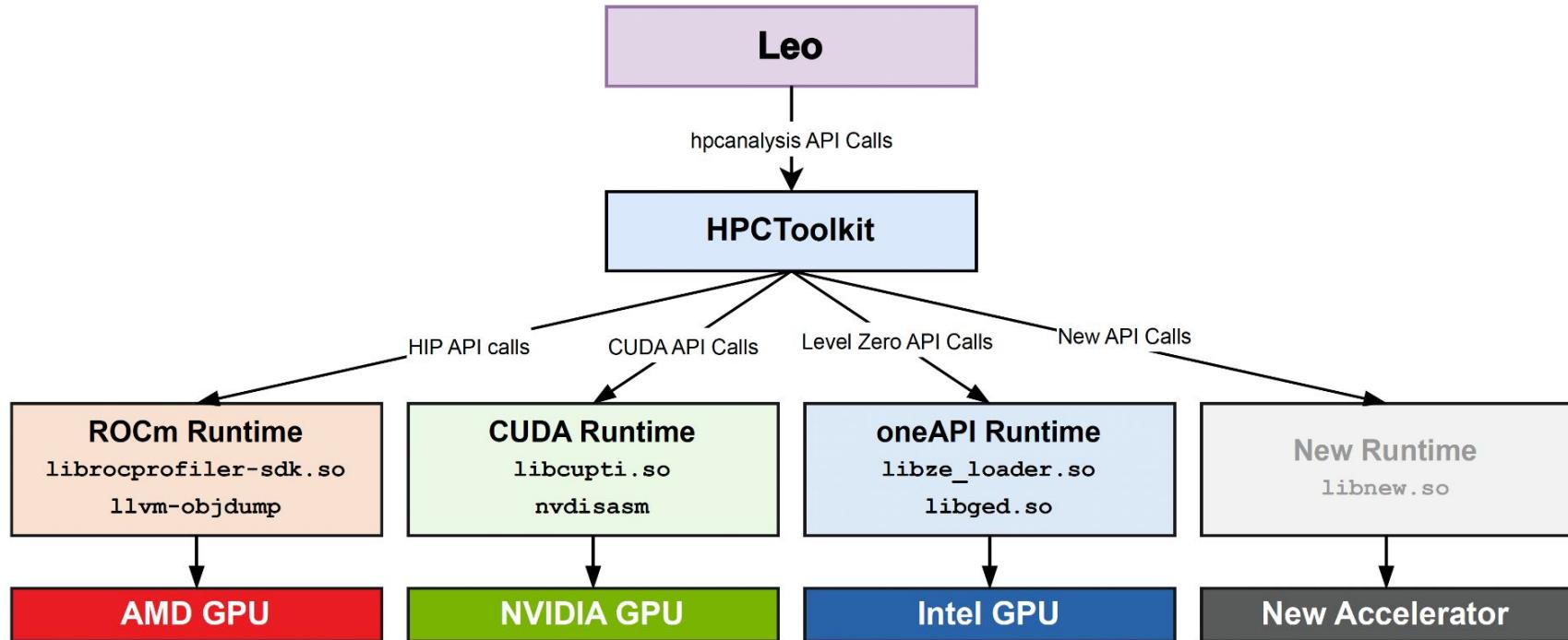


WIP: Enabling Instruction-level Metrics for Complex Kernels

- Case study: Quicksilver
 - 409 unique calling contexts
 - 145 graph nodes (1 per GPU function)
- Preliminary results
 - 48% estimated size reduction
 - 1.3x potential speed up



WIP: Leo - Platform-independent analysis with PC Sampling



WIP: Leo - Platform-independent analysis with PC Sampling

- GPU PC sampling collects a wealth of information
 - Stall reasons (AMD, Intel, NVIDIA)
 - Hidden stalls (NVIDIA, AMD)
 - Register utilization (NVIDIA, AMD)
 - Compute unit utilization (AMD)
 - Pipeline utilization
 - Wavefront occupancy
 - Thread utilization (divergence)
- Leo pinpoints and quantifies root causes of GPU stalls
 - Ranks code locations suffering from GPU instruction stalls
 - Dumps machine code into assembly code
 - Performs dataflow analysis to trace stalls back to potential root causes

WIP: Leo - Sample Report: Analysis of RajaPerfSuite

Database: /data/per-kernel/Algorithm_REDUCE_SUM/amd/hpctoolkit-raja-perf.exe-database
Measurements: /data/per-kernel/Algorithm_REDUCE_SUM/amd/hpctoolkit-raja-perf.exe-measurements

PROGRAM TOTALS

```
Total Execution Time: 0.0037s
Total Stall Cycles: 1,873,805,312
Total GPU Cycles: 1,946,157,056
Overall Stall Ratio: 96.3%
Kernels Analyzed: 1
Kernels Skipped: 0
```

TOP 1 KERNELS BY STALL CYCLES

#	Kernel	Time (s)	Stall Cycles	Stall %	Occupancy	Sample Rate
1	.text	0.0020	343,932,928	97.0%	30% (vgpr)	181.3G/s / 28%

KERNEL #1: .text
GPU Binary: c59f0354ed847c14b96a717c2709e313.gpubin
Occupancy: 30% (12/40 waves/CU, limited by vgpr) [VGPRs=40, SGPRs=50, LDS=64B]

Leo GPU Performance Analysis

WARNING: Very low profile coverage: 66/15359 (0.4%)

Kernel: .text

Architecture: AMD MI300

Total Stall Cycles: 423,624,704

STALL ANALYSIS (PC Sampling → Back-slicing → Root Cause)

Stall Location	Stall Opcode	Root Cause Location	Root Opcode	Cycles	% Total	Speedup
REDUCE_SUM-Hip.cpp:104:25	s_waitcnt vmcnt(0)	<- REDUCE_SUM-Hip.cpp:104:28	global_load_dwordx2	277,872,640	65.6%	2.71x
amd_device_functions.h:778:9	s_barrier	<- amd_device_functions.h:778:9	s_barrier	44,040,192	10.4%	1.11x
functional.hpp:242	s_waitcnt vmcnt(9)	<- block_load_func.hpp:258	global_load_dwordx2	16,777,216	4.0%	1.04x
block_load_func.hpp:258	global_load_dwordx2	<- block_load_func.hpp:258	v_addc_co_u32_e32	16,055,075	3.8%	1.04x
(base) JMC22-5:LEO johnmc\$						

Discussion

- What performance issues do you need help with?
- What would help you investigate these issues?
- Are there any features that you would like added?

HPCToolkit Resources

- Documentation
 - User manual for HPCToolkit: <https://hpctoolkit.gitlab.io/hpctoolkit>
 - Cheat sheet: <https://gitlab.com/hpctoolkit/hpctoolkit/-/wikis/HPCToolkit-cheat-sheet>
 - User manual for hpcviewer: <https://hpctoolkit.gitlab.io/hpctoolkit/users/hpcviewer/hpcviewer.html>
 - Tutorial videos
 - <http://hpctoolkit.org/training.html>
 - recorded demo of GPU analysis of Quicksilver: <https://youtu.be/vixa3hGDuGg>
 - recorded tutorial presentation including demo with GPU analysis of GAMESS: <https://vimeo.com/781264043>
- Software
 - Download hpcviewer GUI binaries for your laptop, desktop, cluster, or supercomputer
 - OS: Linux, Windows, MacOS
 - Processors: x86_64, aarch64, ppc64le
 - <http://hpctoolkit.org/download.html>
 - Install HPCToolkit on your Linux desktop, cluster, or supercomputer using Spack
 - <http://hpctoolkit.org/software.html>