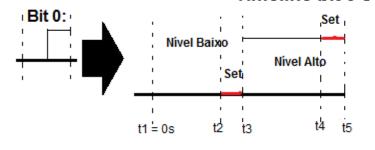
## -> Timeline bit 0 sem PWM:





t\_set = 42 μs t2 = t\_nivelbaixo - t\_set = 402.45 μs t3 = t\_nivelbaixo = 444.45 μs t4 = t\_nivelalto + t\_nivelalto - t\_set = 846.9 μs t5 = t4 + t\_set = t\_bit0 = 888.9 μs