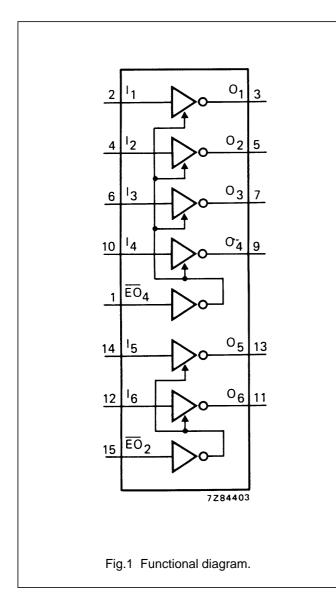
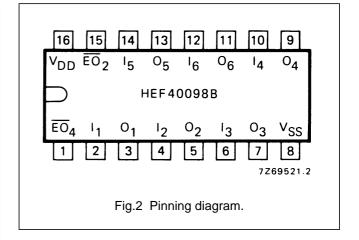
3-state hex inverting buffer

HEF40098B buffers

DESCRIPTION

The HEF40098B is a hex inverting buffer with 3-state outputs. The 3-state outputs are controlled by two enable inputs (\overline{EO}_4 and \overline{EO}_2). A HIGH on \overline{EO}_4 causes four of the six buffer elements to assume a high impedance or OFF-state regardless of the other input conditions and a HIGH on \overline{EO}_2 causes the outputs of the remaining two buffer elements to assume a high impedance or OFF-state regardless of the other input conditions.





HEF40098BP(N): 16-lead DIL; plastic

(SOT38-1)

HEF40098BD(F): 16-lead DIL; ceramic (cerdip)

(SOT74)

HEF40098BT(D): 16-lead SO; plastic

(SOT109-1)

(): Package Designator North America

PINNING

I₁ to I₆ buffer inputs

 \overline{EO}_4 , \overline{EO}_2 enable inputs (active LOW)

O₁ to O₆ buffer outputs (active LOW)

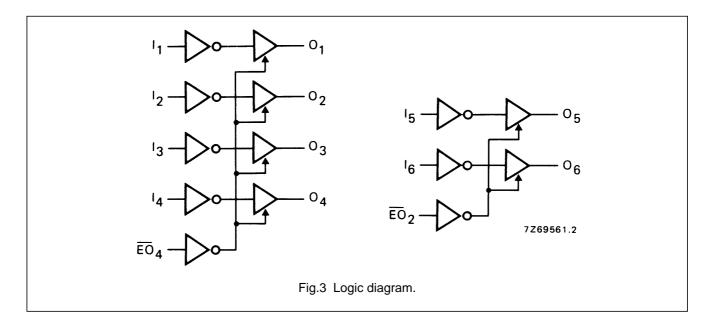
FAMILY DATA, IDD LIMITS category BUFFERS

See Family Specifications

Philips Semiconductors Product specification

3-state hex inverting buffer

HEF40098B buffers



DC CHARACTERISTICS

 $V_{SS} = 0 V$

	V _{DD}	V _{OH}	V _{OL}		T _{amb} (°C)						
HEF				SYMBOL	-40		+25		+8	5	
	-		-		MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
Output current	5	4,6			1,2		1,0		0,8	mA	
HIGH	10	9,5		-I _{OH}	3,8		3,2		2,5	mA	
	15	13,5			12,0		10,0		8,0	mA	
HIGH	5	2,5		-l _{OH}	3,8		3,2		2,5	mA	
Output current	4,75		0,4		3,5		2,9		2,3	mA	
LOW	10		0,5	I _{OL}	12,0		10,0		8,0	mA	
	15		1,5		24,0		20,0		16,0	mA	

	V _{DD}	V _{OH}	V _{OL}	SYMBOL	T _{amb} (°C)						
HEC					-55		+25		+12	25	
	_				MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
Output current	5	4,6			1,25		1,0		0,6	mA	
HIGH	10	9,5		-I _{OH}	4,0		3,2		2,1	mA	
	15	12,5			12,5		10,0		6,7	mA	
HIGH	5	2,5		-l _{OH}	4,0		3,2		2,1	mA	
Output current	4,75		0,4		3,6		2,9		1,9	mA	
LOW	10		0,5	I _{OL}	12,5		10,0		6,7	mA	
	15		1,5		25,0		20,0		13,0	mA	