

```
/* Module A */  
A_in = B_out;
```

▪  
▪  
▪

```
A_out = A_reg;  
A_reg = ...;
```

```
/* Module B */  
B_in = A_out;
```

▪  
▪  
▪

```
B_out = B_reg;  
B_reg = ...;
```

(a)

```
A_out = A_reg; // Module A output  
B_out = B_reg; // Moudle B output
```

```
/* Module A */  
A_in = B_out;
```

▪  
▪  
▪

```
A_reg = ...;
```

```
/* Module B */  
B_in = A_out;
```

▪  
▪  
▪

```
B_reg = ...;
```

(b)