

8086 Minimum and Maximum modes

Minimum Mode

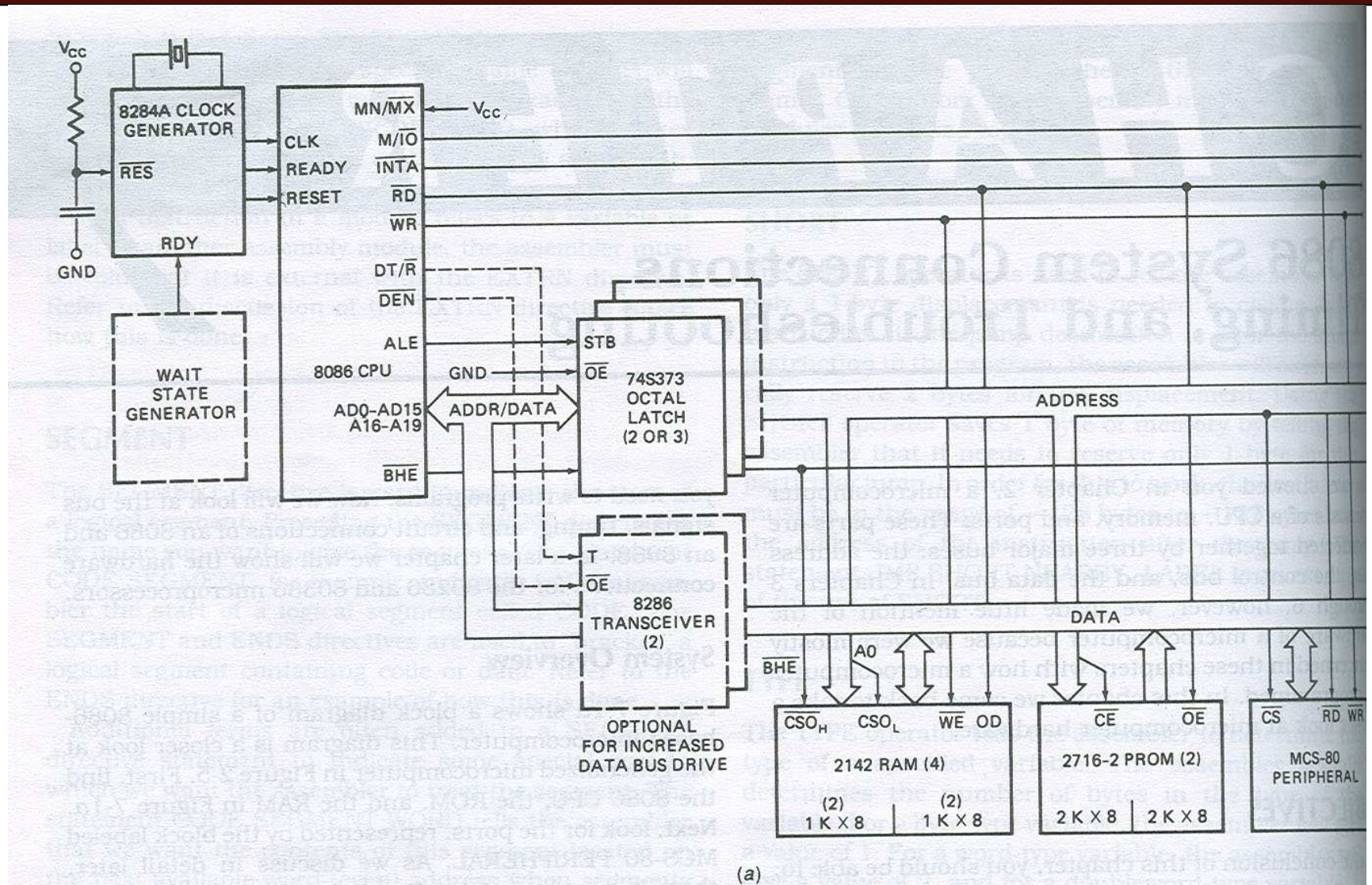
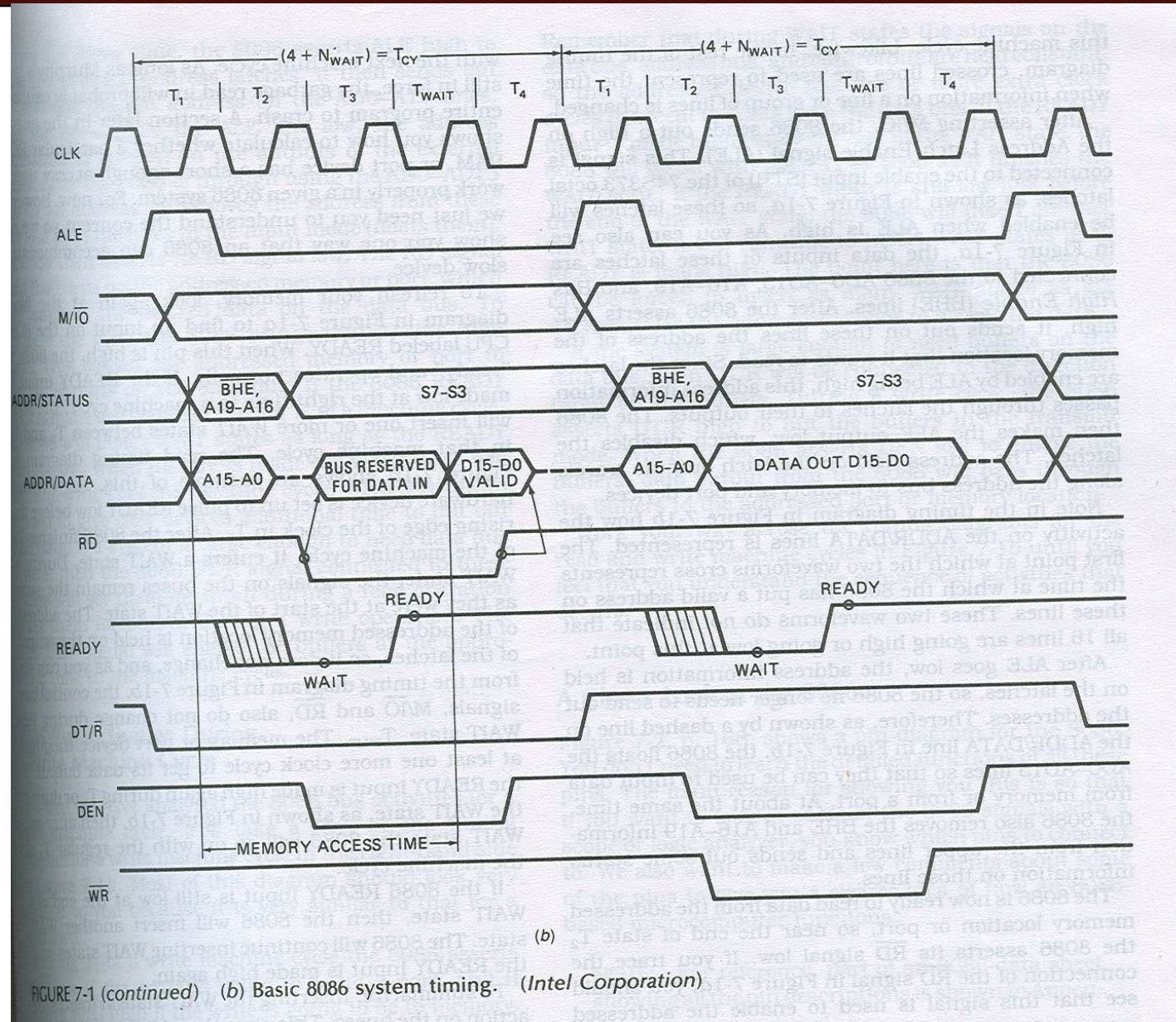


FIGURE 7-1 (a) Block diagram of a simple 8086-based microcomputer. (See also next page.)

Minimum Mode Timing Diagram



Maximum Mode

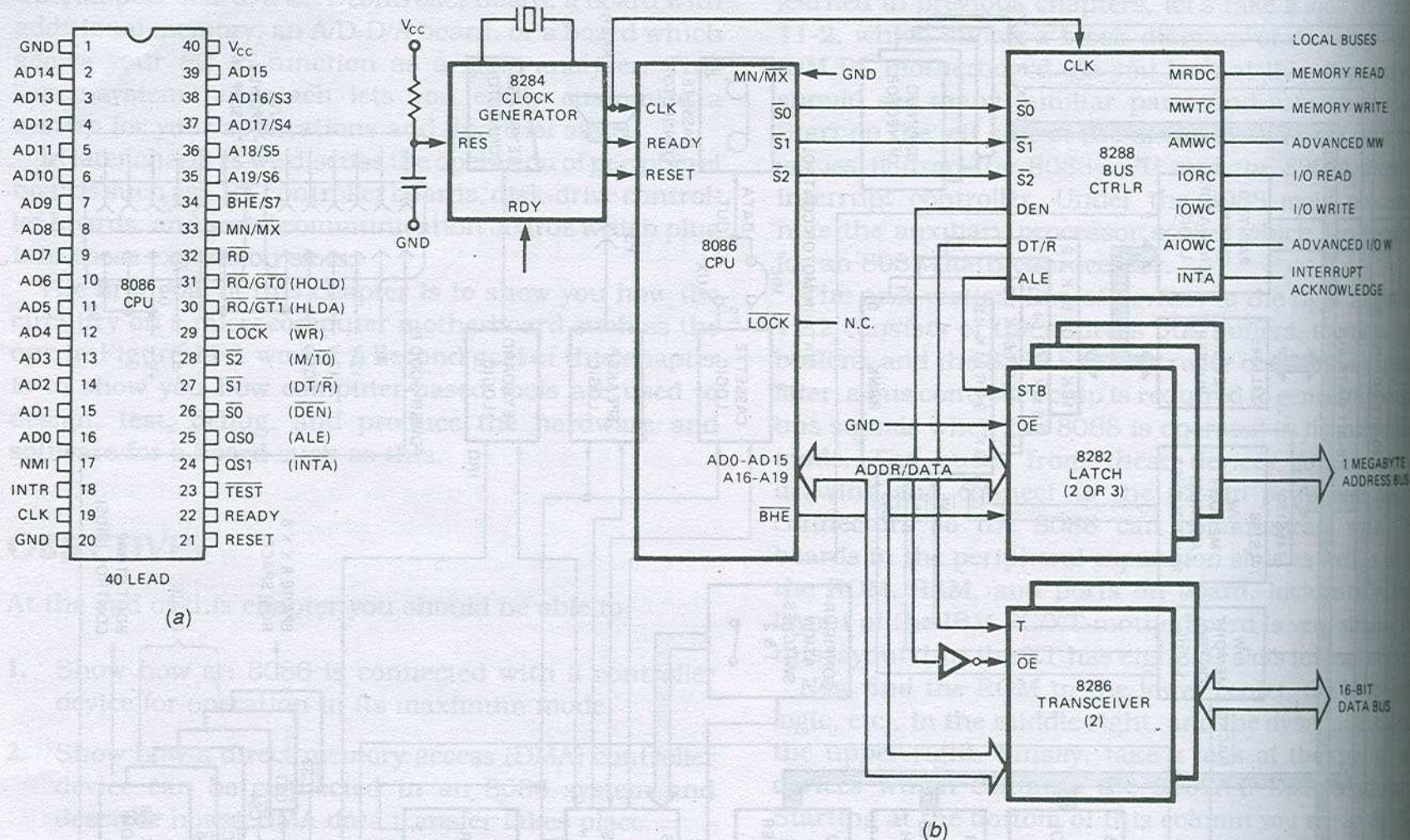


FIGURE 11-3 8086 revisited. (a) 8086 pin diagram. (b) Circuit showing 8086 connections for MAX mode operation. (Intel Corporation)

Maximum Mode Timing Diagram

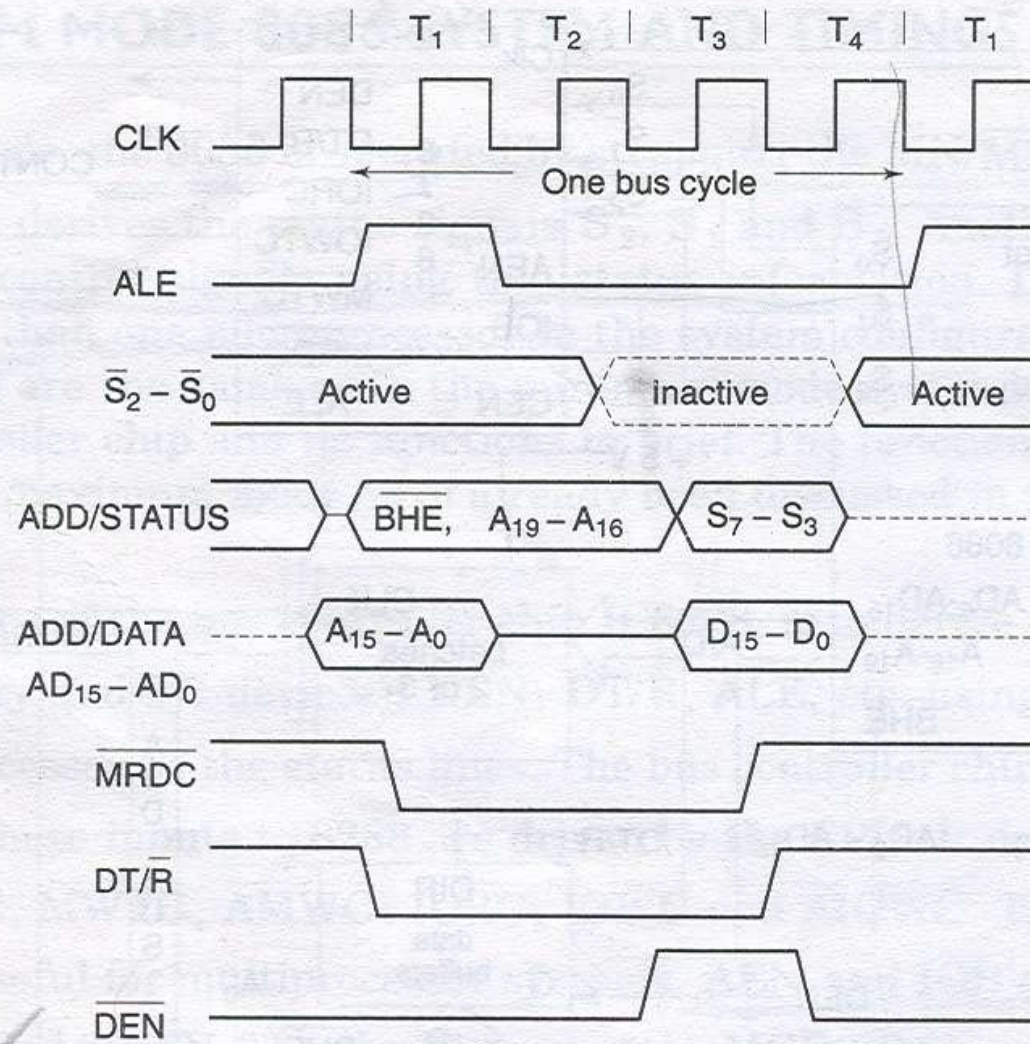


Fig. 1.11(a) Memory Read Timing in Maximum Mode