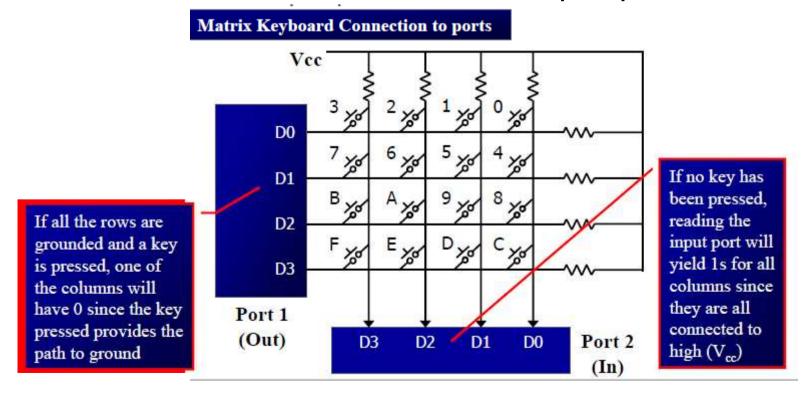
KEYBOARD INTERFACING WITH 8051

Introduction

- Keyboards are organized in a matrix of rows and columns
 - The CPU accesses both rows and columns through ports
 - Therefore, with two 8-bit ports, an 8 x 8 matrix of keys can be connected to a microprocessor
 - When a key is pressed, a row and a column make a contact
 - Otherwise, there is no connection between rows and columns
- In IBM PC keyboards, a single microcontroller takes care of hardware and software interfacing

Scanning and Identifying the key

- A 4x4 matrix connected to two ports
 - The rows are connected to an output port and the columns are connected to an input port



- It is the function of the microcontroller to scan the keyboard continuously to detect and identify the key pressed
- To detect a pressed key, the microcontroller grounds all rows by providing 0 to the output latch, then it reads the columns
 - If the data read from columns is D3 D0 = 1111, no key has been pressed and the process continues till key press is detected
 - If one of the column bits has a zero, this means that a key press has occurred
 - For example, if D3 D0 = 1101, this means that a key in the D1 column has been pressed
 - After detecting a key press, microcontroller will go through the process of identifying the key

- Starting with the top row, the microcontroller grounds it by providing a low to row D0 only
 - It reads the columns, if the data read is all 1s, no key in that row is activated and the process is moved to the next row
- It grounds the next row, reads the columns, and checks for any zero
 - This process continues until the row is identified
- After identification of the row in which the key has been pressed
 - Find out which column the pressed key belongs to

Example 12-3

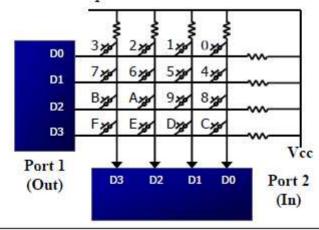
From Figure 12-6, identify the row and column of the pressed key for each of the following.

- (a) D3 D0 = 1110 for the row, D3 D0 = 1011 for the column
- (b) D3 D0 = 1101 for the row, D3 D0 = 0111 for the column

Solution:

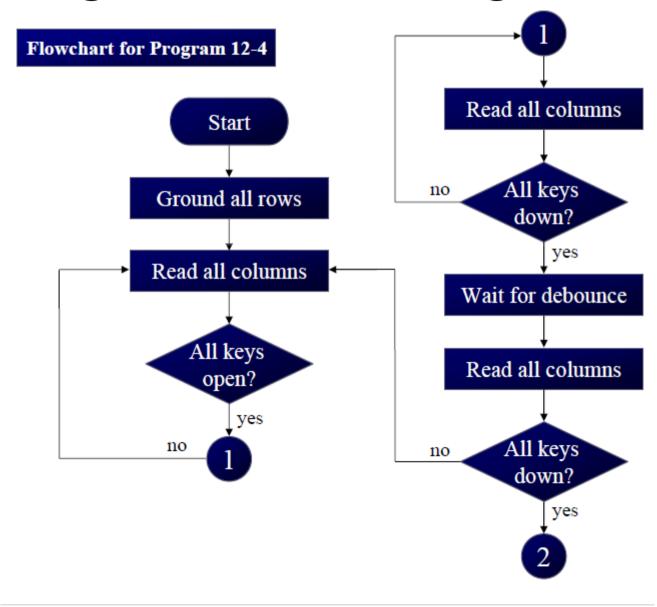
From Figure 13-5 the row and column can be used to identify the key.

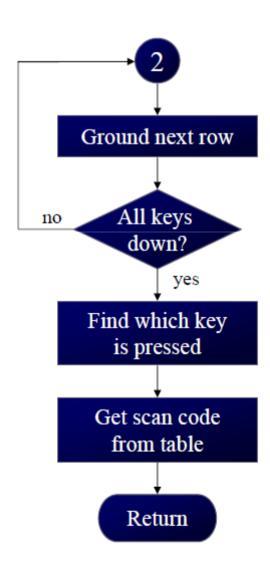
- (a) The row belongs to D0 and the column belongs to D2; therefore, key number 2 was pressed.
- (b) The row belongs to D1 and the column belongs to D3; therefore, key number 7 was pressed.



- Program 12-4 for detection and identification of key activation goes through the following stages:
 - To make sure that the preceding key has been released, 0s are output to all rows at once, and the columns are read and checked repeatedly until all the columns are high
 - When all columns are found to be high, the program waits for a short amount of time before it goes to the next stage of waiting for a key to be pressed

- To detect which row key press belongs to, it grounds one row at a time, reading the columns each time
 - If it finds that all columns are high, this means that the key press cannot belong to that row
 - Therefore, it grounds the next row and continues until it finds the row the key press belongs to
 - Upon finding the row that the key press belongs to, it sets up the starting address for the look-up table holding the scan codes (or ASCII) for that row
- To identify the key press, it rotates the column bits, one bit at a time, into the carry flag and checks to see if it is low
 - Upon finding the zero, it pulls out the ASCII code for that key from the look-up table.
 - otherwise, it increments the pointer to point to the next element of the look-up table





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Program 12-4: Keyboard Program
; keyboard subroutine. This program sends the ASCII
; code for pressed key to P0.1
;P1.0-P1.3 connected to rows, P2.0-P2.3 to column
       MOV P2,#0FFH ; make P2 an input port
K1:
       MOV P1,#0 ;ground all rows at once
       MOV A, P2 ; read all col
                     ; (ensure keys open)
       ANL A,00001111B ; masked unused bits
       CJNE A, #00001111B, K1 ; till all keys release
       ACALL DELAY ; call 20 msec delay
K2:
       MOV A, P2 ; see if any key is pressed
       ANL A,00001111B ; mask unused bits
       CJNE A, #00001111B, OVER; key pressed, find row
              ;check till key pressed
       SJMP K2
       ACALL DELAY :wait 20 msec debounce time
OVER:
       MOV A, P2 ; check key closure
       ANL A,00001111B ; mask unused bits
       CJNE A, #00001111B, OVER1; key pressed, find row
       SJMP K2
                     ; if none, keep polling
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. . . .
OVER1: MOV P1, #11111110B ; ground row 0
                         ;read all columns
      MOV A, P2
      ANL A, #00001111B ; mask unused bits
      CJNE A, #00001111B, ROW 0 ; key row 0, find col.
      MOV P1, #11111101B ; ground row 1
                             read all columns;
      MOV A, P2
      ANL A, #00001111B ; mask unused bits
      CJNE A, #00001111B, ROW 1 ; key row 1, find col.
      MOV P1, #11111011B ; ground row 2
                             read all columns;
      MOV A, P2
      ANL A, #00001111B ; mask unused bits
      CJNE A, #00001111B, ROW 2 ; key row 2, find col.
      MOV P1, #11110111B ; ground row 3
                              read all columns;
      MOV A, P2
      ANL A, #00001111B ; mask unused bits
      CJNE A, #00001111B, ROW 3 ; key row 3, find col.
                  ; if none, false input,
      LJMP K2
                         ;repeat
. . . .
```