

NOTE: PB10 is a 3.6V tolerant pin. Do not connect 5V UART.

Pinout diagram for the STM32F407VGT6 microcontroller, showing connections for pins J1 through J37. The diagram is organized into seven groups of four pins each, with each group having a common GND and +5V connection. The pins are labeled with their functions: UART_1_RX, UART_1_TX, UART_2_RX, UART_2_TX, UART_3_RX, UART_3_TX, UART_4_RX, UART_4_TX, I2C_SDA, I2C_CLK, PWM_1, PWM_2, PWM_3, PWM_4, CURRENT, BOOT0, SWCLK, NRST, SWDIO, BUZZER, +BATT, and GND.

Pin	Function
J1	GND
J2	+5V
J3	UART_1_RX
J4	UART_1_TX
J5	GND
J6	+5V
J7	UART_2_RX
J8	UART_2_TX
J9	GND
J10	+5V
J11	UART_3_RX
J12	UART_3_TX
J13	GND
J14	+5V
J15	UART_4_RX
J16	UART_4_TX
J17	GND
J18	+5V
J19	I2C_SDA
J20	I2C_CLK
J21	GND
J22	PWM_1
J23	PWM_2
J24	PWM_3
J25	PWM_4
J26	CURRENT
J27	GND
J28	BOOT0
J29	+3.3V
J30	SWCLK
J31	NRST
J32	SWDIO
J33	GND
J34	+5V
J35	BUZZER
J36	+BATT
J37	GND

Diagram illustrating the voltage divider circuit for the 555 timer. The circuit consists of a +BATT supply connected to a 68k resistor (R5) in series with a 10k resistor (R6). The junction between R5 and R6 is connected to the non-inverting input of the 555 timer (pin 5) and also to a 100nF capacitor (C14) which is connected to GND. A label 'VOLTAGE' points to the junction between R5 and R6.

H1 H2 H3 H4