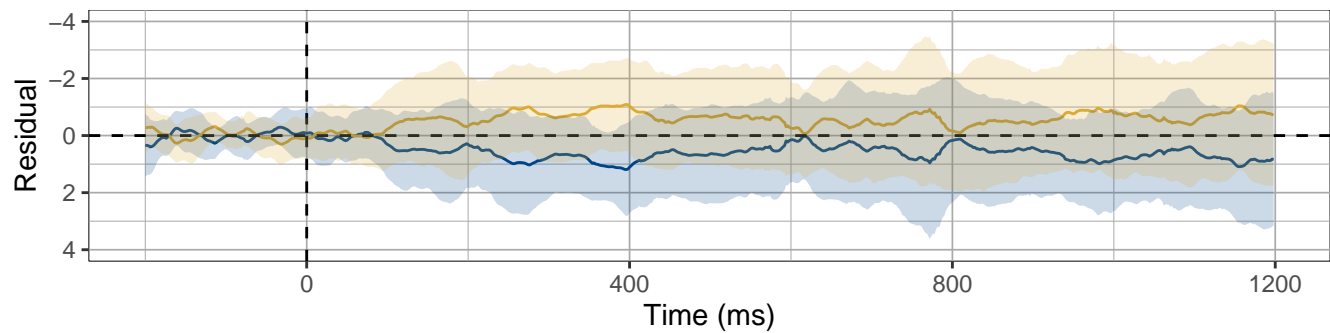
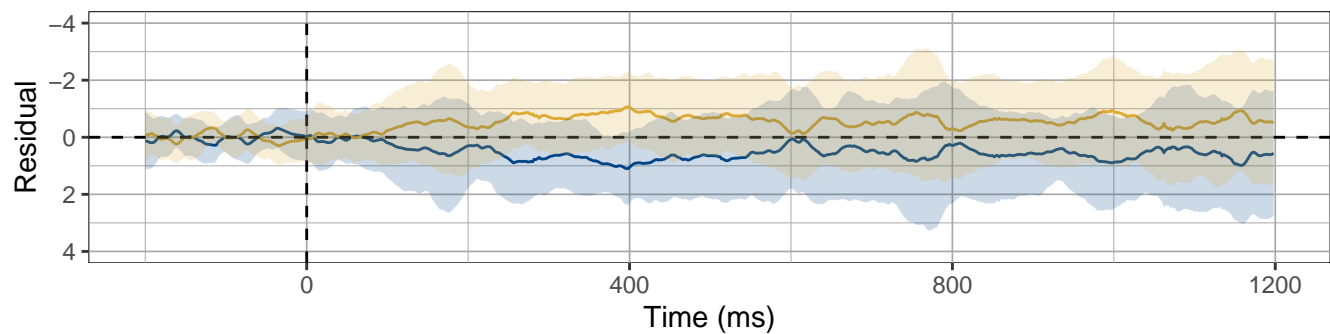


res_rcverb

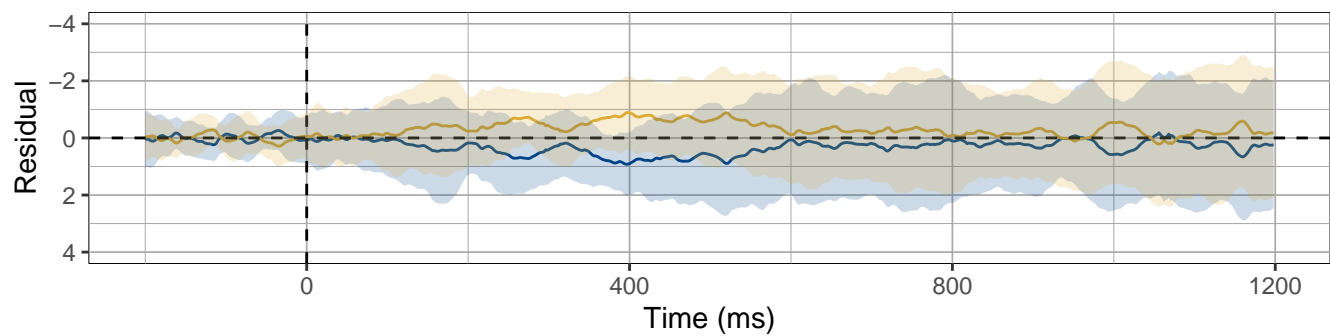
Fz



Cz



Pz



Condition



C: A+E-



D: A-E-