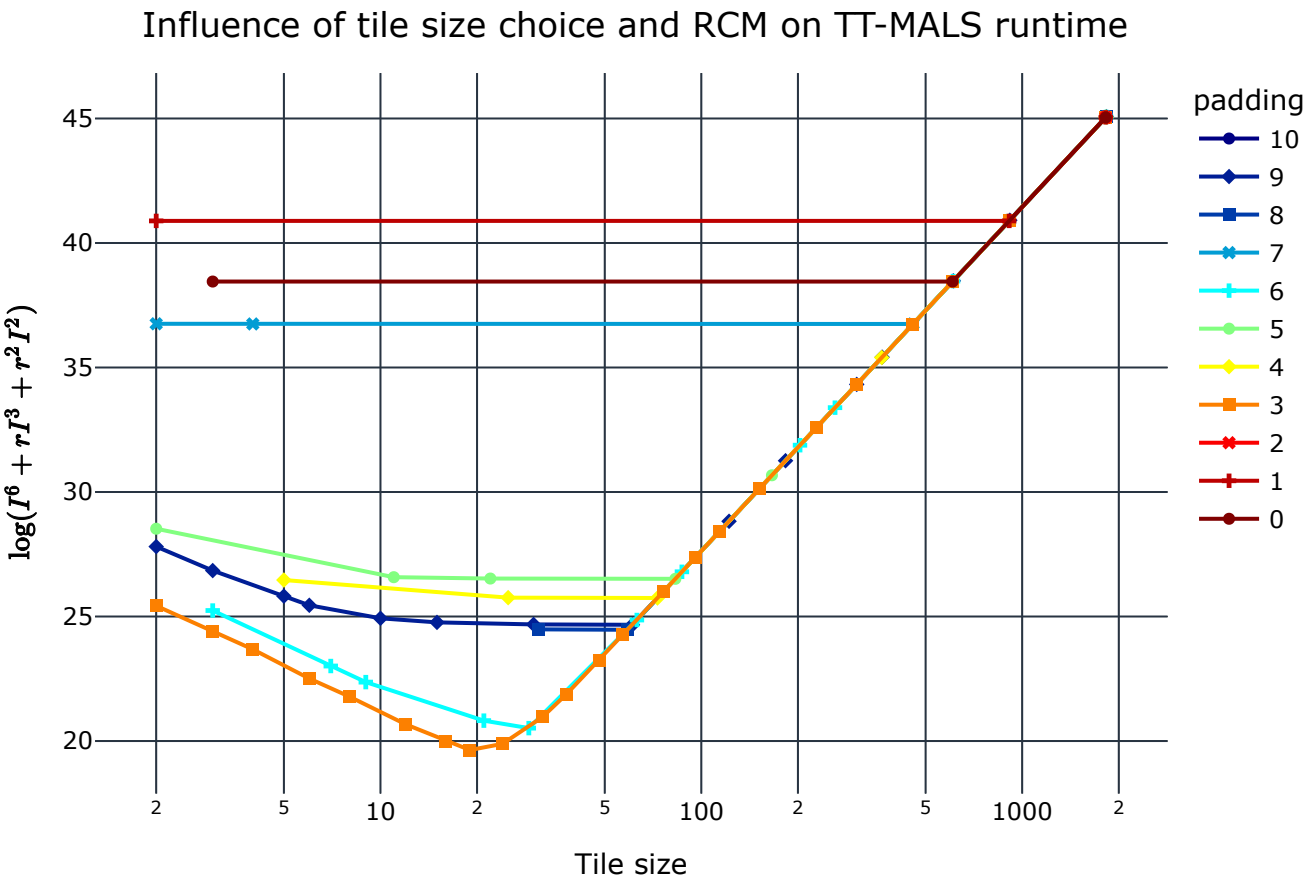
Influence of tile size choice and RCM on TT-MALS runtime
$\log(I^6 + rI^3 + r^2 I^2)$
45
40
35
30
25
20
2
5
10
2
5
100
2
5
1000
2
Tile size
padding
10
9
8
7
6
5
4
3
2
1
0