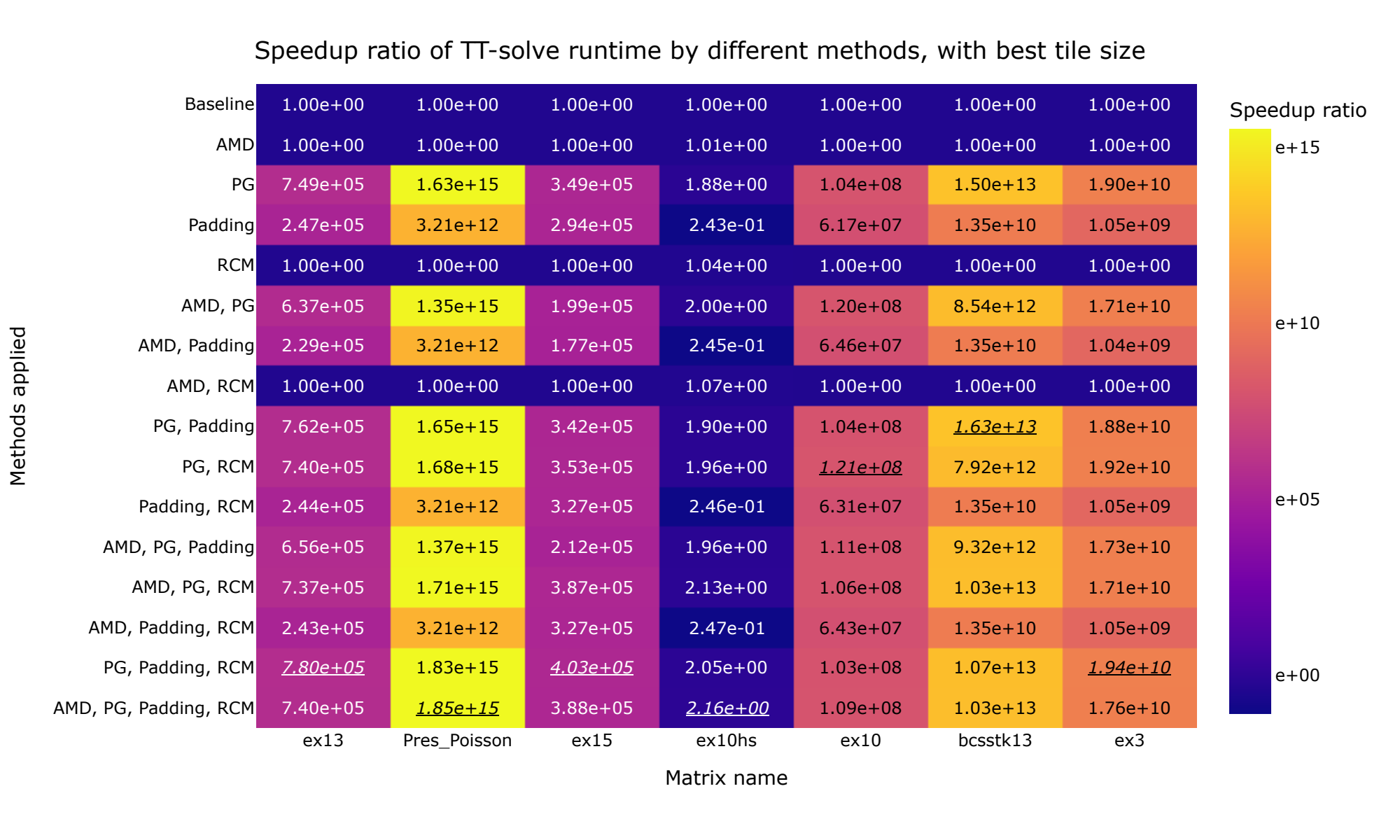
Speedup ratio of TT-solve runtime by different methods, with best tile size
Methods applied
Baseline 1.00e+00 1.00e+00 1.00e+00 1.00e+00 1.00e+00 1.00e+00 1.00e+00
AMD 1.00e+00 1.00e+00 1.00e+00 1.01e+00 1.00e+00 1.00e+00 1.00e+00
PG 7.49e+05 1.63e+15 3.49e+05 1.88e+00 1.04e+08 1.50e+13 1.90e+10
Padding 2.47e+05 3.21e+12 2.94e+05 2.43e-01 6.17e+07 1.35e+10 1.05e+09
RCM 1.00e+00 1.00e+00 1.00e+00 1.04e+00 1.00e+00 1.00e+00 1.00e+00
AMD, PG 6.37e+05 1.35e+15 1.99e+05 2.00e+00 1.20e+08 8.54e+12 1.71e+10
AMD, Padding 2.29e+05 3.21e+12 1.77e+05 2.45e-01 6.46e+07 1.35e+10 1.04e+09
AMD, RCM 1.00e+00 1.00e+00 1.00e+00 1.07e+00 1.00e+00 1.00e+00 1.00e+00
PG, Padding 7.62e+05 1.65e+15 3.42e+05 1.90e+00 1.04e+08 1.63e+13 1.88e+10
PG, RCM 7.40e+05 1.68e+15 3.53e+05 1.96e+00 1.21e+08 7.92e+12 1.92e+10
Padding, RCM 2.44e+05 3.21e+12 3.27e+05 2.46e-01 6.31e+07 1.35e+10 1.05e+09
AMD, PG, Padding 6.56e+05 1.37e+15 2.12e+05 1.96e+00 1.11e+08 9.32e+12 1.73e+10
AMD, PG, RCM 7.37e+05 1.71e+15 3.87e+05 2.13e+00 1.06e+08 1.03e+13 1.71e+10
AMD, Padding, RCM 2.43e+05 3.21e+12 3.27e+05 2.47e-01 6.43e+07 1.35e+10 1.05e+09
PG, Padding, RCM 7.80e+05 1.83e+15 4.03e+05 2.05e+00 1.03e+08 1.07e+13 1.94e+10
AMD, PG, Padding, RCM 7.40e+05 1.85e+15 3.88e+05 2.16e+00 1.09e+08 1.03e+13 1.76e+10
ex13 Pres_Poisson ex15 ex10hs ex10 bcsstk13 ex3
Matrix name
Speedup ratio
e+15
e+10
e+05
e+00