Speedup ratio of TT-solve runtime by different methods, largest prime factor as tile size

Baseline	1.00e+00						
AMD	1.00e+00						
PG	5.16e+05	1.63e+15	3.49e+05	1.84e+00	1.04e+08	1.15e+13	1.90e+10
Padding	2.47e+05	3.21e+12	2.94e+05	2.44e-01	6.12e+07	1.35e+10	1.05e+09
RCM	1.00e+00	1.00e+00	1.00e+00	1.05e+00	1.00e+00	1.00e+00	1.00e+00
AMD, PG	4.69e+05	1.35e+15	1.99e+05	1.98e+00	1.16e+08	7.80e+12	1.70e+10
AMD, Padding	2.29e+05	3.21e+12	1.77e+05	2.46e-01	6.43e+07	1.35e+10	1.04e+09
AMD, RCM	1.00e+00	1.00e+00	1.00e+00	1.05e+00	1.00e+00	1.00e+00	1.00e+00
PG, Padding	5.11e+05	1.65e+15	3.01e+05	1.88e+00	1.03e+08	<u>1.19e+13</u>	1.88e+10
PG, RCM	5.46e+05	1.68e+15	3.53e+05	2.11e+00	1.15e+08	7.95e+12	1.94e+10
Padding, RCM	2.44e+05	3.21e+12	3.27e+05	2.48e-01	6.60e+07	1.35e+10	1.05e+09
AMD, PG, Padding	4.80e+05	1.37e+15	1.97e+05	1.94e+00	1.09e+08	8.06e+12	1.72e+10
AMD, PG, RCM	5.26e+05	1.71e+15	3.87e+05	2.07e+00	1.15e+08	8.04e+12	1.70e+10
AMD, Padding, RCM	2.43e+05	3.21e+12	3.27e+05	2.47e-01	6.76e+07	1.35e+10	1.05e+09
PG, Padding, RCM	<u>5.48e+05</u>	1.83e+15	<u>4.03e+05</u>	2.12e+00	1.15e+08	8.76e+12	<u>1.96e+10</u>
AMD, PG, Padding, RCM	5.24e+05	<u>1.85e+15</u>	3.19e+05	<u>2.25e+00</u>	<u>1.39e+08</u>	7.64e+12	1.72e+10
	ex13	Pres_Poisson	ex15	ex10hs	ex10	bcsstk13	ex3

Speedup ratio e+15 e+10 e+05 e+00

Matrix name