Speedup ratio of TT-MALS runtime by different methods, largest prime factor as tile size

Baseline	1.00e+00	Speedup ratio							
AMD	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	9.99e-01	1.02e+00		e+12
PG	2.91e+03	2.33e+05	6.66e+10	1.53e+08	3.90e+12	7.21e+02	8.64e-01		
Padding	2.47e+03	3.44e+05	1.21e+10	1.48e+08	2.14e+12	6.87e+02	1.10e+00		
RCM	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.25e+00		
AMD, PG	2.10e+03	2.63e+05	3.06e+10	1.13e+08	2.79e+12	3.69e+02	1.01e+00		
AMD, Padding	1.97e+03	3.67e+05	1.07e+10	1.15e+08	1.90e+12	3.88e+02	1.25e+00		e+08
AMD, RCM	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.26e+00		
PG, Padding	3.07e+03	3.46e+05	<u>6.82e+10</u>	1.61e+08	3.52e+12	7.22e+02	1.09e+00		
PG, RCM	<u>3.15e+03</u>	2.62e+05	3.92e+10	1.59e+08	4.24e+12	8.61e+02	1.17e+00		
Padding, RCM	2.36e+03	3.94e+05	1.11e+10	1.70e+08	2.42e+12	<u>1.44e+03</u>	1.49e+00		e+04
AMD, PG, Padding	2.21e+03	3.85e+05	2.97e+10	1.25e+08	2.54e+12	3.69e+02	1.25e+00		
AMD, PG, RCM	2.70e+03	2.60e+05	3.78e+10	1.63e+08	3.78e+12	8.67e+02	1.12e+00		
AMD, Padding, RCM	2.32e+03	4.67e+05	1.10e+10	1.73e+08	2.36e+12	1.44e+03	1.38e+00		
PG, Padding, RCM	3.05e+03	<u>4.90e+05</u>	4.23e+10	<u>1.76e+08</u>	4.30e+12	8.76e+02	1.52e+00		
AMD, PG, Padding, RCM	2.76e+03	4.84e+05	3.56e+10	1.75e+08	<u>4.42e+12</u>	8.73e+02	<u>1.65e+00</u>		e+00
	ex13	ex10	bcsstk13	ex3	Pres_Poisson	ex15	ex10hs		

Matrix name