Speedup ratio of TT-MALS runtime by different methods, with best tile size

Baseline	1.00e+00						
AMD	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	9.99e-01	1.18e+00
PG	3.05e+03	3.99e+05	6.68e+10	1.56e+08	4.80e+12	7.79e+02	1.02e+00
Padding	2.92e+03	3.94e+05	1.21e+10	1.48e+08	2.14e+12	6.87e+02	1.05e+00
RCM	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.42e+00
AMD, PG	2.32e+03	4.48e+05	3.16e+10	1.27e+08	3.46e+12	3.86e+02	1.15e+00
AMD, Padding	2.16e+03	4.14e+05	1.07e+10	1.15e+08	1.90e+12	3.88e+02	1.19e+00
AMD, RCM	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.00e+00	1.45e+00
PG, Padding	3.07e+03	3.46e+05	<u>7.03e+10</u>	1.61e+08	5.00e+12	7.30e+02	1.07e+00
PG, RCM	<u>3.15e+03</u>	4.75e+05	4.34e+10	1.82e+08	5.19e+12	1.49e+03	1.59e+00
Padding, RCM	2.80e+03	4.89e+05	1.11e+10	1.70e+08	2.42e+12	1.44e+03	1.42e+00
AMD, PG, Padding	2.21e+03	3.85e+05	3.30e+10	1.27e+08	3.50e+12	4.21e+02	1.19e+00
AMD, PG, RCM	2.75e+03	4.89e+05	4.13e+10	1.77e+08	5.14e+12	1.37e+03	1.56e+00
AMD, Padding, RCM	2.76e+03	<u>5.04e+05</u>	1.10e+10	1.73e+08	2.36e+12	1.44e+03	1.32e+00
PG, Padding, RCM	3.05e+03	4.90e+05	4.55e+10	<u>1.85e+08</u>	5.47e+12	<u>1.49e+03</u>	1.49e+00
AMD, PG, Padding, RCM	2.79e+03	4.84e+05	4.25e+10	1.83e+08	<u>5.54e+12</u>	1.41e+03	<u>1.66e+00</u>
	ex13	ex10	bcsstk13	ex3	Pres_Poisson	ex15	ex10hs

Speedup ratio e+12 e+08 e+04 e+00

Matrix name