

# TC4047BP

C<sup>2</sup>MOS DIGITAL INTEGRATED CIRCUIT  
SILICON MONOLITHIC

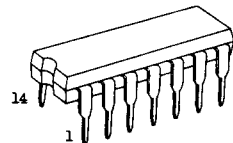
## TC4047BP LOW-POWER MONOSTABLE/ASTABLE MULTIVIBRATOR

TC4047BP is the multivibrator equipped with both astable function and mono-stable function, and retrigger operation and reset operation are also achievable.

For both operational modes, the pulse width can be varied by externally connected capacitor (C) and resistor (R).

Establish RETRIG="L". +TRIG="L", -TRIG="H" for astable operation and AST="L",  $\overline{\text{AST}}$ ="H" for mono-stable operation. (Refer to FUNCTION TABLE and OPERATING CONSIDERATIONS).

When EXTERNAL RESET input is set at "H", both operational modes of astable and mono-stable operations are reset to Q="L" and  $\overline{\text{Q}}$ ="H".

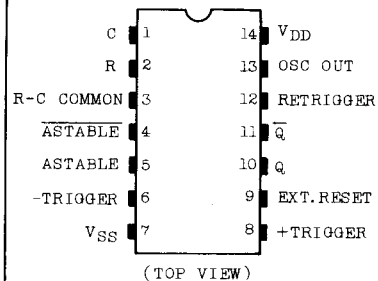


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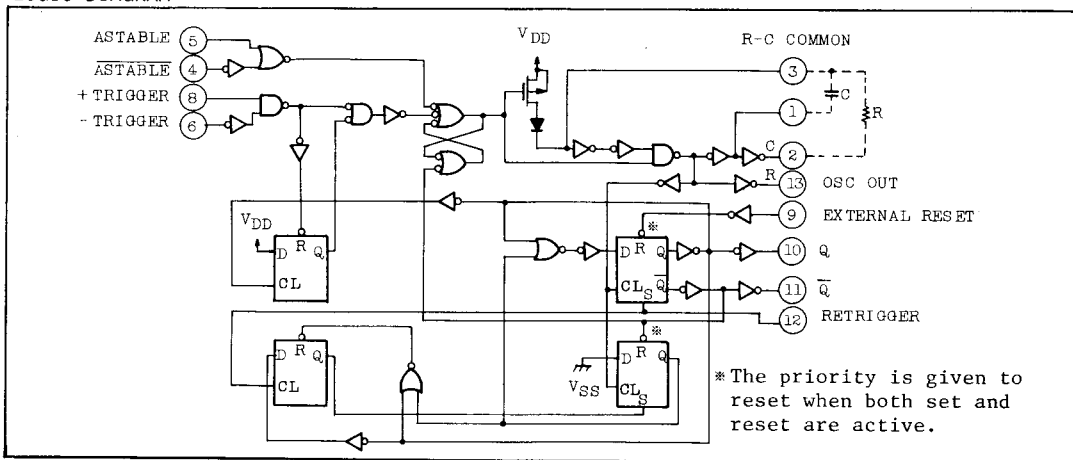
## ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V <sub>DD</sub>	V <sub>SS</sub> -0.5 ~ V <sub>SS</sub> +20	V
Input Voltage	V <sub>IN</sub>	V <sub>SS</sub> -0.5 ~ V <sub>DD</sub> +0.5	V
Output Voltage	V <sub>OUT</sub>	V <sub>SS</sub> -0.5 ~ V <sub>DD</sub> +0.5	V
DC Input Current	I <sub>IN</sub>	±10	mA
Power Dissipation	P <sub>D</sub>	300	mW
Operating Temperature Range	T <sub>A</sub>	-40 ~ 85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 ~ 150	°C
Lead Temp./Time	T <sub>sol</sub>	260°C · 10 sec	




## PIN ASSIGNMENT



## LOGIC DIAGRAM



FUNCTION TABLE

FUNCTION							OUTPUT PERIOD OR PULSE WIDTH
	AST.	AST.	-TRIG.	+TRIG.	RETRIG.	EXT. RESET	
ASTABLE MULTIVIBRATOR							Q, $\bar{Q}$
Free Running	*	H	H	L	L	L	T=4.40RC
	L	*	H	L	L	L	OSC OUT
Inhibit	H	L	H	L	L	L	T=2.20RC
MONOSTABLE MULTIVIBRATOR							
Positive-Edge Trigger	H	L	L		L	L	Q, $\bar{Q}$
Negative-Edge Trigger	H	L		H	L	L	$t_w=2.48RC$
Retrigger	H	L	L			L	
Note : External resistor and capacitance as LOGIC DIAGRAM * Don't care							

## OPERATING CONSIDERATION

## 1. Astable Operation

By connecting inputs of -TRIGGER, +TRIGGER, RETRIGGER AND EXTERNAL RESET as shown in FUNCTION TABLE, stable operation of astable multivibrator can be obtained.

- When ASTABLE input is placed at "H", oscillation is continued regardless of ASTABLE input. When ASTABLE input is placed at "L", oscillation is continued regardless of ASTABLE input. (Free Running).
- Having ASTABLE input at "H", if ASTABLE input is set at "L", oscillation stops as long as it is at "L". (True Gating)
- Having ASTABLE input at "L", if ASTABLE input is set at "H", oscillation stops as long as it is at "H". (Complement Gating)

The oscillating period is determined by the external resistor and capacitor to be approximately  $T=2.2RC$ . This oscillation waveform is obtained as it is at OSC OUT and the oscillation waveform with double period and 50% duty cycle is obtained at outputs Q and  $\bar{Q}$ .

## 2. Mono-Stable Operation

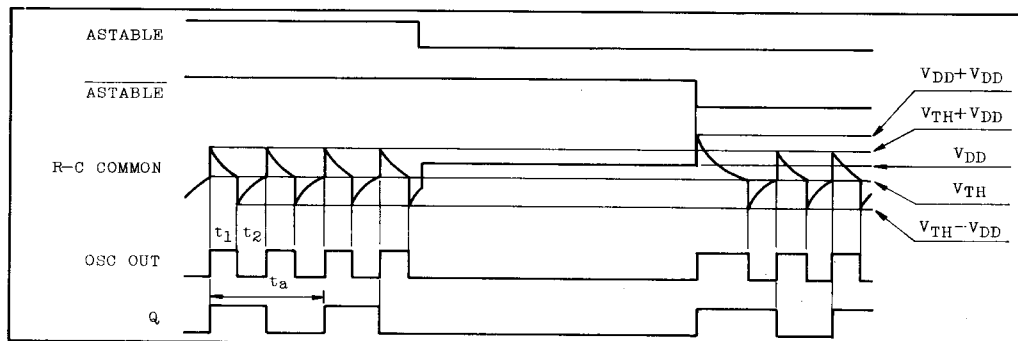
By connecting ASTABLE and ASTABLE inputs to "L" level and "H" level respectively, mono-stable multivibrator with the capabilities of retrigger operation and external asynchronous reset operation is obtained. This is normally used with RETRIGGER input and EXTERNAL RESET input connected to "L" level.

- When -TRIGGER input is set to "L", mono-stable pulse is obtained at the rising edge of +TRIGGER input at Q and  $\bar{Q}$  outputs. (Positive-edge Trigger)
  - When +TRIGGER input is set to "H", mono-stable pulse is obtained at the falling edge of -TRIGGER input at Q and  $\bar{Q}$  outputs. (Negative-edge Trigger)
  - Keeping -TRIGGER input at "L", if the same pulse input ( $T < 2.48RC$ ) is applied to both +TRIGGER input and RETRIGGER input, retrigger operation is achieved. However, the last transition of this pulse input must be negative going. (Retrigger)
- The width of mono-stable pulse is determined by the external resistor and capacitor to be approximately  $t_w=2.48RC$ .

Note : The external resistor and capacitor should be connected as shown by broken lines in the logical diagram for both mono-stable and astable operations.  
The capacitor used should be non-directional.

## TIMING DIAGRAM

### (ASTABLE MODE)

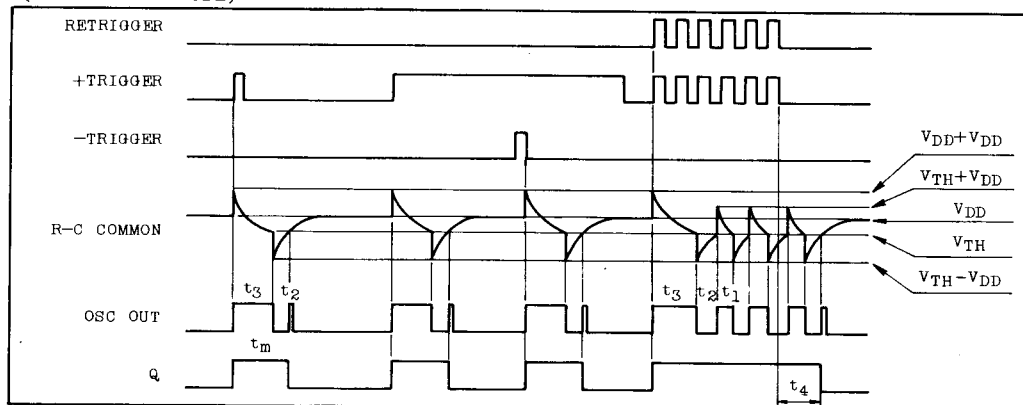


$$t_1 = -RC \ln \frac{V_{TH}}{V_{DD} + V_{TH}} \quad , \quad t_2 = -RC \ln \frac{V_{DD} - V_{TH}}{2V_{DD} - V_{TH}}$$

$$t_a = 2(t_1 + t_2) = -2RC \ln \frac{V_{TH}(V_{DD} - V_{TH})}{(V_{DD} + V_{TH})(2V_{DD} - V_{TH})}$$

$$\text{TYPICAL : } V_{TH} = 1/2 V_{DD} \quad t_a = 4.4 RC$$

### (MONO STABLE MODE)



$$t_1 = -RC \ln \frac{V_{TH}}{V_{DD} + V_{TH}} \quad , \quad t_2 = -RC \ln \frac{V_{DD} - V_{TH}}{2V_{DD} - V_{TH}}$$

$$t_3 = -RC \ln \frac{V_{TH}}{2V_{DD}} \quad , \quad t_1 + t_2 < t_4 < 2(t_1 + t_2)$$

$$t_m = t_3 + t_2 = -RC \ln \frac{V_{TH}(V_{DD} - V_{TH})}{2V_{DD}(2V_{DD} - V_{TH})}$$

$$\text{TYPICAL : } V_{TH} = 1/2 V_{DD} \quad t_m = 2.48 RC$$

Note :  $V_{TH}$ : Threshold Level

## RECOMMENDED OPERATING CONDITIONS (VSS=0V)

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V <sub>DD</sub>		3	-	18	V
Input Voltage	V <sub>IN</sub>		0	-	V <sub>DD</sub>	V
External Resistance	R <sub>X</sub>		5	-	1000	k $\Omega$
External Condenser	C <sub>X</sub>		No Limit			$\mu$ F

## STATIC ELECTRICAL CHARACTERISTICS (VSS=0V)

CHARACTERISTIC	SYM-BOL	TEST CONDITION	V <sub>DD</sub> (V)	-40°C		25°C			85°C		UNIT
				MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
High-Level Output Voltage	V <sub>OH</sub>	I <sub>OUT</sub>   < 1 $\mu$ A V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	5	4.95	-	4.95	5.00	-	4.95	-	V
			10	9.95	-	9.95	10.00	-	9.95	-	
			15	14.95	-	14.95	15.00	-	14.95	-	
Low-Level Output Voltage	V <sub>OL</sub>	I <sub>OUT</sub>   < 1 $\mu$ A V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	5	-	0.05	-	0.00	0.05	-	0.05	V
			10	-	0.05	-	0.00	0.05	-	0.05	
			15	-	0.05	-	0.00	0.05	-	0.05	
Output High Current	I <sub>OH</sub>	V <sub>OH</sub> =4.6V	5	-0.61	-	-0.51	-1.0	-	-0.42	-	mA
		V <sub>OH</sub> =2.5V	5	-2.5	-	-2.1	-4.0	-	-1.7	-	
		V <sub>OH</sub> =9.5V	10	-1.5	-	-1.3	-2.2	-	-1.1	-	
		V <sub>OH</sub> =13.5V	15	-4.0	-	-3.4	-9.0	-	-2.8	-	
		V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>									
Output Low Current	I <sub>OL</sub>	V <sub>OL</sub> =0.4V	5	0.61	-	0.51	1.5	-	0.42	-	mA
		V <sub>OL</sub> =0.5V	10	1.5	-	1.3	3.8	-	1.1	-	
		V <sub>OL</sub> =1.5V	15	4.0	-	3.4	15.0	-	2.8	-	
		V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>									
Input High Voltage	V <sub>IH</sub>	V <sub>OUT</sub> =0.5V, 4.5V	5	3.5	-	3.5	2.75	-	3.5	-	V
		V <sub>OUT</sub> =1.0V, 9.0V	10	7.0	-	7.0	5.5	-	7.0	-	
		V <sub>OUT</sub> =1.5V, 13.5V	15	11.0	-	11.0	8.25	-	11.0	-	
		I <sub>OUT</sub>   < 1 $\mu$ A									
Input Low Voltage	V <sub>IL</sub>	V <sub>OUT</sub> =0.5V, 4.5V	5	-	1.5	-	2.25	1.5	-	1.5	V
		V <sub>OUT</sub> =1.0V, 9.0V	10	-	3.0	-	4.5	3.0	-	3.0	
		V <sub>OUT</sub> =1.5V, 13.5V	15	-	4.0	-	6.75	4.0	-	4.0	
		I <sub>OUT</sub>   < 1 $\mu$ A									

STATIC ELECTRICAL CHARACTERISTICS ( $V_{SS}=0V$ )

CHARACTERISTIC		SYM-BOL	TEST CONDITION	$V_{DD}$ (V)	-40°C		25°C			85°C		UNIT
					MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
Input Current	"H" Level	$I_{IH}$	$V_{IH}=18V$	18	-	0.1	-	$10^{-5}$	0.1	-	1.0	$\mu A$
	"L" Level	$I_{IL}$	$V_{IL}=0V$	18	-	-0.1	-	$-10^{-5}$	-0.1	-	-1.0	
Quiescent Device Current		$I_{DD}$	$V_{IN}=V_{SS}, V_{DD}$ *	5	-	1	-	0.005	1	-	30	$\mu A$
				10	-	2	-	0.010	2	-	60	
				15	-	4	-	0.015	4	-	120	

\* All valid input combinations.

 DYNAMIC ELECTRICAL CHARACTERISTICS ( $T_a=25^\circ C$ ,  $V_{SS}=0V$ ,  $C_L=50pF$ )

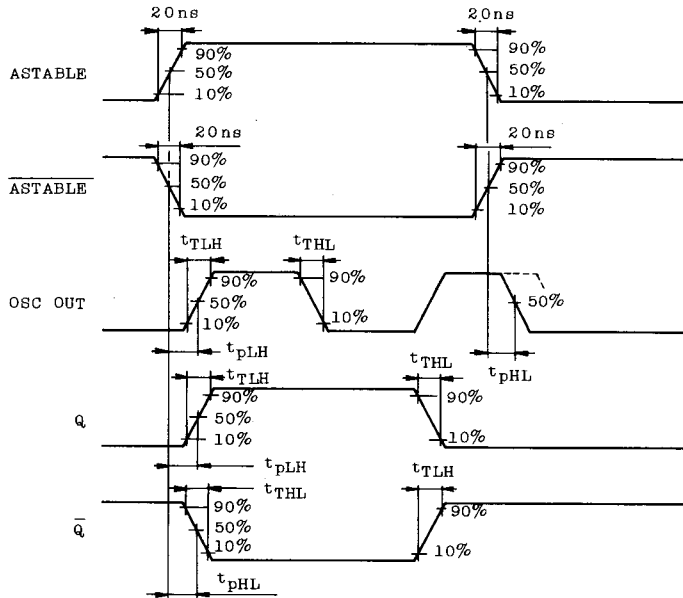
CHARACTERISTIC	SYMBOL	TEST CONDITION	$V_{DD}$ (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time (Low to High)	$t_{TLH}$		5	-	80	200	ns
			10	-	50	100	
			15	-	40	80	
Output Transition Time (High to Low)	$t_{THL}$		5	-	80	200	ns
			10	-	50	100	
			15	-	40	80	
Propagation Delay Time (ASTABLE, $\overline{ASTABLE}$ - OSC OUT)	$t_{PLH}$ $t_{PHL}$		5	-	290	580	ns
			10	-	110	220	
			15	-	70	160	
Propagation Delay Time (ASTABLE, $\overline{ASTABLE}$ - Q, $\overline{Q}$ )	$t_{PLH}$ $t_{PHL}$		5	-	480	960	ns
			10	-	170	350	
			15	-	110	250	
Propagation Delay Time (+TRIGGER, -TRIGGER - Q, $\overline{Q}$ )	$t_{PLH}$ $t_{PHL}$		5	-	550	1100	ns
			10	-	200	450	
			15	-	130	300	
Propagation Delay Time (RETRIGGER - Q, $\overline{Q}$ )	$t_{PLH}$ $t_{PHL}$		5	-	250	600	ns
			10	-	100	300	
			15	-	65	200	
Propagation Delay Time (EXTERNAL RESET - Q, $\overline{Q}$ )	$t_{PLH}$ $t_{PHL}$		5	-	270	540	ns
			10	-	100	200	
			15	-	65	140	

**DYNAMIC ELECTRICAL CHARACTERISTICS** (Ta=25°C, VSS=0V, CL=50pF)

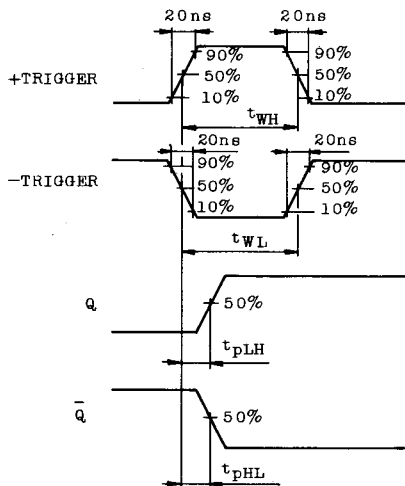
CHARACTERISTIC	SYMBOL	TEST CONDITION	VDD(V)	MIN.	TYP.	MAX.	UNIT
Min. Pulse Width (+TRIGGER, -TRIGGER)	t <sub>WH</sub> t <sub>WL</sub>		5	-	370	740	ns
			10	-	130	260	
			15	-	70	140	
Min. Pulse Width (EXT. RESET)	t <sub>WH</sub>		5	-	100	200	ns
			10	-	35	100	
			15	-	25	60	
Min. Pulse Width (RETRIGGER)	t <sub>WH</sub>		5	-	95	600	ns
			10	-	40	230	
			15	-	25	150	
Max. Clock Input Rise Time Max. Clock Input Fall Time	t <sub>r</sub> t <sub>f</sub>		5	20	-	-	μs
			10	2.5	-	-	
			15	1.0	-	-	
Deviation from 50% Duty Factor (Q, $\overline{Q}$ )		$\frac{t_w(H) - t_w(L)}{t_w(L)} \times 100$ (%)	5	-	±0.2	-	%
			10	-	±0.2	-	
			15	-	±0.1	-	
Input Capacitance	C <sub>IN</sub>			-	5	7.5	pF

WAVEFORMS FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

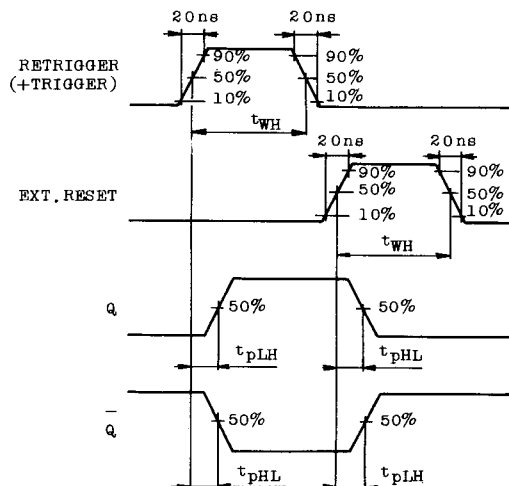
WAVEFORM 1.



WAVEFORM 2.



WAVEFORM 3.



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