

CD4070BM/CD4070BC **Quad 2-Input EXCLUSIVE-OR Gate** CD4077BM/CD4077BC **Quad 2-Input EXCLUSIVE-NOR Gate**

General Description

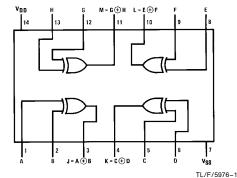
Employing complementary MOS (CMOS) transistors to achieve wide power supply operating range, low power consumption, and high noise margin, the CD4070BM/BC and CD4077BM/BC provide basic functions used in the implementation of digital integrated circuit systems. The N- and P-channel enhancement mode transistors provide a symmetrical circuit with output swing essentially equal to the supply voltage. No DC power other than that caused by leakage current is consumed during static condition. All inputs are protected from damage due to static discharge by diode clamps to $V_{\mbox{\scriptsize DD}}$ and $V_{\mbox{\scriptsize SS}}$.

Features

- 3.0V to 15V ■ Wide supply voltage range ■ High noise immunity 0.45 V_{DD} typ.
- Low power TTL Fan out of 2 driving 74L or 1 driving 74LS compatibility
- CD4070B—Pin compatible to CD4030A
 - -Equivalent to MM54C86/MM74C86 and MC14070B
- CD4077B—Equivalent to MC14077B

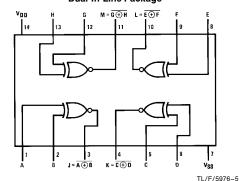
Connection Diagram

CD4070BM/CD4070BC **Dual-In-Line Package**



Top View

CD4077BM/CD4077BC Dual-In-Line Package



Top View

Typical Performance Characteristics

Propagation Delay Time vs Load Capacitance LOAD CAPACITANCE (pF)

CD4070BM/CD4070BC

Truth Tables

Inp	Inputs A B L L L H	Outputs					
Α	В	Υ					
L	L	L					
L	Н	Н					
Н	L	Н					
н	н	1					

Inp	uts	Outputs					
Α	В	Y					
LL		Н					
L	Н	L					
Н	L	L					
Н	Н	Н					

CD4077BM/CD4077BC

TL/F/5976-2

Absolute Maximum Ratings (Notes 1 and 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

DC Supply Voltage (V_{DD}) $-0.5 \text{ to } +18 \text{ V}_{DC}$ Input Voltage (V_{IN}) $-0.5 \text{ to V}_{DD} +0.5 \text{ V}_{DC}$ Storage Temperature Range (T_S) $-65^{\circ}\text{C to } +150^{\circ}\text{C}$

Power Dissipation (PD)

 Dual-In-Line
 700 mW

 Small Outline
 500 mW

Lead Temperature (T_L) (Soldering, 10 seconds) 260°C

Recommended Operating Conditions (Note 2)

DC Supply Voltage (V_{DD}) 3V to 15 V_{DC} Input Voltage (V_{IN}) 0 to V_{DD} V_{DC}

Operating Temperature Range (T_A) CD4070BC/CD4077BC CD4070BM/CD4077BM

-40°C to +85°C -55°C to +125°C

DC Electrical Characteristics CD4070BM/CD4077BM (Note 2)

Symbol	Parameter	Conditions	−55°C		+ 25°C			+ 125°C		Units
			Min	Max	Min	Тур	Max	Min	Max	Onits
I _{DD}	Quiescent Device Current	$V_{DD} = 5V$, $V_{IN} = V_{DD}$ or V_{SS}		0.25			0.25		7.5	μΑ
		$V_{DD} = 10V$, $V_{IN} = V_{DD}$ or V_{SS}		0.5			0.5		15	μΑ
		$V_{DD} = 15V,$ $V_{IN} = V_{DD} \text{ or } V_{SS}$		1.0			1.0		30	μΑ
V _{OL}	Low Level Output Voltage	$ I_O < 1 \mu A$ $V_{DD} = 5V$		0.05		0	0.05		0.05	V
		$V_{DD} = 10V$ $V_{DD} = 15V$		0.05 0.05		0 0	0.05 0.05		0.05 0.05	V V
V _{OH}	High Level Output Voltage	$ I_O < 1 \mu A$ $V_{DD} = 5V$	4.95 9.95		4.95 9.95	5 10		4.95 9.95		V
		$V_{DD} = 10V$ $V_{DD} = 15V$	14.95		14.95	15		14.95		V
V _{IL}	Low Level Input Voltage	$\begin{tabular}{l l l l l l l l l l l l l l l l l l l $		1.5 3.0 4.0			1.5 3.0 4.0		1.5 3.0 4.0	V V
V _{IH}	High Level	I _O < 1 μA		4.0			4.0		4.0	
	Input Voltage	$V_{DD} = 5V$, $V_{O} = 0.5V$ or 4.5V $V_{DD} = 10V$, $V_{O} = 1.0V$ or 9.0V $V_{DD} = 15V$, $V_{O} = 1.5V$ or 13.5V	3.5 7.0 11.0		3.5 7.0 11.0			3.5 7.0 11.0		V V V
l _{OL}	Low Level Output Current (Note 3)	$V_{DD} = 5V, V_{O} = 0.4V$ $V_{DD} = 10V, V_{O} = 0.5V$ $V_{DD} = 15V, V_{O} = 1.5V$	0.64 1.6 4.2		0.51 1.3 3.4	0.88 2.25 8.8		0.36 0.9 2.4		mA mA mA
Гон	High Level Output Current (Note 3)	$V_{DD} = 5V, V_{O} = 4.6V$ $V_{DD} = 10V, V_{O} = 9.5V$ $V_{DD} = 15V, V_{O} = 13.5V$	-0.64 -1.6 -4.2		-0.51 -1.3 -3.4	-0.88 -2.25 -8.8		-0.36 -0.9 -2.4		mA mA mA
I _{IN}	Input Current	$V_{DD} = 15V, V_{IN} = 0V$ $V_{DD} = 15V, V_{IN} = 15V$		-0.1 0.1		-10 ⁻⁵	-0.1 0.1		-1.0 1.0	μA μA

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of "Recommended Operating Conditions" and "Electrical Characteristics" provides conditions for actual device operation.

Note 2: $V_{SS} = 0V$ unless otherwise specified.

Note 3: I_{OL} and I_{OH} are tested one output at a time.

DC Electrical Characteristics CD4070BC/CD4077BC (Note 2)

Symbol	Parameter	Conditions	-40°C		+ 25°C			+85°C		Units
Symbol		Conditions	Min	Max	Min	Тур	Max	Min	Max	Oilles
I _{DD}	Quiescent Device Current	$V_{DD} = 5V,$ $V_{IN} = V_{DD} \text{ or } V_{SS}$		1.0			1.0		7.5	μΑ
		$V_{DD} = 10V$, $V_{IN} = V_{DD}$ or V_{SS}		2.0			2.0		15	μΑ
		$V_{DD} = 15V,$ $V_{IN} = V_{DD} \text{ or } V_{SS}$		4.0			4.0		30	μΑ
V _{OL}	Low Level Output Voltage	$ig I_{O} < 1 \mu A$ $V_{DD} = 5V$		0.05		0	0.05		0.05	V
		$V_{DD} = 10V$ $V_{DD} = 15V$		0.05 0.05		0 0	0.05 0.05		0.05 0.05	V V
V _{OH}	High Level Output Voltage	$ I_O < 1 \mu A$ $V_{DD} = 5V$	4.95 9.95		4.95 9.95	5 10		4.95 9.95		V
		V _{DD} = 10V V _{DD} = 15V	14.95		14.95	15		14.95		V
V _{IL}	Low Level Input Voltage	$ I_O < 1 \mu A$ $V_{DD} = 5V, V_O = 4.5V \text{ or } 0.5V$		1.5 3.0			1.5 3.0		1.5 3.0	V
		$V_{DD} = 10V, V_{O} = 9V \text{ or } 1.0V$ $V_{DD} = 15V, V_{O} = 13.5V \text{ or } 1.5V$		4.0			4.0		4.0	V
V _{IH}	High Level Input Voltage	$ I_O < 1 \mu A$ $V_{DD} = 5V, V_O = 0.5V \text{ or } 4.5V$	3.5		3.5			3.5		V
		$V_{DD} = 10V, V_{O} = 1V \text{ or } 9.0V$ $V_{DD} = 15V, V_{O} = 1.5V \text{ or } 13.5V$	7.0 11.0		7.0 11.0			7.0 11.0		V V
I _{OL}	Low Level Output Current	$V_{DD} = 5V, V_{O} = 0.4V$ $V_{DD} = 10V, V_{O} = 0.5V$	0.52 1.3		0.44 1.1	0.88 2.25		0.36 0.9		mA mA
		$V_{DD} = 15V, V_{O} = 1.5V$	3.6		3.0	8.8		2.4		mA
l _{OH}	High Level Output Current	$V_{DD} = 5V, V_{O} = 4.6V$ $V_{DD} = 10V, V_{O} = 9.5V$ $V_{DD} = 15V, V_{O} = 13.5V$	-0.52 -1.3 -3.6		-0.44 -1.1 -3.0	-0.88 -2.25 -8.8		-0.36 -0.9 -2.4		mA mA mA
I _{IN}	Input Current	V _{DD} = 15V, V _{IN} = 0V V _{DD} = 15V, V _{IN} = 15V	5.3	-0.3 0.3	- 3.3	-10 ⁻⁵	-0.3 0.3		-1.0 1.0	μA μA

AC Electrical Characteristics*

 $T_A=$ 25°C, $C_L=$ 50 pF, $R_L=$ 200k, t_r and $t_f\leq$ 20 ns, unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t _{PHL} or	Propagation Delay Time	$V_{DD} = 5V$		110	185	ns
t _{PLH}	from Input to Output	$V_{DD} = 10V$		50	90	ns
		$V_{DD} = 15V$		40	75	ns
t _{THL} or	Transition Time	$V_{DD} = 5V$		100	200	ns
t _{TLH}		$V_{DD} = 10V$		50	100	ns
		$V_{DD} = 15V$		40	80	ns
C _{IN}	Average Input Capacitance	Any Input		5	7.5	pF
C _{PD}	Power Dissipation Capacitance	Any Input (Note 4)		20		pF

 $^{^*\}mbox{AC}$ Parameters are guaranteed by DC correlated testing.

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of "Recommended Operating Conditions" and "Electrical Characteristics" provides conditions for actual device operation.

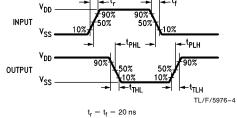
Note 2: $V_{SS} = 0V$ unless otherwise specified.

Note 3: $I_{\mbox{\scriptsize OL}}$ and $I_{\mbox{\scriptsize OH}}$ are tested one output at a time.

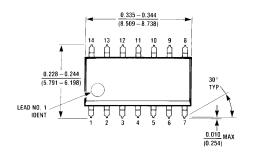
Note 4: C_{PD} determines the no load AC power consumption of any CMOS device. For complete explanation, see 54C/74C Family Characteristics Application Note—AN-90.

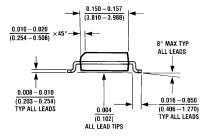
AC Test Circuit and Switching Time Waveforms VIN O VOUT VDD -

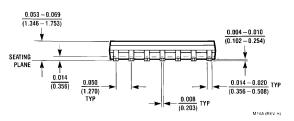
\$TL/F/5976-3\$ Note: Delays measured with input $t_{\rm f},\,t_{\rm f}=20$ ns.





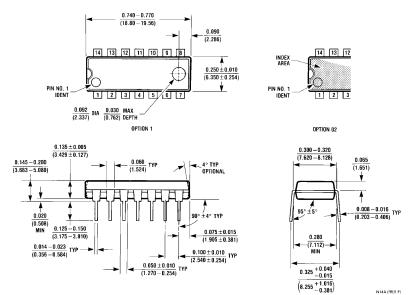






Molded Small Outline Package (M) Order Number CD4070BCM or CD4077BCM NS Package Number M14A

Physical Dimensions inches (millimeters) (Continued)



Molded Dual-In-Line Package (N) Order Number CD4070BCN or CD4077BCN NS Package Number N14A

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