



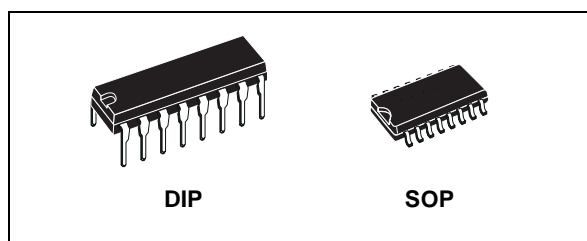
# HCF4512B

## 8 CHANNEL DATA SELECTOR

- 3-STATE OUTPUT
- STANDARDIZED SYMMETRICAL OUTPUT CHARACTERISTICS
- QUIESCENT CURRENT SPECIF. UP TO 20V
- 5V, 10V AND 15V PARAMETRIC RATINGS
- INPUT LEAKAGE CURRENT  
 $I_l = 100\text{nA}$  (MAX) AT  $V_{DD} = 18\text{V}$   $T_A = 25^\circ\text{C}$
- 100% TESTED FOR QUIESCENT CURRENT
- MEETS ALL REQUIREMENTS OF JEDEC JESD13B "STANDARD SPECIFICATIONS FOR DESCRIPTION OF B SERIES CMOS DEVICES"

### DESCRIPTION

HCF4512B is a monolithic integrated circuit fabricated in Metal Oxide Semiconductor technology available in DIP and SOP packages.

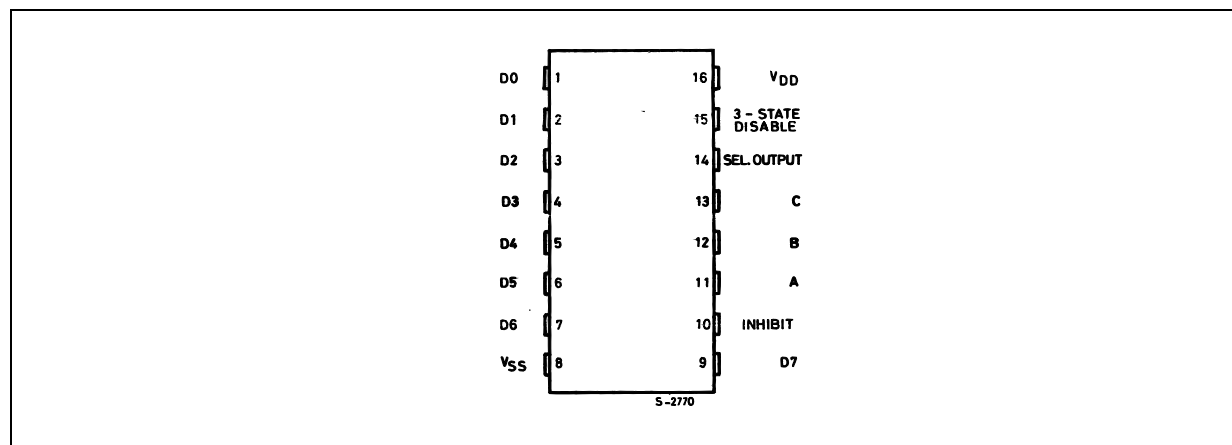


### ORDER CODES

PACKAGE	TUBE	T & R
DIP	HCF4512BEY	
SOP	HCF4512BM1	HCF4512M013TR

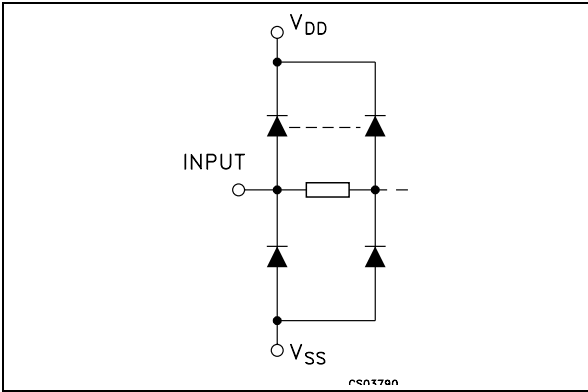
HCF4512B is an 8-channel data selector featuring a three-state output that can interface directly with, and drive, data lines of bus oriented systems.

### PIN CONNECTION



HCF4512B

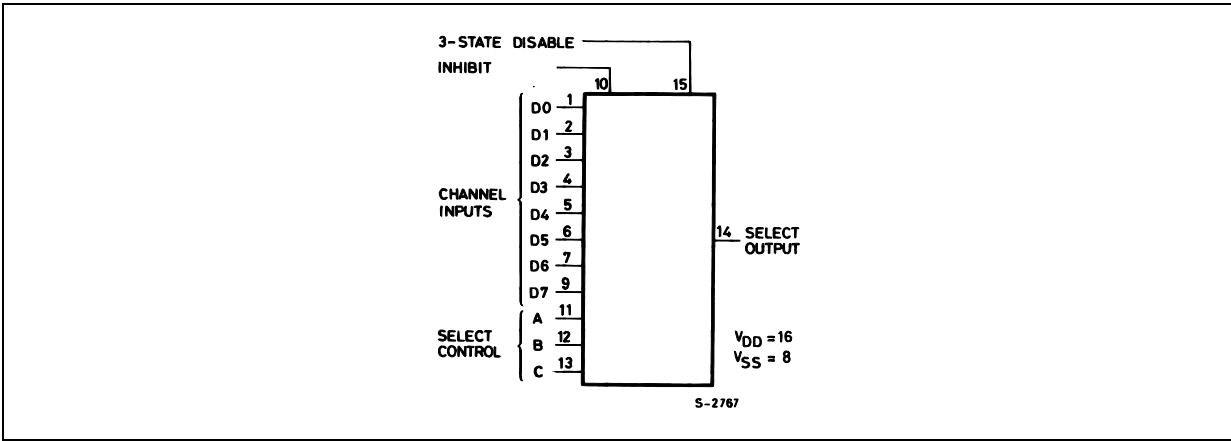
IINPUT EQUIVALENT CIRCUIT



PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1, 2, 3, 4, 5, 6, 7, 9	D0 to D7	Channel Inputs
11, 12, 13	A, B, C	Select Control
10	INHIBIT	Inhibit Output
15	3-STATE DISABLE	3 State Disable Output
14	SEL OUT-PUT	Select Output
8	VSS	Negative Supply Voltage
16	VDD	Positive Supply Voltage

FUNCTIONAL DIAGRAM



TRUTH TABLE

SELECT CONTROL			INHIBIT	3-STATE DISABLE	SELECT OUTPUT
A	B	C			
L	L	L	L	L	D0
H	L	L	L	L	D1
L	H	L	L	L	D2
H	H	L	L	L	D3
L	L	H	L	L	D4
H	L	H	L	L	D5
L	H	H	L	L	D6
H	H	H	L	L	D7
X	X	X	H	L	0
X	X	X	X	H	Z

X : Don't Care  
Z : High Impedance





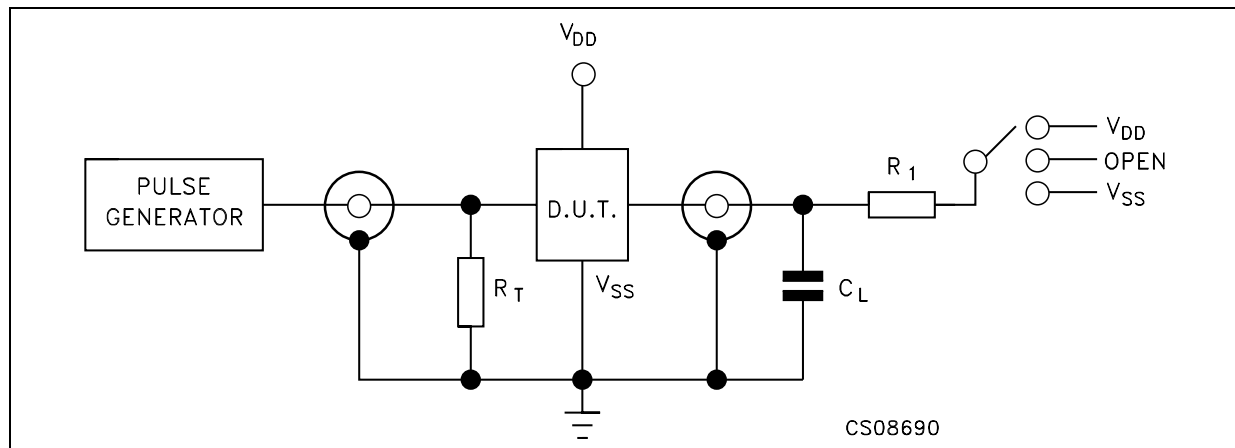
## DC SPECIFICATIONS

Symbol	Parameter	Test Condition				Value							Unit
		V <sub>I</sub> (V)	V <sub>O</sub> (V)	I <sub>O</sub>   (μA)	V <sub>DD</sub> (V)	T <sub>A</sub> = 25°C			-40 to 85°C		-55 to 125°C		
						Min.	Typ.	Max.	Min.	Max.	Min.	Max.	
I <sub>L</sub>	Quiescent Current	0/5			5		0.04	5		150		150	μA
		0/10			10		0.04	10		300		300	
		0/15			15		0.04	20		600		600	
		0/20			20		0.08	100		3000		3000	
V <sub>OH</sub>	High Level Output Voltage	0/5		<1	5	4.95			4.95		4.95		V
		0/10		<1	10	9.95			9.95		9.95		
		0/15		<1	15	14.95			14.95		14.95		
V <sub>OL</sub>	Low Level Output Voltage	5/0		<1	5		0.05			0.05		0.05	V
		10/0		<1	10		0.05			0.05		0.05	
		15/0		<1	15		0.05			0.05		0.05	
V <sub>IH</sub>	High Level Input Voltage		0.5/4.5	<1	5	3.5			3.5		3.5		V
			1/9	<1	10	7			7		7		
			1.5/13.5	<1	15	11			11		11		
V <sub>IL</sub>	Low Level Input Voltage		4.5/0.5	<1	5			1.5		1.5		1.5	V
			9/1	<1	10			3		3		3	
			13.5/1.5	<1	15			4		4		4	
I <sub>OH</sub>	Output Drive Current	0/5	2.5	<1	5	-1.36	-3.2		-1.1		-1.1		mA
		0/5	4.6	<1	5	-0.44	-1		-0.36		-0.36		
		0/10	9.5	<1	10	-1.1	-2.6		-0.9		-0.9		
		0/15	13.5	<1	15	-3.0	-6.8		-2.4		-2.4		
I <sub>OL</sub>	Output Sink Current	0/5	0.4	<1	5	0.44	1		0.36		0.36		mA
		0/10	0.5	<1	10	1.1	2.6		0.9		0.9		
		0/15	1.5	<1	15	3.0	6.8		2.4		2.4		
I <sub>I</sub>	Input Leakage Current	0/18	Any Input		18		±10 <sup>-5</sup>	±0.1		±1		±1	μA
I <sub>OZ</sub>	3-State Output Leakage Current	0/18	0/18		18		±10 <sup>-4</sup>	±0.4		±12		±12	μA
C <sub>I</sub>	Input Capacitance		Any Input				5	7.5					pF

The Noise Margin for both "1" and "0" level is: 1V min. with V<sub>DD</sub>=5V, 2V min. with V<sub>DD</sub>=10V, 2.5V min. with V<sub>DD</sub>=15V

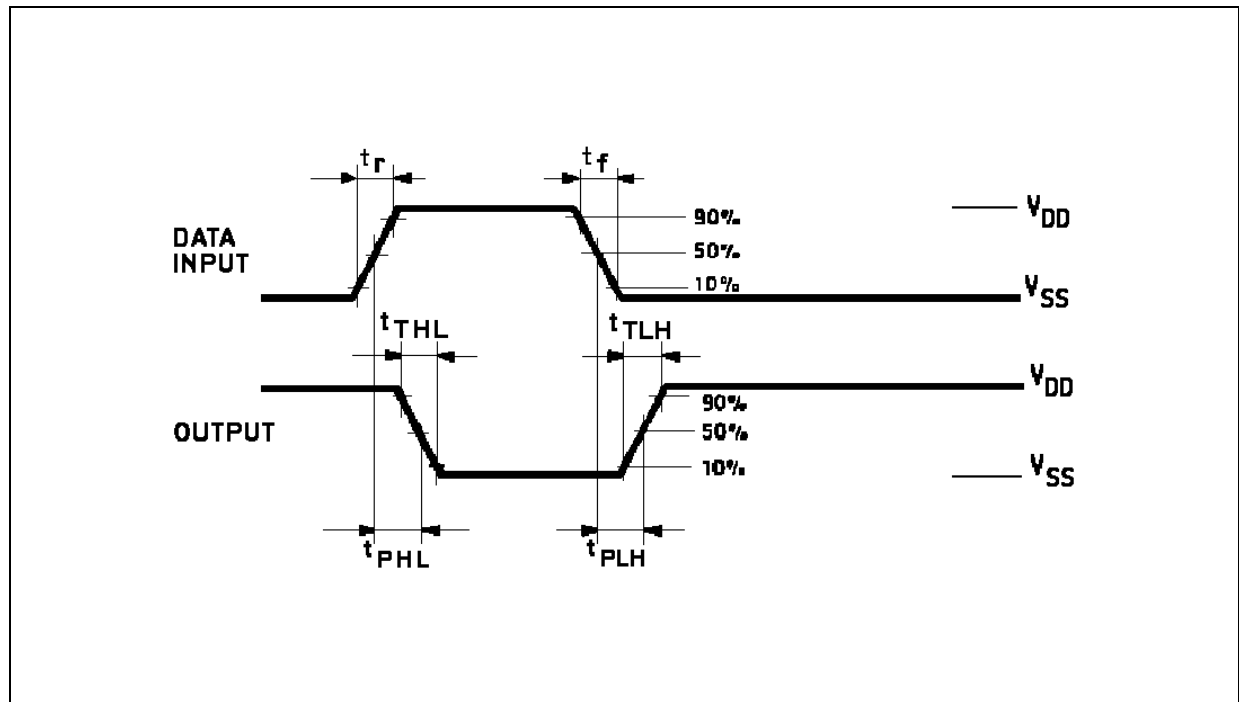
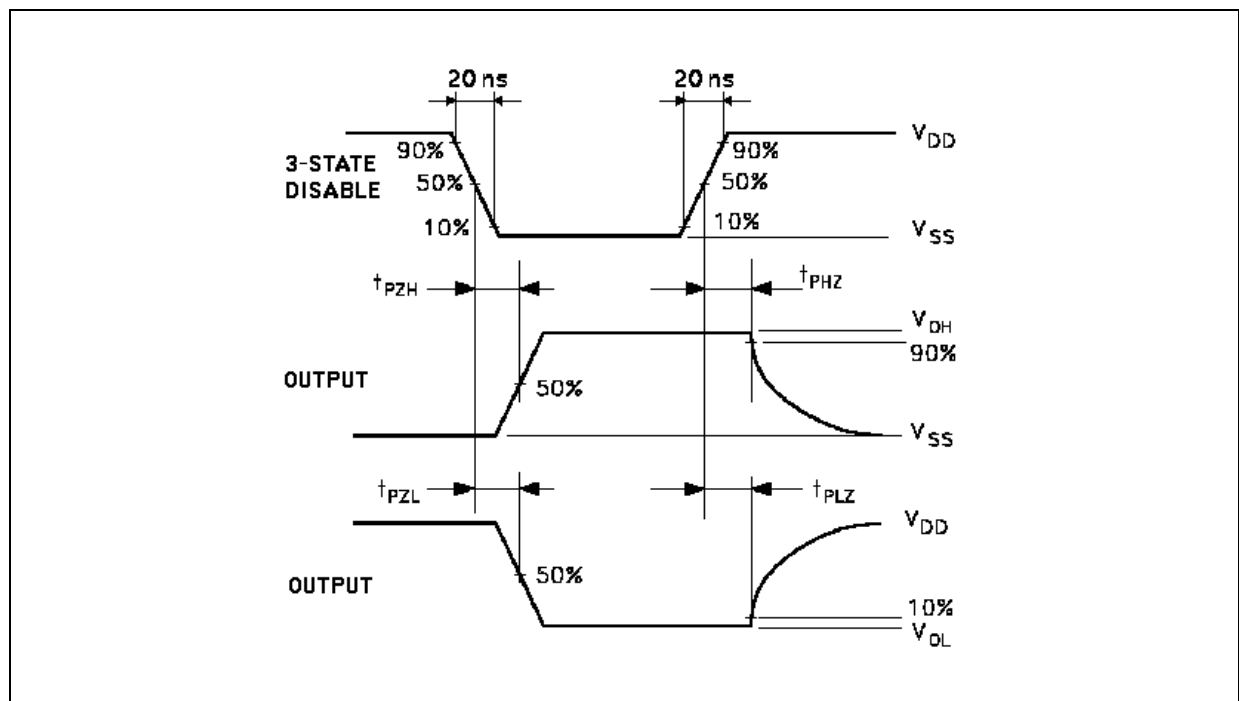
**DYNAMIC ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25^{\circ}\text{C}$ ,  $C_L = 50\text{pF}$ ,  $R_L = 200\text{K}\Omega$ ,  $t_r = t_f = 20\text{ ns}$ )

Symbol	Parameter	Test Condition		Value (*)			Unit
		$V_{DD}$ (V)		Min.	Typ.	Max.	
$t_{PHL}$ $t_{PLH}$	Propagation Delay Time Inhibit to Output	5			140	280	ns
		10			70	140	
		15			50	100	
$t_{PHL}$ $t_{PLH}$	Propagation Delay Time "A" select to Output	5			200	400	ns
		10			85	170	
		15			60	120	
$t_{PHL}$ $t_{PLH}$	Propagation Delay Time Data to Output	5			180	360	ns
		10			75	150	
		15			55	110	
$t_{PZL}$ $t_{PLZ}$ $t_{PHZ}$ $t_{PZH}$	3-State Disable Delay Time	5			60	120	ns
		10			30	60	
		15			20	40	
$t_{THL}$ $t_{TLH}$	Transition Time	5			100	200	ns
		10			50	100	
		15			40	80	

(\*) Typical temperature coefficient for all  $V_{DD}$  value is 0.3 %/ $^{\circ}\text{C}$ .**TEST CIRCUIT**

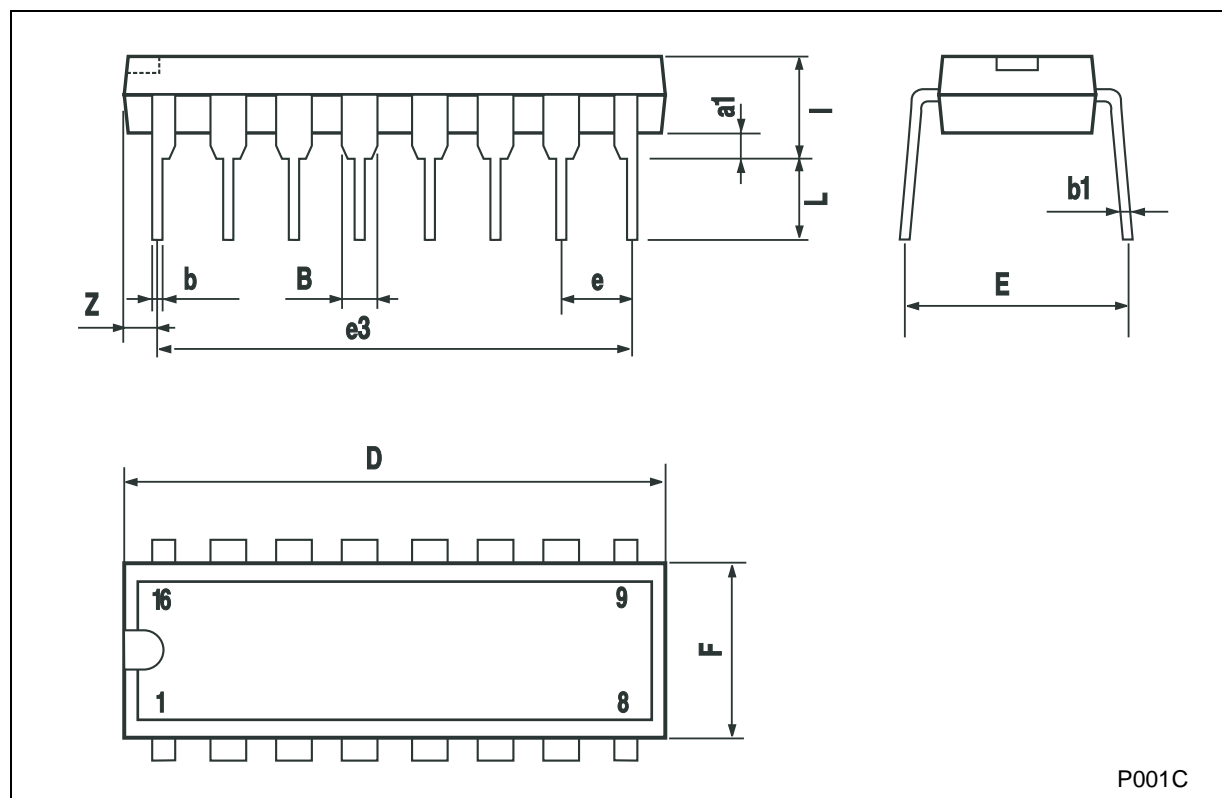
TEST	SWITCH
$t_{PLH}$ , $t_{PHL}$	Open
$t_{PZL}$ , $t_{PLZ}$	$V_{DD}$
$t_{PZH}$ , $t_{PHZ}$	$V_{SS}$

 $C_L = 50\text{pF}$  or equivalent (includes jig and probe capacitance) $R_L = 200\text{K}\Omega$  $R_T = Z_{OUT}$  of pulse generator (typically  $50\Omega$ )

WAVEFORM 1 : PROPAGATION DELAY TIMES ( $f=1\text{MHz}$ ; 50% duty cycle)WAVEFORM 2 : OUTPUT ENABLE AND DISABLE TIMES ( $f=1\text{MHz}$ ; 50% duty cycle)

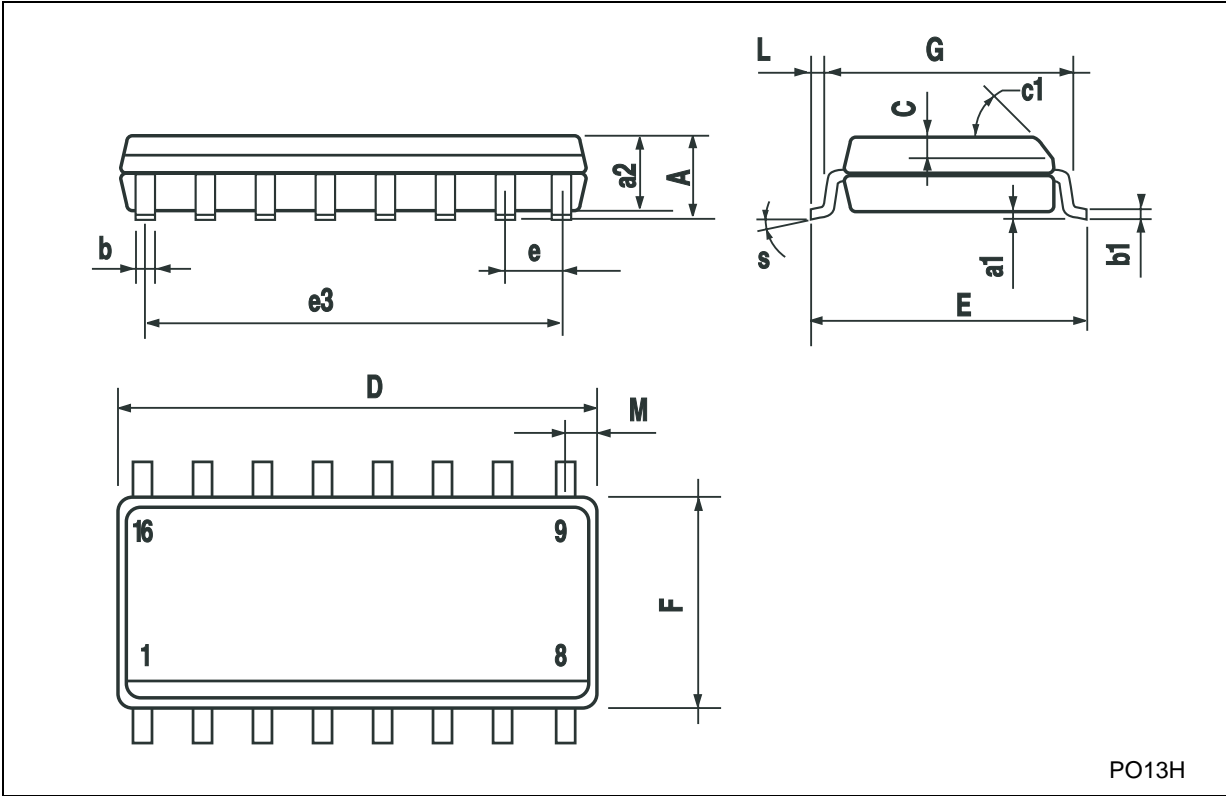
### Plastic DIP-16 (0.25) MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.77		1.65	0.030		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
E		8.5			0.335	
e		2.54			0.100	
e3		17.78			0.700	
F			7.1			0.280
I			5.1			0.201
L		3.3			0.130	
Z			1.27			0.050



SO-16 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			1.75			0.068
a1	0.1		0.2	0.003		0.007
a2			1.65			0.064
b	0.35		0.46	0.013		0.018
b1	0.19		0.25	0.007		0.010
C		0.5			0.019	
c1	45° (typ.)					
D	9.8		10	0.385		0.393
E	5.8		6.2	0.228		0.244
e		1.27			0.050	
e3		8.89			0.350	
F	3.8		4.0	0.149		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.019		0.050
M			0.62			0.024
S	8° (max.)					





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