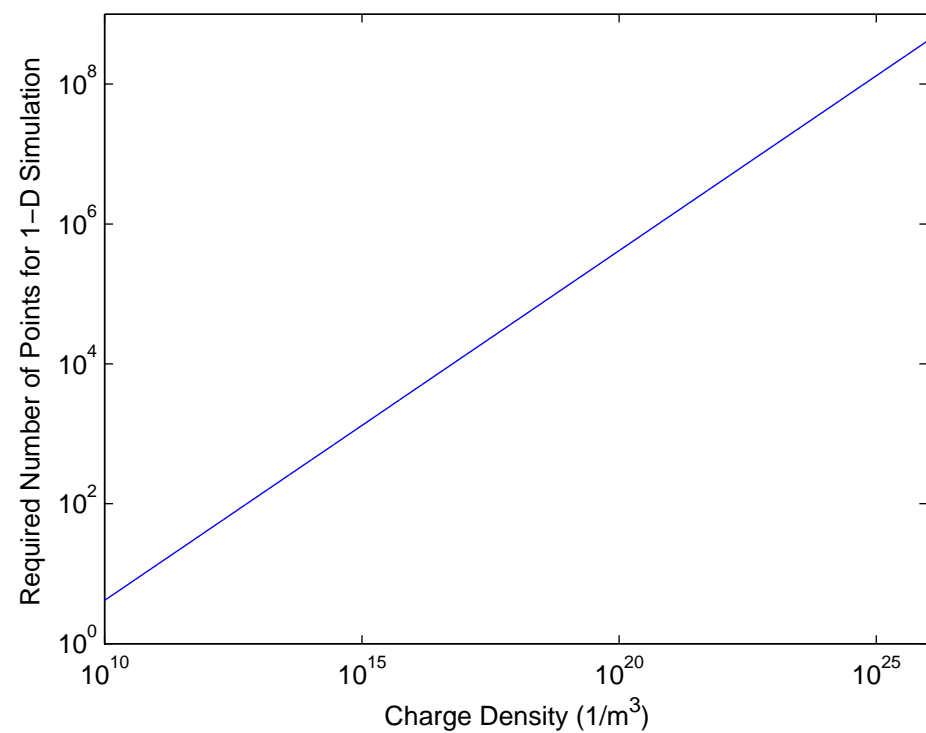
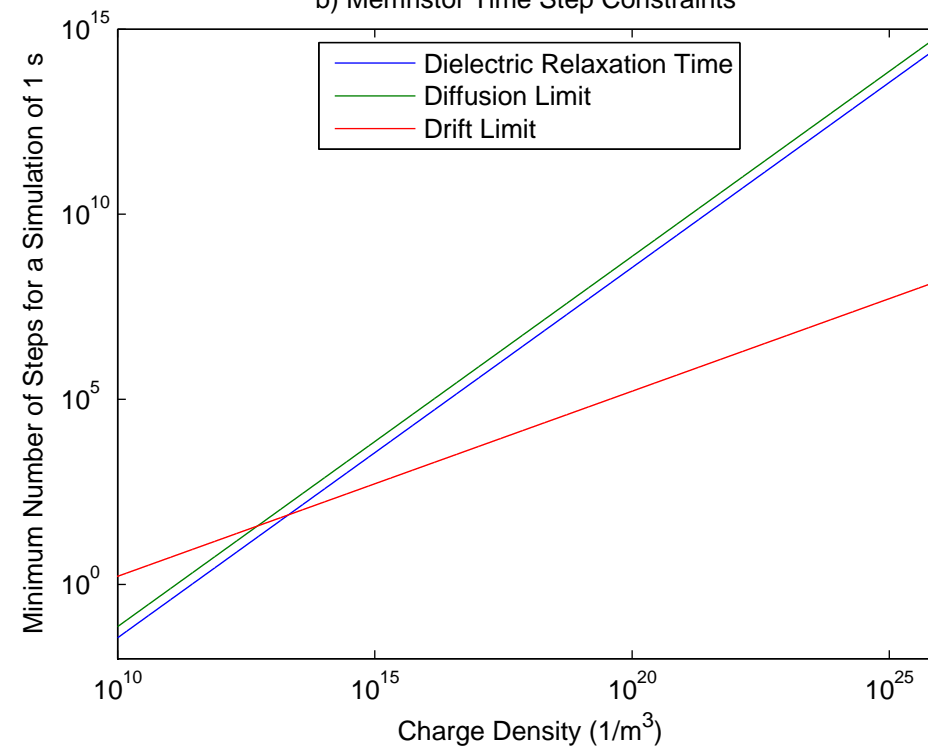


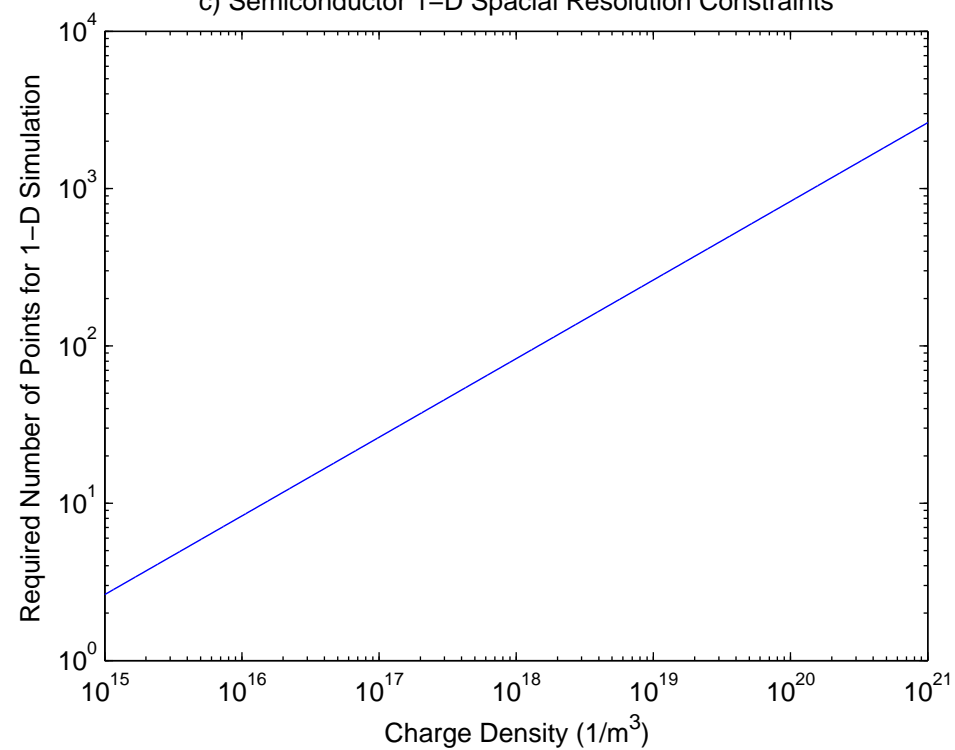
a) Memristor 1-D Spatial Resolution Constraints



b) Memristor Time Step Constraints



c) Semiconductor 1-D Spatial Resolution Constraints



d) Semiconductor Time Step Constraints

