

# CAMILLE BOSSUT

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## EDUCATION

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Georgia Institute of Technology

PhD Candidate of the School of Computer Science

Enrolled: August 2021    Graduating: May 2026

Georgia Institute of Technology

Bachelor of Science in Computer Science

GPA: 3.81

Enrolled: August 2017    Graduated: May 2021

## SKILL

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### Programming Languages:

- Proficient: Java, C, C++
- Intermediate: Python, Matlab, JavaScript, Rust, VHDL, Verilog

**Systems/frameworks:** Mac, Linux, Git, Bash, Docker

**Relevant Coursework:** Honors Algorithms (CS 3511), Advanced Algorithms (CS 4540), Computer Organization and Programming (CS 2110), Computer Systems and Networking (CS 2200), Operating Systems (CS 3210), Advanced Computer Architecture (CS 4290), Graduate Algorithms (CS 6515), Automata and Complexity (CS 4510), Processor Design (CS 3220), Compilers (CS 4240), Advanced Operating Systems (CS 6210), Information Security (CS 6035), Programming Languages (CS 6390), Software Analysis and Testing (CS 6340)

**Spoken Languages:** English (native), French (native)

## WORK EXPERIENCE

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**IBM Research Intern (2021):** Researched software and hardware optimizations and for the Kyber post-quantum cryptography protocol on IBM POWER processors, in both POWER assembly and C, using hardware data collection and simulation.

*Employer: IBM Research*

*Employed: May 24, 2020 – August 13, 2020*

**IBM Research Intern (2020):** Researched optimizations for the SIKE (supersingular isogeny key encapsulation) post-quantum cryptography protocol on IBM POWER processors, in both POWER assembly and C.

*Employer: IBM Research*

*Employed: June 15, 2020 – August 14, 2020*

**Software Intern at Annapolis Microsystems:** Coded in C, C++, and Java. Wrote code for the AMS API, allowing a user to write programs that interact with the ADCs and DACs on the companies' manufactured mezzanine cards. Company designs and manufactures embedded systems and products for DSP and high performance embedded computing.

*Employer: Annapolis Microsystems*

*Employed: May 13, 2019 – August 9, 2019*

**Teaching Assistant — Object Oriented Programming, Computer Organization and Programming:** Taught recitation and mandatory lab weekly on topics including object oriented design and object oriented programming concepts, low level circuits, processor organization. Taught coding in Java, Assembly and C. Participated in creation of course materials (written assessments, lab assignments, and homework).

*Employer: Georgia Institute of Technology*

*Employed: August 20, 2018 – Dec 11, 2020*

## PROJECTS AND RESEARCH

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**Optimizing CFL Reachability:** Analyzing the effects of grammar structure, graph structure, and worklist ordering on the performance of standard CFL-Reachability algorithms, and how to subsequently control these factors for better performance.

**Improving SMT solver Performance:** Determining systematic ways to relax and restrict mathematical statements a solver can't reason about to produce over- or under-approximations and achieve a subsequent sat or unsat result.

**Analysis of LLVM Debug Information:** Analyzing effects of LLVM optimization passes on debug information to improve and correct debug info updates.