

## Education

<b>Georgia Institute of Technology</b> <i>Doctorate of Philosophy in Computer Science (GPA: 4.00 / 4.00)</i>	Expected May 2026 Atlanta, GA
<ul style="list-style-type: none"><li>• <b>Relevant Coursework:</b> Advanced Operating Systems (C++), Information Security (C++), Advanced Software Engineering (React, Java), Graduate Algorithms, Programming Languages, Software Analysis and Testing, Applied Cryptography</li></ul>	
<b>Georgia Institute of Technology</b> <i>Bachelor of Science in Computer Science (GPA: 3.80 / 4.00)</i>	May 2021 Atlanta, GA
<ul style="list-style-type: none"><li>• <b>Relevant Coursework:</b> Computer Organization and Programming (C, Assembly), Computer Systems and Networking (C++), Operating Systems (Rust), Advanced Computer Architecture (C++), Processor Design (VHDL), Compilers (Java), Honors Algorithms, Advanced Algorithms, Automata and Complexity</li></ul>	

## Work Experience

<b>MIT Lincoln Laboratory</b> <i>Research Intern</i>	May 2024 – Aug 2024
<ul style="list-style-type: none"><li>• Developed a C++ image processing pipeline for an embedded system that successfully met constrained timing requirements on resource-constrained hardware (Group 99).</li></ul>	
<b>IBM</b> <i>Research Intern</i>	May 2021 – Aug 2021
<ul style="list-style-type: none"><li>• Developed C and assembly-level optimizations for the Kyber post-quantum cryptography protocol on POWER PC using perf measurements and the POWER PC hardware pipeline simulation.</li></ul>	
<b>IBM</b> <i>Research Intern</i>	June 2020 – Aug 2020
<ul style="list-style-type: none"><li>• Developed assembly optimizations for the SIKE (supersingular isogeny key encapsulation) post-quantum cryptography protocol for the IBM POWER PC architecture.</li></ul>	

## Teaching Experience

<b>Object Oriented Programming, Computer Organization and Programming</b> <i>Undergraduate teaching assistant</i>	2018, 2019, 2020
<ul style="list-style-type: none"><li>• Taught recitation and mandatory lab weekly object oriented programming concepts, low level circuits, and computer architecture. Taught coding in Java, Assembly and C. Held weekly office hours, created and graded course materials (written exams, lab assignments, and homework).</li></ul>	
<b>Compilers and Interpreters, Software Analysis and Testing</b> <i>Graduate teaching assistant</i>	2022, 2023
<ul style="list-style-type: none"><li>• Held weekly office hours, created and graded of course materials (written exams, lab assignments, and homework).</li></ul>	

## Research Projects and Papers

<b>Automated Vectorization of Cryptography Code</b>   C++, Python
<ul style="list-style-type: none"><li>• Improving automatic vectorization of mathematical code, namely cryptographic primitives, through source-code transformations using e-graphs, equality saturation, and LLVM compiler feedback. Presented this work at a programming languages seminar, SERPL, at Augusta University in 2023.</li></ul>
<b>Scalability of Cryptography Code for HLS</b>   C++, Python
<ul style="list-style-type: none"><li>• Improving scalability of high-level synthesized cryptographic primitives for programmable hardware (FPGAs) using automated loop re-rolling and code re-structuring through equality saturation and informed by HLS feedback.</li></ul>
<b>Static Code Analysis Techniques Optimization</b>   C++
<ul style="list-style-type: none"><li>• <i>Context-Free Language Reachability via Skewed Tabulation</i>, Yuxiang Lei*, Camille Bossut*, Yulei Sui and Qirun Zhang (PLDI '24). Optimized algorithms for static program analysis (namely CFL-reachability). Optimized program-graph (CFG) traversal through input grammar analysis and transformation, and optimized traversal ordering for improved data locality and better cache performance. (*equal contribution)</li></ul>

## Technical Skills

**Languages:** C, C++, Rust, Java, Python  
**Concepts:** Compilers, Operating Systems, Virtual Memory, Cache Memory, Encryption, Decryption, Security, Optimization, HLS, Assembly Optimization