Transformer Lab

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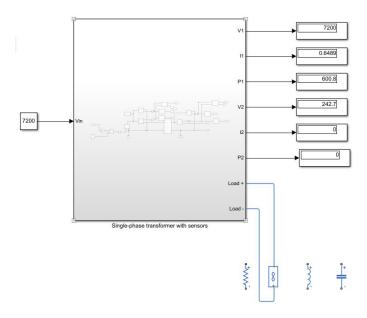
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Var: 4

1.2 Complete m-file with rated parameters of the transformer using nameplate information:

```
% Transformer nameplate information: 37.5 kVA, 7200/240 V, 60 Hz
% Add following parameters to m-file: rated primary voltage, rated secondary vc
% rated primary current, rated secondary current.
rpv=7200;
rsv=240;
rpc=37500/7200;
rsc=37500/240;
```

2.1 Perform open-circuit test (use rated primary voltage):



2.2 Complete m-file with data of the open-circuit test and write a script for calculation of the magnetization branch parameters

(Rc, Xm):

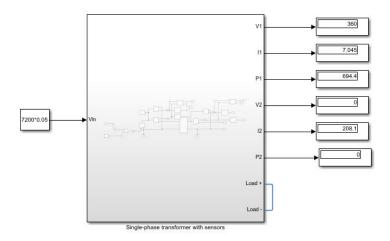
```
%% Open-circuit test
% Simulate the open-circuit test and then add following parameters to m-file: Voc, Ioc, Poc
Vloc = 7200;
Vloc = 242.7;
Ioc = 0.6489;
Poc = 600.8;

% Write script for determination of the magnetization branch parameters (Rc, Xm)
a = Vloc/V2oc; %a=30
Zoc = Vloc/Ioc; %11096
PF = Poc/(Vloc*Ioc); %0.1286
Rc = Zoc * PF; %1426.8
Xm = Zoc * sqrt(1-PF^2); %11004
theta_oc = acos(Poc/(Vloc*Ioc)); %1.4418
```

2.3 Draw phasor diagram for open-circuit test

```
%draw
vVocdZ = V1oc / sqrt(Xm^2+Rc^2) * exp(1i * 0);
vIoc = Ioc * exp(1i * theta_oc);
vIocL = V1oc / Xm * exp(1i * -pi/2);
vIocR = V1oc / Rc * exp(11 * 0);
figure;
hold on;
quiver(0, 0, real(vVocdZ), imag(vVocdZ), 0, 'cyan', 'LineWidth', 3);
quiver(0, 0, real(vIoc), imag(vIoc), 0, 'green', 'LineWidth', 2);
quiver(0, 0, real(vIocL), imag(vIocL), 0, 'blue', 'LineWidth', 2);
quiver(0, 0, real(vIocR), imag(vIocR), 0, 'magenta', 'LineWidth', 2);
hold off;
axis equal;
title("Open Circuit Test Phasor Diagram");
legend("Voc/Z", "Ioc", "IocL", "IocR");
saveas(gcf, "Open_Circuit_Test_Phasor_Diagram.png");
                               Open Circuit Test Phasor Diagram
                                                                                    loc
       1.5
       0.5
         0
       -1.5
```

3.1 Make short-circuit test (you must find the primary voltage value when there are rated current in primary winding):



3.2 Complete m-file with data of the short circuit test and write a script for calculation of the leakage resistances (R_1 , R_2) and reactances (X_{11} , X_{12}):

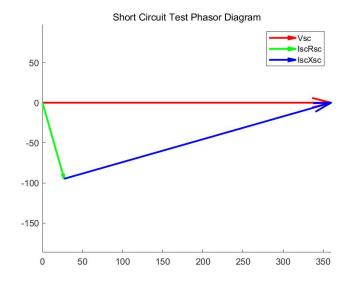
```
%% Short - circuit test
% Simulate the short-circuit test and then add following parameters to m-file: Vsc, Isc, Psc
Vsc = 360;
Isc = 7.045;
Psc = 694.4;

% Write script for determination of the leakage resistances (R1, R2) and reactances (X11, X12)
PF_sc = Psc/(Vsc*Isc);
Z_eq = Vsc/Isc;
R_eq = Psc/Isc^2;
X_eq = sqrt(Z_eq^2-R_eq^2);
a = 7200/240;
R1 = R_eq/2;%6.9955
R2 = R_eq/(2*a^2)%0.0077
X11 = X_eq/2;%24.5737
X12 = X_eq/(2*a^2)%0.0273
```

3.3 Draw phasor diagram for short-circuit test.

```
%draw
theta_sc = acos(PF_sc);

vVsc = Vsc * exp(1i*0);
vVscR = Isc * R_eq *exp(-1i * theta_sc);
vVscX = Isc * X_eq * exp(1i * (pi/2-theta_sc));
figure;
hold on;
quiver(0, 0, real(vVsc), imag(vVsc), 0, 'r', 'LineWidth', 2);
quiver(0, 0, real(vVscR), imag(vVscR), 0, 'green', 'LineWidth', 2);
quiver(real(vVscR), imag(vVscR), real(vVscX), imag(vVscX), 0, 'blue', 'LineWidth', 2);
axis equal;
title("Short Circuit Test Phasor Diagram");
legend("Vsc", "IscRsc", "IscXsc");
saveas(gcf, "Short_Circuit_Test_Phasor_Diagram.png");
```



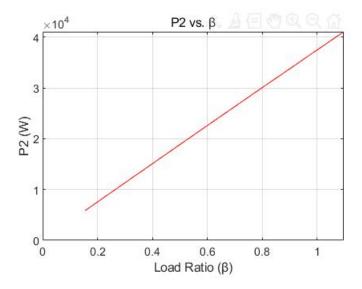
1.2 Complete m-file with data of R-load test

1.3 Calculate load ratio, load power factor, transformer

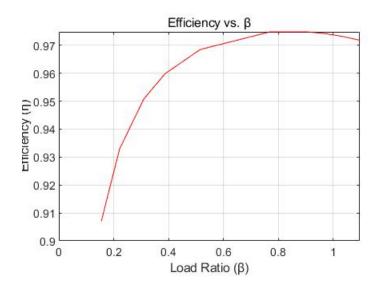
efficiency and voltage regulation:

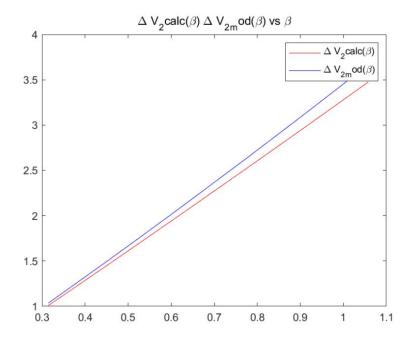
```
% Write script for determination of the load ratio, transformer efficiency,
beta = zeros(size(R1));
power_factor = zeros(size(R1));
efficiency = zeros(size(R1));
voltage_regulation_mod = zeros(size(R1));
voltage_regulation_calc = zeros(size(R1));
theta_r = zeros(size(R1));
for k = 1:length(R1)
    beta(k) = I2_R(k)/rsc;
    power_factor(k) = P2_R(k)/(U2_R(k)*I2_R(k));
    efficiency(k) = P2_R(k) / (P2_R(k) + beta(k)^2 *R_eq *I1_R(k)^2 + Poc);
    theta_r(k) = acos(power_factor(k)); % Calculate phase angle
    voltage_regulation_mod(k) = ((V2oc - U2_R(k))* 100 )/ U2_R(k); % Calculate measured voltage regulation rate
    voltage_regulation_calc(k) = (beta(k) *(Vsc/a) * (cos(theta_sc) * cos(theta_r(k)) + sin(theta_sc) * sin(theta_r(k)))) *100 /U2_R(k);
end
```

1.4 Draw following diagrams: P2(β), PFload(β), $\eta(\beta)$, Δ V%2_calc(β), Δ V%2_mod(β)

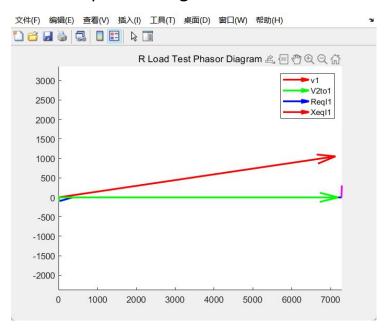








1.5 Draw phasor diagram for one of the tests.

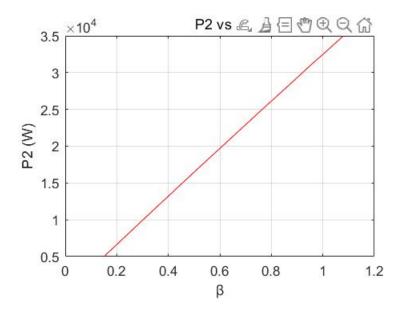


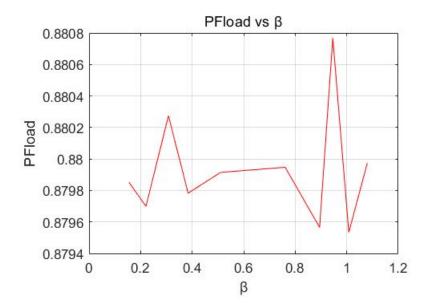
2.2 Complete m-file with data of RL-load test

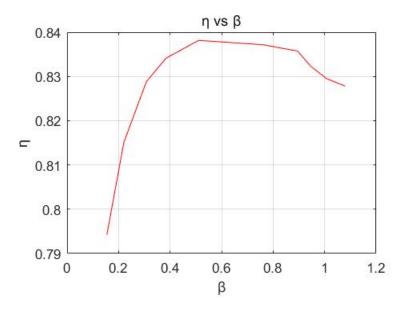
2.3 Calculate load ratio, load power factor, transformer efficiency and voltage regulation:

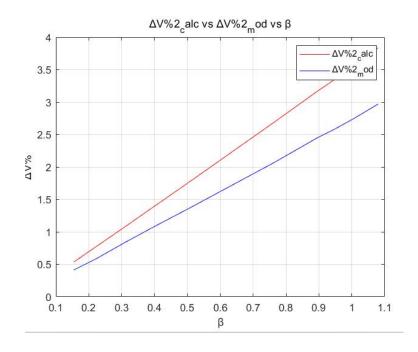
```
% Write script for determination of the load ratio, transformer efficiency,
beta RL = zeros( size(R2));
PF_RL = zeros(size(R2));
theta_RL = zeros(size(R2));
voltage_regulation_mod_RL = zeros(size(R2));
voltage_regulation_mod_RL = zeros(size(R2));
voltage_regulation_acl_c_RL = zeros(size(R2));
for k = 1:length(R2)
beta_RL(k) = 12_RL(k)/Ursc;
PF_RL(k) = P2_RL(k)/Ursc | PF_RL(k)/Crec_regulation_start(k) | P2_RL(k)/Crec_regulation_start(k) | P3_RL(k)/Crec_regulation_start(k) | P3_RL(k)/Crec_r
```

2.4 Draw following diagrams: $P_2(\beta)$, $PF_{load}(\beta)$, $\eta(\beta)$, $\Delta V\%_{2_rad}(\beta)$, $\Delta V\%_{2_mod}(\beta)$

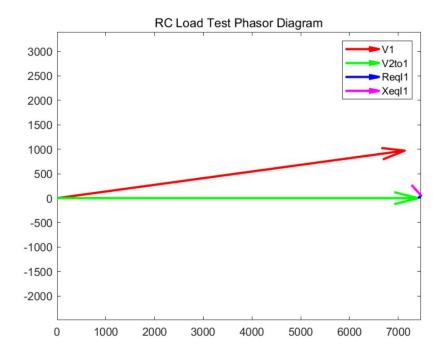








2.5 Draw phasor diagram for one of the tests

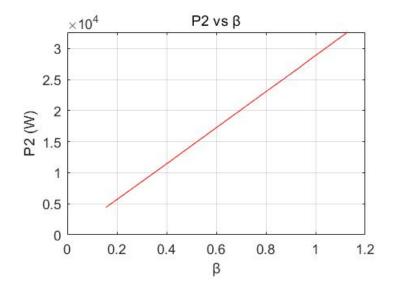


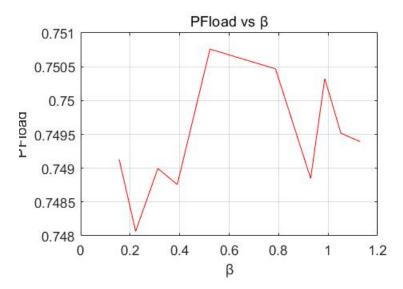
3.2 Complete m-file with data of RC-load test

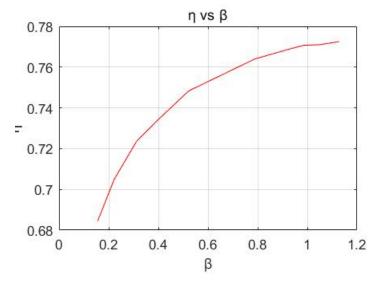
3.3 Calculate load ratio, load power factor, transformer efficiency and voltage regulation:

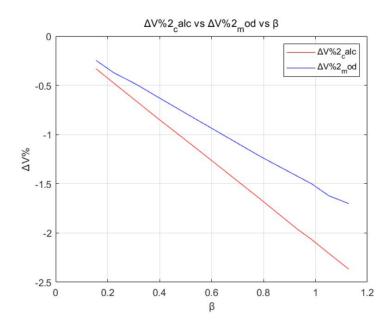
```
% Write script for determination of the load ratio, transformer efficiency,
beta RC = zeros( size(R3));
PF_RC = zeros(size(R3));
theta_RC = zeros(size(R3));
voltage_regulation_mod_RC = zeros(size(R3));
voltage_regulation_calc_RC = zeros(size(R3));
eta_RC = zeros(size(R3));
for k = 1:length(R3)
    beta_RC(k) = 12_RC(k)/rsc;
    PF_RC(k) = P2_RC(k)/(V2_RC(k) + II_RC(k)^2 *R_eq *beta_RC(k)^2 + Poc);
    theta_RC(k) = P2_RC(k)/(V2_RC(k) + II_RC(k)^2 *R_eq *beta_RC(k)^2 + Poc);
    theta_RC(k) = acos(PF_RC(k)); % Calculate phase angle
    voltage_regulation_mod_RC(k) = ((V2oc - U2_RC(k))* 100 )/ U2_RC(k); % Calculate measured voltage regulation rate
    voltage_regulation_calc_RC(k) = (beta_RC(k) *(Vsc/a) * (cos(theta_sc) * cos(theta_sc) * sin(theta_sc) *
```

3.4 Draw following diagrams: $P_2(\beta)$, $PF_{load}(\beta)$, $\eta(\beta)$, $\Delta V\%_{2_rad}(\beta)$, $\Delta V\%_{2_mod}(\beta)$

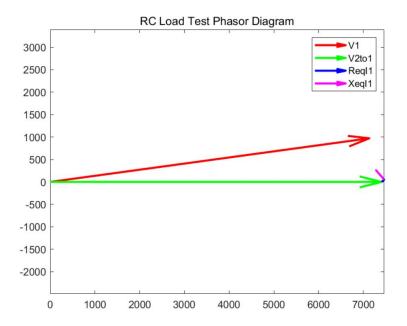








3.5 Draw phasor diagram for one of the tests.



```
a beta RC beta RL C efficiency
                                                             30
[1.0963,1.0240...
[1.1283,1.0515...
[1.0797,1.0080...
[2.8600,2.6700...
[0.9719,0.9734...
[0.7725,0.7710...
          eta_RC
eta_RL
I1_R
I1_RC
I1_RL
I2_R
I2_RC
I2_RL
                                                               [0.8278,0.8295...
[5.9150,5.5360...
[5.6260,5.2240...
                                                              [6.1000,5.7230...
[171.3000,160,...
[176.3000,164....
[168.7000,157....
           loc
lsc
k
L
                                                              0.6489
7.0450
10
[1.7600,1.8900...
                                                               [42130,39390,...
[33700,31400,...
[36060,33730,...
           P1_R
P1_RC
P1_RL
P2_R
P2_RC
P2_RL
PF
PF_1_r
PF_1_rL
PF_RC
PF_RC
PF_RL
PF_sc
                                                               [41060.38380....
                                                              [32620,30380,...
[34990,32720,...
0.1286
                                                              0.9892
0.8210
[0.7494,0.7495...
                                                               [0.8800,0.8795...
                                                              0.2738
600.8000
[0.9996,0.9995...
           poc
power_factor
Psc
R1
R2
R3
                                                               694.4000
[1.4000,1.5000...
[1.2300,1.3200...
[1.0500,1.1250...
                                                               13.9910
1.4268e+03
5.2083
           R_eq
Rc
            rpc
         rpv
rsc
rsc
S_1_rL
theta_oc
theta_prime_r
theta_prime...
theta_RC
theta_RC
theta_SC
U1_R
U1_RC
U1_RL
U2_RC
U2_RC
U2_RC
U2_RL
V1oc
V2oc
vloc
vloc
vloc
vlocR
                                                              7200
156.2500
240
42588
                                                               43920
                                                              1.4418
0.1468
                                                               0.6076
                                                              [0.0294 + 0.00...
[0.7236,0.7235...
[0.4950,0.4959...
                                                                1.2935
                                                              [7200,7200,72...
[7200,7200,72...
[7200,7200,72...
                                                              [239.8000,240,...
[246.9000,246....
[235.7000,236....
                                                               7200
                                                              7200
242.7000
0.0834 + 0.6435i
0.0000 - 0.6543i
                                                               5.0461
vlock
voltage_reg...
vVReqI1R
vVsc
vVIR
vV2to1R
vV2to1R
vV2to1R
vV2to1R
vV3cX
vVsc
vVsc
vVscX
vVscX
vXscR
vXscX
vXscq11R
vXeq11RL
xX11
xX12
x eq
xm
Z eq
7oc
                                                              5.0461
[1.6565 + 0.00...
[-2.3671,-2.206...
[3.8358,3.5769...
                                                             [3.8358,3.5769...
[1.2093,1.1250...
[-1.7011,-1.621...
[2.9699,2.7519...
82.7300 - 2.43...
75.1085 - 40.5...
360
7.1226e+03 + ...
                                                              3.6035e+03 + ...
239.8000
235.7000
7194
                                                               7071
0.6489
                                                               360
26.9870 - 94.7...
                                                               3.3301e+02 + ...
8.5427e+00 + ...
                                                              2.1664e-05 + 2...
24.5737
0.0273
49.1474
                                                               1.1004e+04
                                                               51.1001
1 1096e+04
```

Conclusion:

This laboratory exercise focused on investigating the impact of different load types on transformer performance, specifically voltage regulation. We conducted tests with resistive (R-load), resistive-inductive (RL-load), and resistive-capacitive (RC-load) loads and compared the results with theoretical calculations and Simulink models.

The experiments demonstrated a strong correlation between theoretical and modeled values, validating the accuracy of our approach. However, some discrepancies were observed, particularly in the case of RC-loads, which could be attributed to potential errors in model parameters or assumptions.

Load Type Impact: Different load types significantly affect transformer performance:

R-loads (Resistive): Most efficient with a power factor of 1, resulting in minimal voltage drops and stable operation.

RL-loads (Resistive-Inductive): Lagging power factor due to inductive component leads to increased voltage drops and reduced efficiency.

RC-loads (Resistive-Capacitive): Leading power factor due to capacitive component can cause voltage rise and impact transformer efficiency.

Waveform Characteristics:

RL-loads: Current lags behind voltage due to inductive component, leading to distorted waveforms and increased losses.

RC-loads: Current leads the voltage due to capacitive component, causing similar waveform distortions and increased losses.

Efficiency and Voltage Regulation:

R-loads: Efficiency increases initially with load ratio and then decreases due to increased copper losses.

RL-loads: Similar trend as R-loads but with a slightly lower peak efficiency.

RC-loads: Efficiency increases initially but experiences a sharp drop due to the leading power factor.

Voltage Regulation:

R- and RL-loads: Positive and increase proportionally with load ratio.

RC-loads: Negative and decrease with load ratio, with some discrepancies observed between calculated and modeled values.