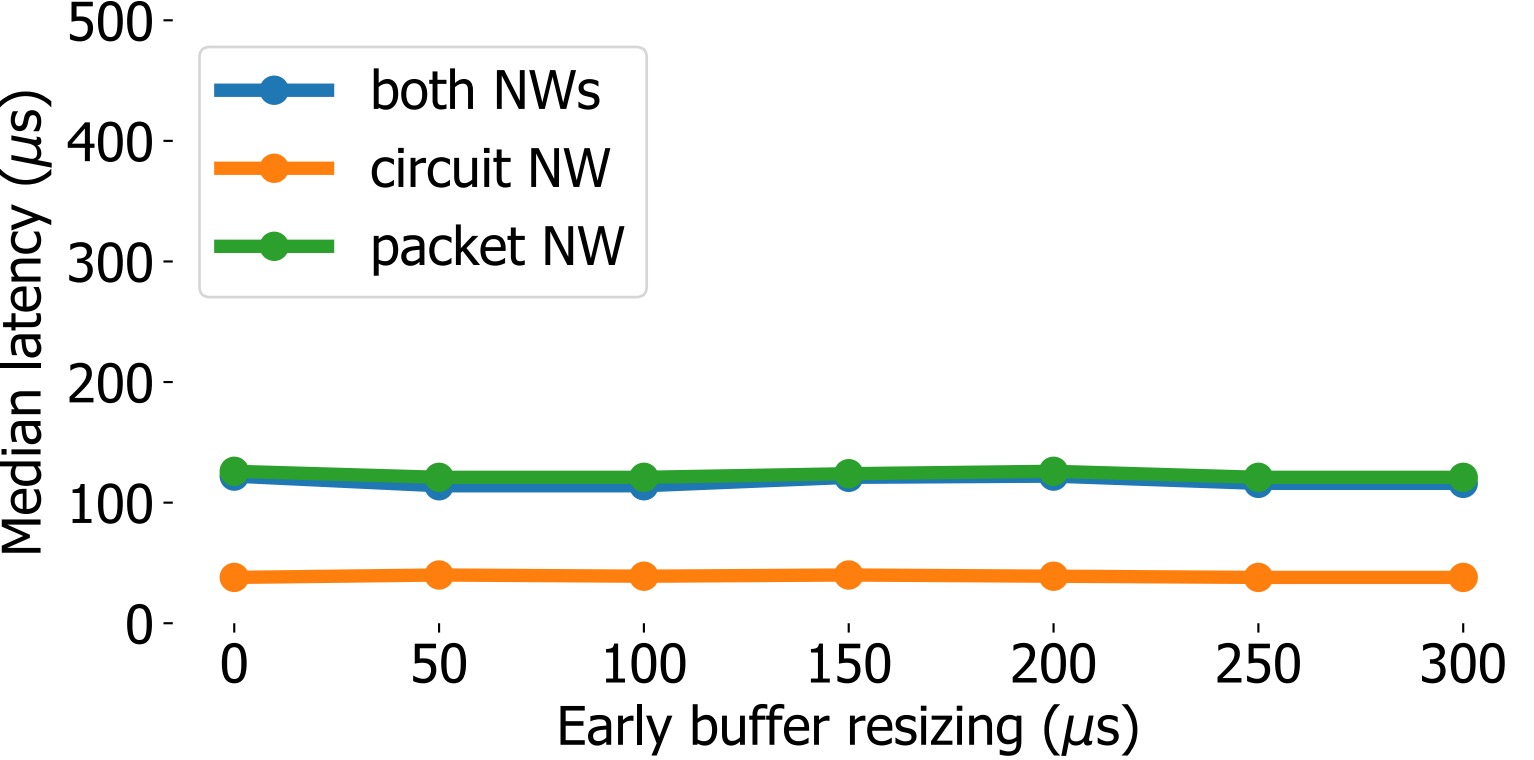
500
400
300
200
100
0
Median latency (μs)
both NWs
circuit NW
packet NW
0
50
100
150
200
250
300
Early buffer resizing (μs)