

Company Name: GigaDevice

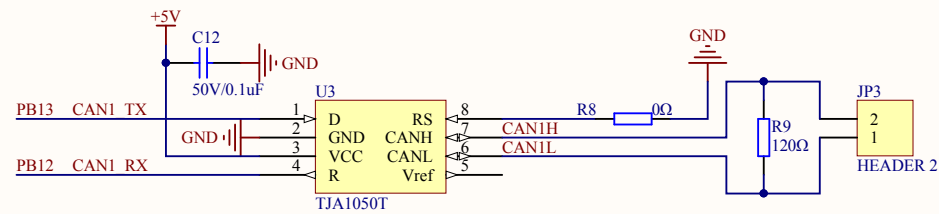
File Name: ADC

Revision: 1.0

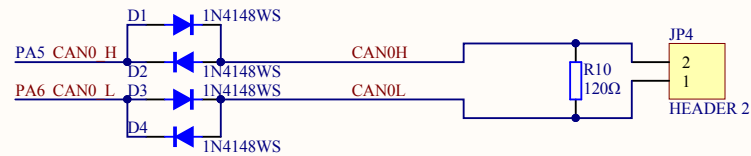
Data: 2015-11

Author: Xufei

CAN



PA5 is an AFIO, please refer to DAC schematic for right config



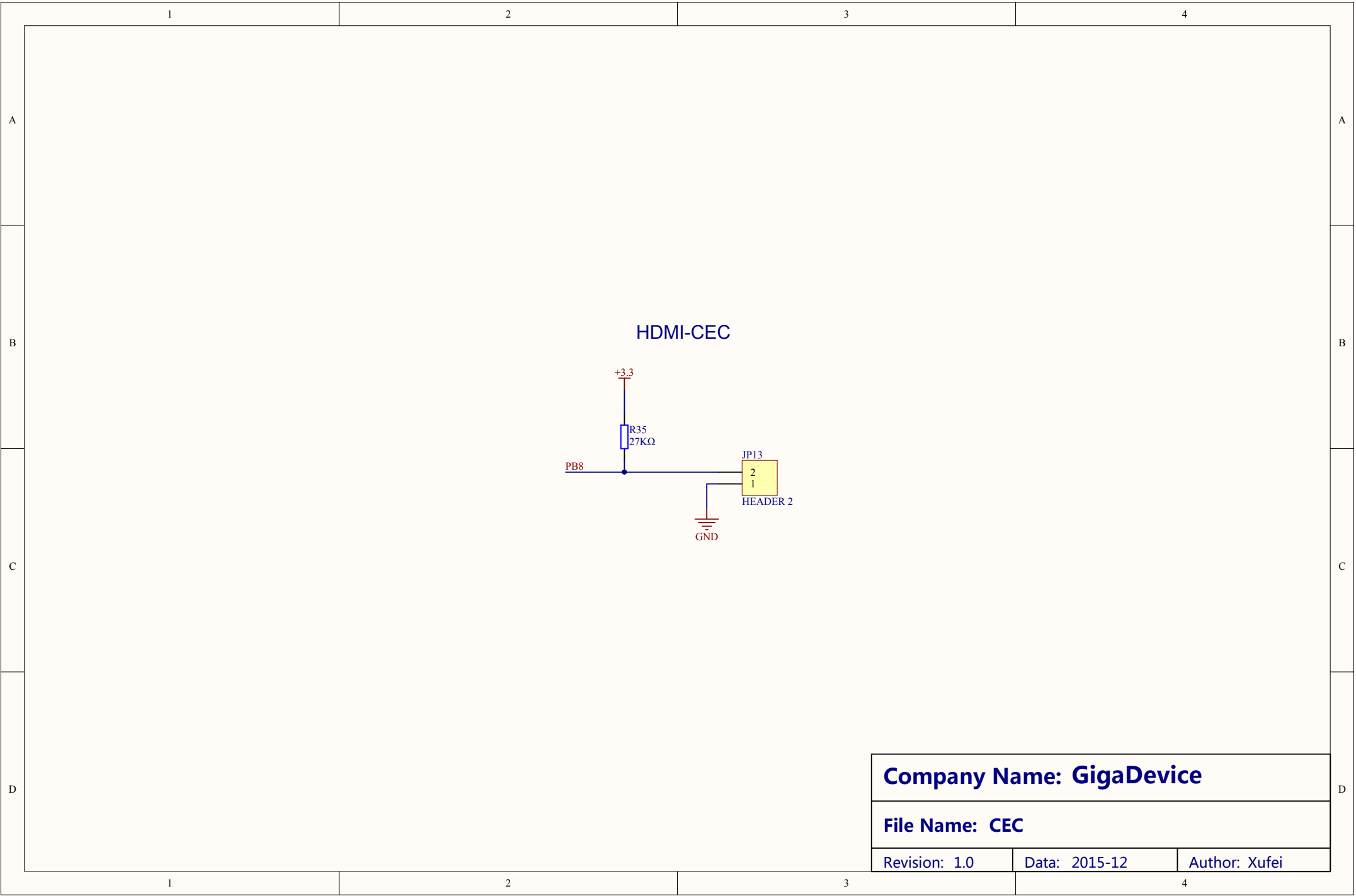
Company Name: GigaDevice

File Name: CAN

Revision: 1.0

Data: 2015-11

Author: Xufei



1

2

3

4

A

A

B

B

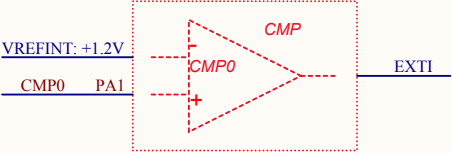
C

C

D

D

CMP



Company Name: GigaDevice

File Name: CMP

Revision: 1.0

Data: 2015-11

Author: Xufei

1

2

3

4

1

2

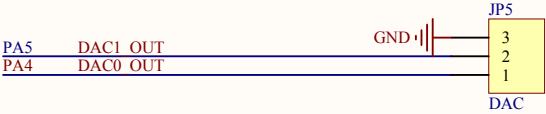
3

4

A

A

DAC



Short JP6(1,2) for DAC function
Short JP6(2,3) for CAN0 function



B

B

C

C

D

D

Company Name: GigaDevice

File Name: DAC

Revision: 1.0

Data: 2015-11

Author: Xufei

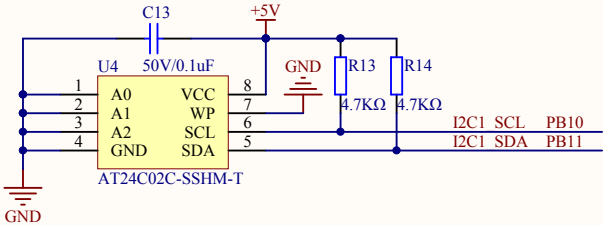
1

2

3

4

I2C



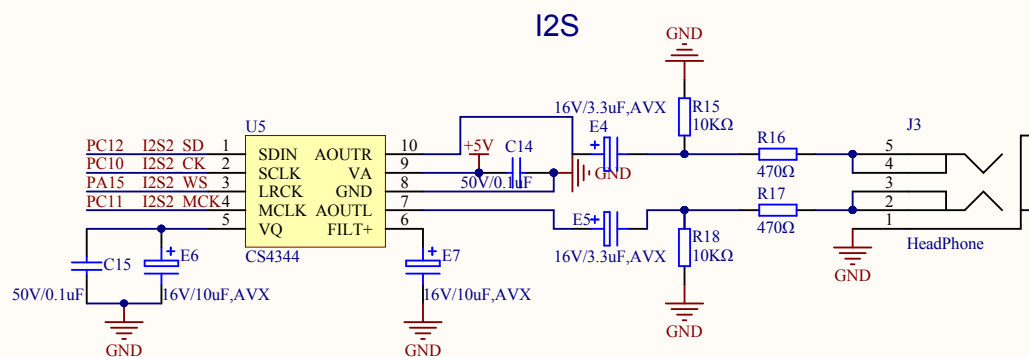
Company Name: GigaDevice

File Name: I2C

Revision: 1.0

Data: 2015-11

Author: Xufei



Company Name: GigaDevice

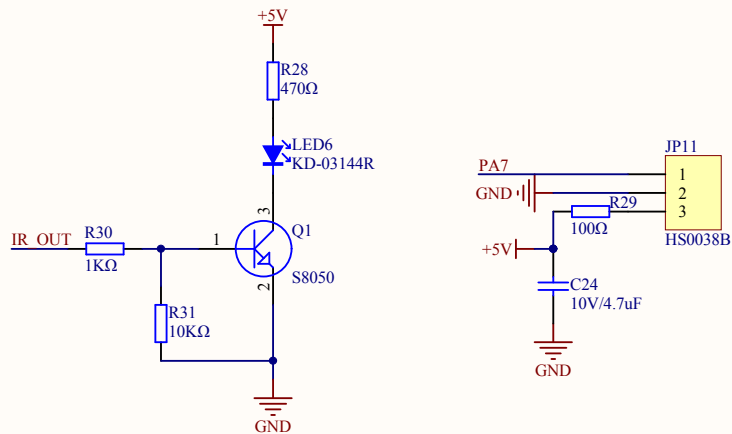
File Name: I2S

Revision: 1.0

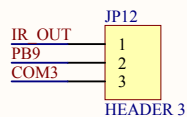
Data: 2015-11

Author: Xufei

IFRR



Short JP12(1,2) for IFRR function
Short JP12(2,3) for SLCD function



Company Name: GigaDevice

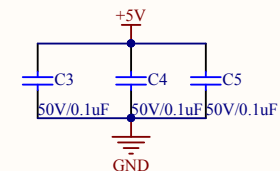
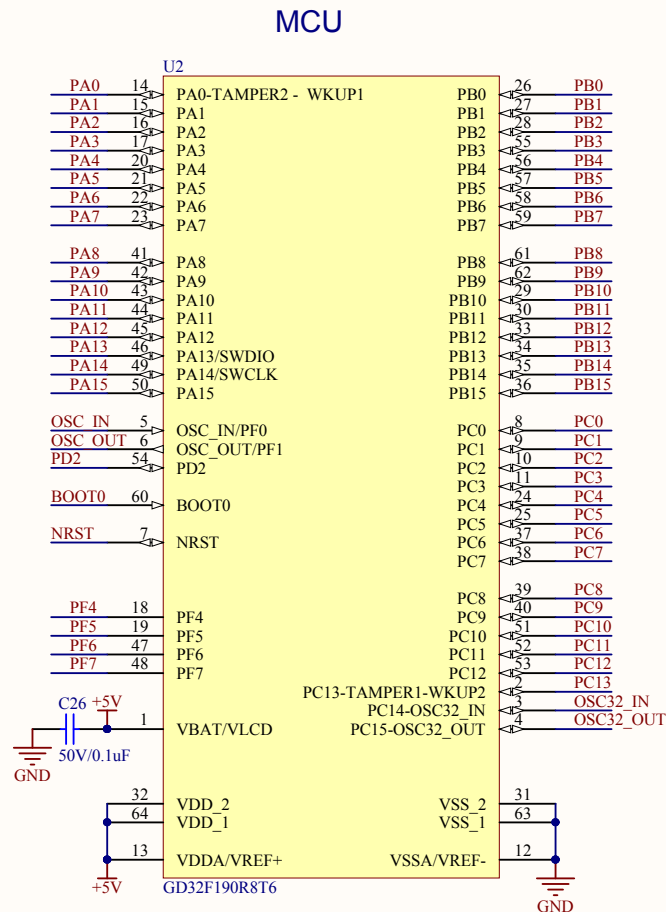
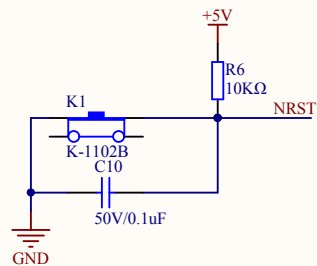
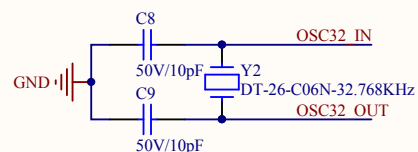
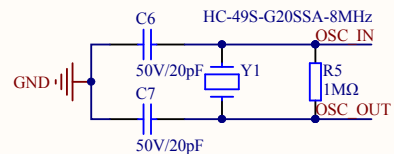
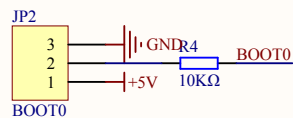
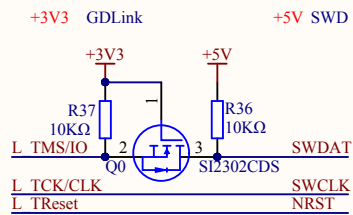
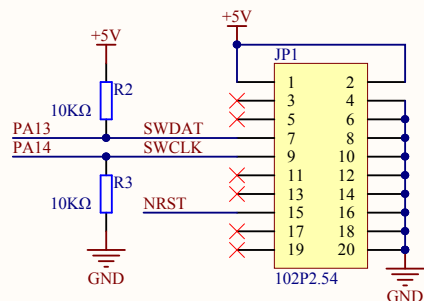
File Name: IFRR

Revision: 1.0

Data: 2015-11

Author: Xufei

	1	2	3	4
A		<div>U_ADC ADC.SchDoc</div> <div></div>	<div>U_IFRR IFRR.SchDoc</div> <div></div>	<div>U_SPI SPI.SchDoc</div> <div></div>
B		<div>U_CAN CAN.SchDoc</div> <div></div>	<div>U_SLCD SLCD.SchDoc</div> <div></div>	<div>U_TSI TSI.SchDoc</div> <div></div>
		<div>U_CMP CMP.SchDoc</div> <div></div>	<div>U_MCU MCU.SchDoc</div> <div></div>	<div>U_USART USART.SchDoc</div> <div></div>
C		<div>U_DAC DAC.SchDoc</div> <div></div>	<div>U_OPA OPA.SchDoc</div> <div></div>	<div>U_USART CEC.SchDoc</div> <div></div>
		<div>U_I2C I2C.SchDoc</div> <div></div>	<div>U_Peripherals Peripherals.SchDoc</div> <div></div>	
		<div>U_I2S I2S.SchDoc</div> <div></div>	<div>U_POWER POWER.SchDoc</div> <div></div>	
D				<div>Company Name: GigaDevice</div> <div>File Name: MAIN</div> <div>Revision: 1.0</div> <div>Data: 2015-11</div> <div>Author: Xufei</div>
	1	2	3	4



Company Name: GigaDeice

File Name: MCU

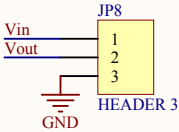
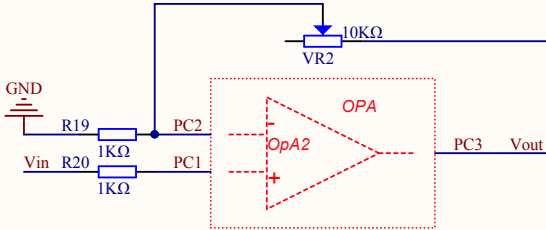
Revision: 1.0

Data: 2015-11

Author: Xufei

OPA

OPA2	VINM	PC2
OPA2	VINP	PC1
OPA2	VOUT	PC3



Company Name: GigaDevice

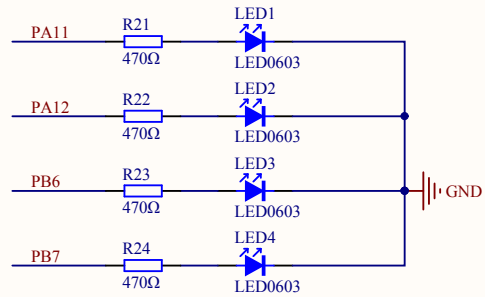
File Name: OPA

Revision: 1.0

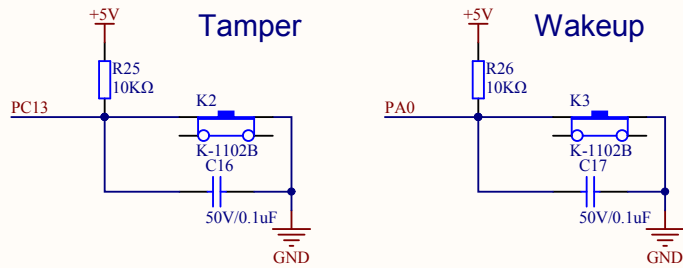
Data: 2015-11

Author: Xufei

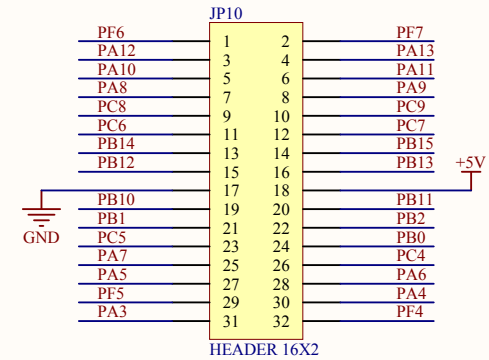
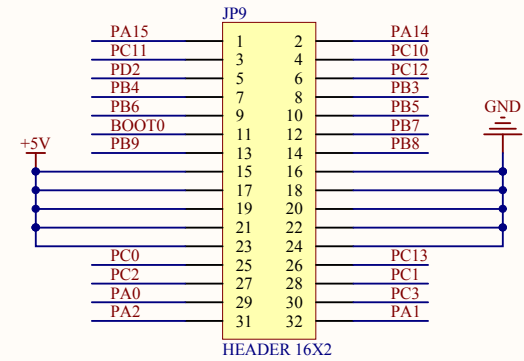
LED



Button KEY



Extention



Company Name: GigaDevice

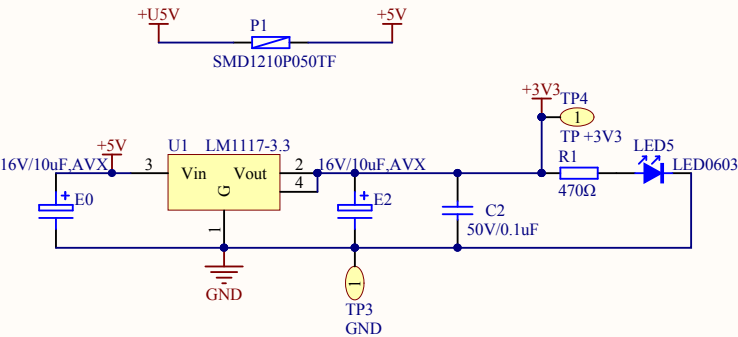
File Name: Peripherals

Revision: 1.0

Data: 2015-11

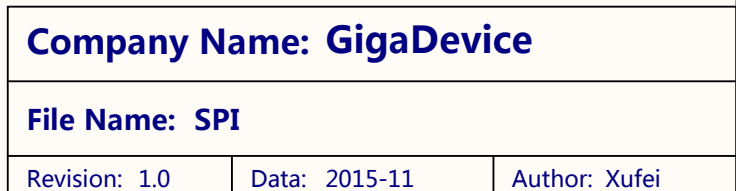
Author: Xufei

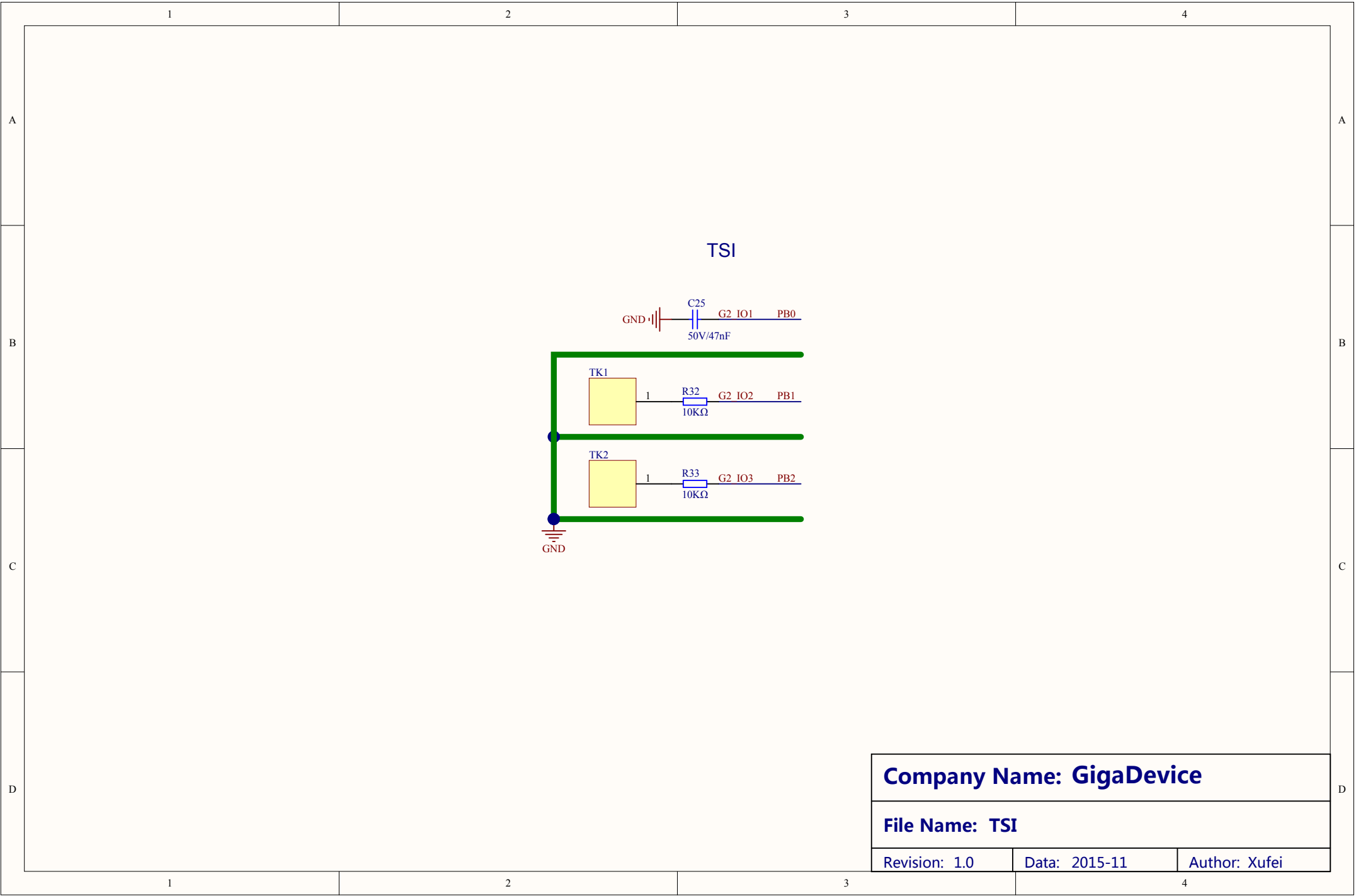
POWER



Company Name: GigaDevice		
File Name: POWER		
Revision: 1.2	Data: 2016-3	Author: Xufei

1	2	3	4																																																																																	
A				A																																																																																
B	<div>SLCD</div> <div>PB9 is an AFIO, please refer to IFRR schematic for right config PA9,PA10 are AFIOs, please refer to USART schematic for right config</div> <div><table><tr><td>PA8</td><td>COM0</td><td>R00</td><td>10KΩ</td><td>1</td></tr><tr><td>PA9</td><td>COM1</td><td>R01</td><td>10KΩ</td><td>2</td></tr><tr><td>PA10</td><td>COM2</td><td>R02</td><td>10KΩ</td><td>3</td></tr><tr><td>PB9</td><td>COM3</td><td>R03</td><td>10KΩ</td><td>4</td></tr><tr><td></td><td>PB14</td><td>SEG14</td><td></td><td>5</td></tr><tr><td></td><td>PB15</td><td>SEG15</td><td></td><td>6</td></tr><tr><td></td><td>PC4</td><td>SEG22</td><td></td><td>7</td></tr><tr><td></td><td>PC5</td><td>SEG23</td><td></td><td>8</td></tr><tr><td></td><td>PC6</td><td>SEG24</td><td></td><td>9</td></tr><tr><td></td><td>PC7</td><td>SEG25</td><td></td><td>10</td></tr><tr><td></td><td>PC8</td><td>SEG26</td><td></td><td>11</td></tr><tr><td></td><td>PC9</td><td>SEG27</td><td></td><td>12</td></tr><tr><td></td><td>PF4</td><td>SEG28</td><td></td><td>13</td></tr><tr><td></td><td>PF5</td><td>SEG29</td><td></td><td>14</td></tr><tr><td></td><td>PF6</td><td>SEG30</td><td></td><td>15</td></tr><tr><td></td><td>PF7</td><td>SEG31</td><td></td><td>16</td></tr></table></div> <div>JP7</div> <div>HEADER 16</div>			PA8	COM0	R00	10KΩ	1	PA9	COM1	R01	10KΩ	2	PA10	COM2	R02	10KΩ	3	PB9	COM3	R03	10KΩ	4		PB14	SEG14		5		PB15	SEG15		6		PC4	SEG22		7		PC5	SEG23		8		PC6	SEG24		9		PC7	SEG25		10		PC8	SEG26		11		PC9	SEG27		12		PF4	SEG28		13		PF5	SEG29		14		PF6	SEG30		15		PF7	SEG31		16	B
PA8	COM0	R00	10KΩ	1																																																																																
PA9	COM1	R01	10KΩ	2																																																																																
PA10	COM2	R02	10KΩ	3																																																																																
PB9	COM3	R03	10KΩ	4																																																																																
	PB14	SEG14		5																																																																																
	PB15	SEG15		6																																																																																
	PC4	SEG22		7																																																																																
	PC5	SEG23		8																																																																																
	PC6	SEG24		9																																																																																
	PC7	SEG25		10																																																																																
	PC8	SEG26		11																																																																																
	PC9	SEG27		12																																																																																
	PF4	SEG28		13																																																																																
	PF5	SEG29		14																																																																																
	PF6	SEG30		15																																																																																
	PF7	SEG31		16																																																																																
C				C																																																																																
D	<div>Company Name: GigaDevice</div> <div>File Name: SLCD</div> <div>Revision: 1.0</div> <div>Data: 2015-11</div> <div>Author: Xufei</div>			D																																																																																
1	2	3	4																																																																																	





[illegible]

Author: Xufei

