











Revision History

PCB Revision 2.0 (See PCB Revision 1.0 for Previous Revision History)

09.26.2010

Added eZ430-FET to F5529 Voltage-Level Translator (TXS0104E). Added Alternate LDO (TPS73533) Option for Vcc System Power. Added DOGS 102x64 Dot-Matrix LCD and White LED Backlight. Added LED Backlight Driver (TPS75105). Modified power selection for Vcc and DVcc. Removed extra headers and prototype area. Extensive PCB Layout Rearrangement. Modified RF Headers for Additional EVM Capability.

TITLE: MSP-EXP430F5529_2_0

Document Number: REU: 2.0

Date: 10/19/2010 4:35:40 PM Sheet: 7/7