3.1.19 Use nodal analysis to find V_1 in the circuit in Fig. P3.1.19.

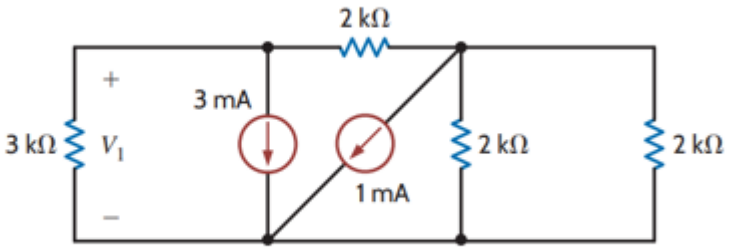

FIGURE P3.1.19

3.1.40 Use nodal analysis to find V_o in the circuit in Fig. P3.1.40.

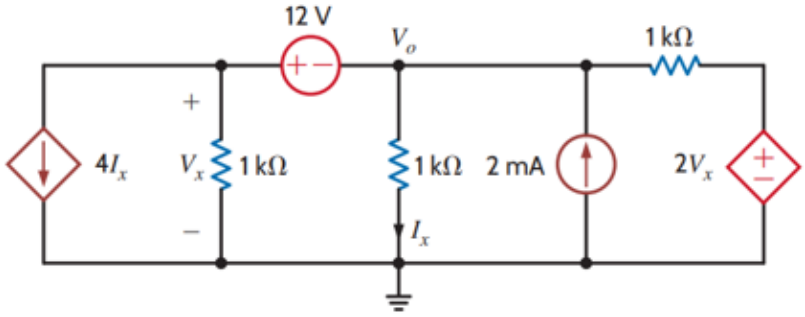

FIGURE P3.1.40

3.1.45 Use nodal analysis to determine the node voltages defined in the circuit in Fig. P3.1.45.

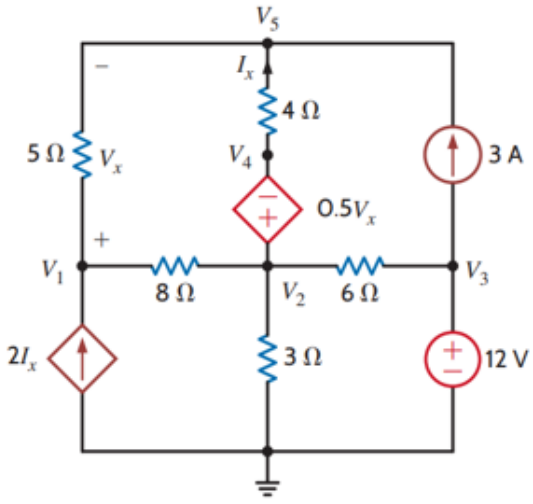

FIGURE P3.1.45

3.2.15 Using loop analysis, find V_o in the circuit in Fig. P3.2.15.

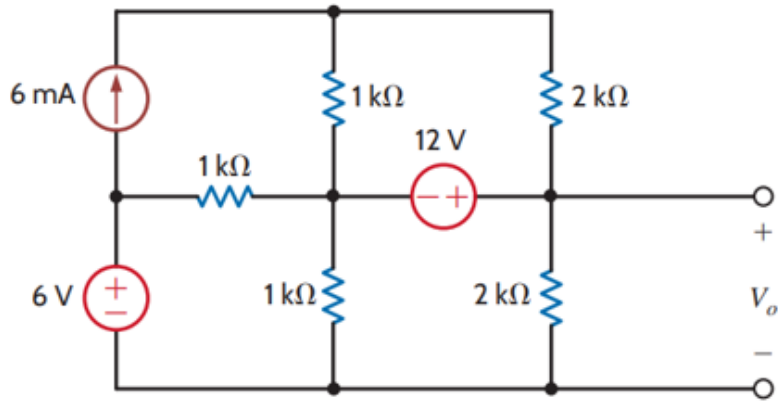

FIGURE P3.2.15

3.2.16 Find I_o in the circuit in Fig. P3.2.16 using loop analysis.

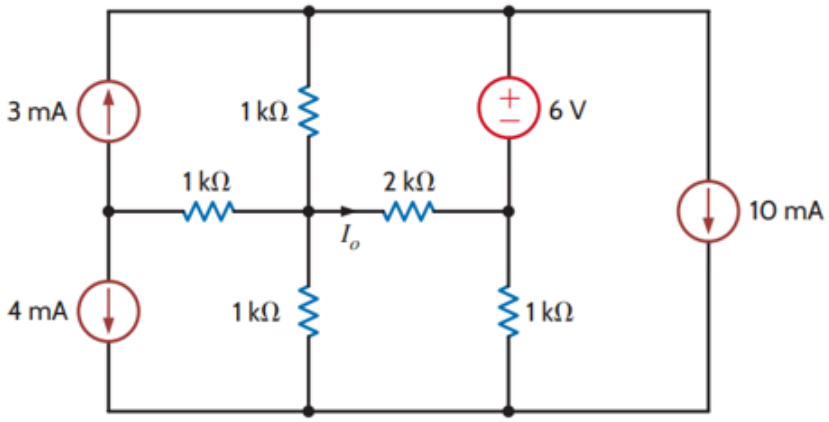

FIGURE P3.2.16

3.2.22 Using loop analysis, find V_o in the network in Fig. P3.2.22.

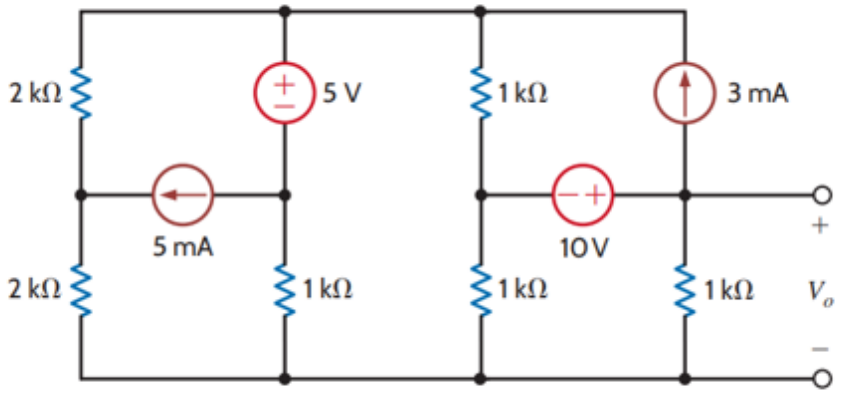

FIGURE P3.2.22

3.2.26 Use both nodal analysis and mesh analysis to find V_o in the circuit in Fig. P3.2.26.

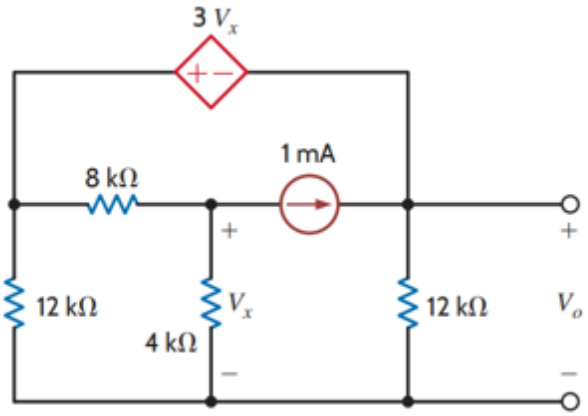

FIGURE P3.2.26

3.2.34 Using loop analysis, find I_o in the network in Fig. P3.2.34.

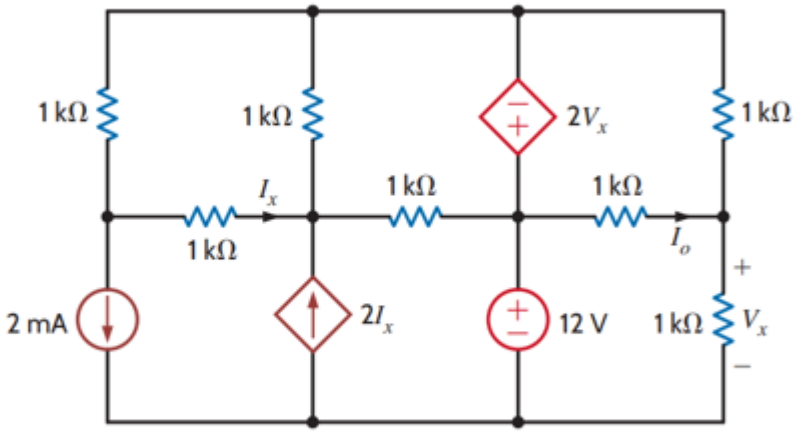

FIGURE P3.2.34