**EEC172 Lab 0 Lab Report**

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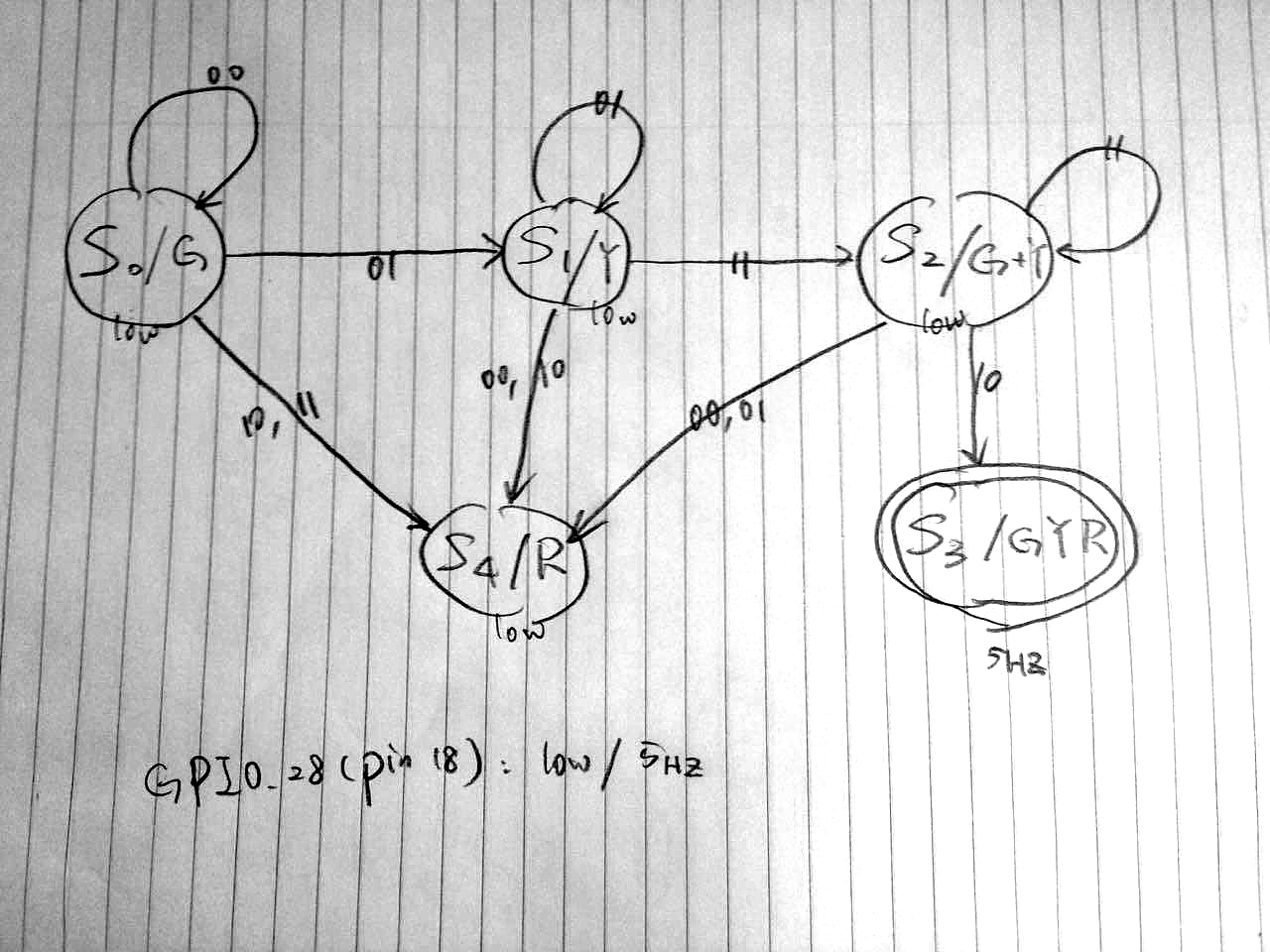
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**Introduction**

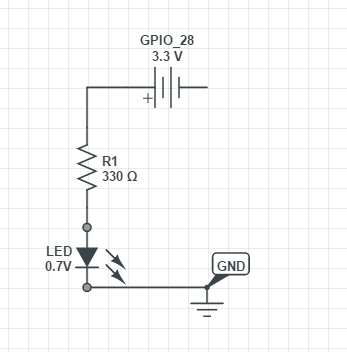
This Lab covers the basic software development tools that will be used in this course. We basically learned how to configure the board, write codes in CCS, implement some simple orders and flash the final code onto the board.

**Verification sheet** and the **original code** will be attached to the end of this report. The hard copy of the code contains **main.c**, **pin\_mux\_config.h** and **pin\_mux\_config.c**. The rest of the files can be found in the soft copy submission of Lab 0.

**Moore State Transition Diagram**



**LED Circuit Schematics**



**Current Calculation**

**Challenge: Baud Rate Mismatch**

When our group finished with all the code and tried to print some messages to the Tera Term console simulation, we got garbage messages that did not make any sense. We later found out that the baud rate of the port and the Tera Term software were not configured properly to match the baud rate of the board. The baud rate of the board in this case is 115200. We had to first go to the Windows Device Manager to set the port baud rate to 115200, then we configured the software to receive and process the information at 115200 too.