

# **COMP5123-P01**

## **Advanced Computer Architecture**

### **Assignment 3**

Instructor: Dr. Mary Heejin Kim

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**Due: September 28, 2016 11:55 pm (upload file in Ecourse)**

- 1.** List and briefly define two approaches to dealing with multiple interrupts. (15 pts)
- 2.** What types of transfers must a computer's interconnection structure (e.g., bus) support? (15 pts)
- 3.** List and briefly define the QPI protocol layers. (15 pts)
- 4.** Exercise 3.1 (pp. 116) (20 pts)
- 5.** Exercise 3.3 (pp. 117) (20 pts)
- 6.** Exercise 3.17 (pp. 119) (15 pts)