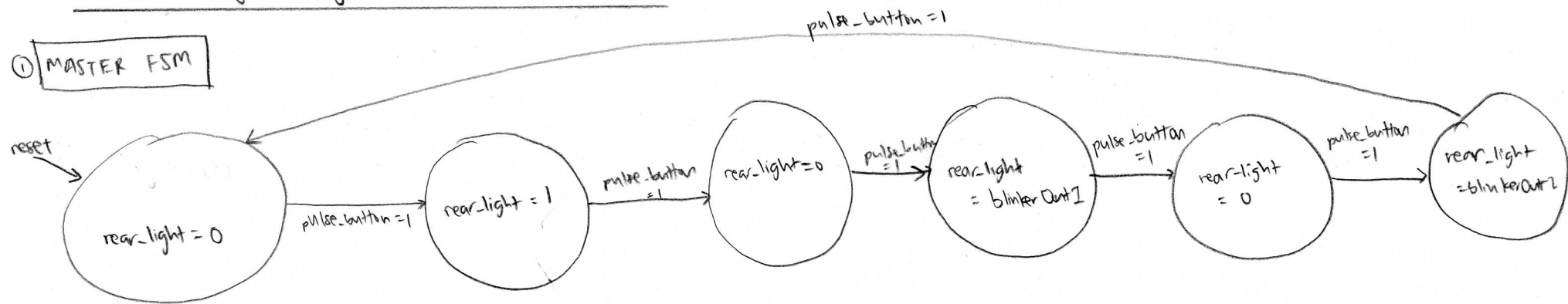
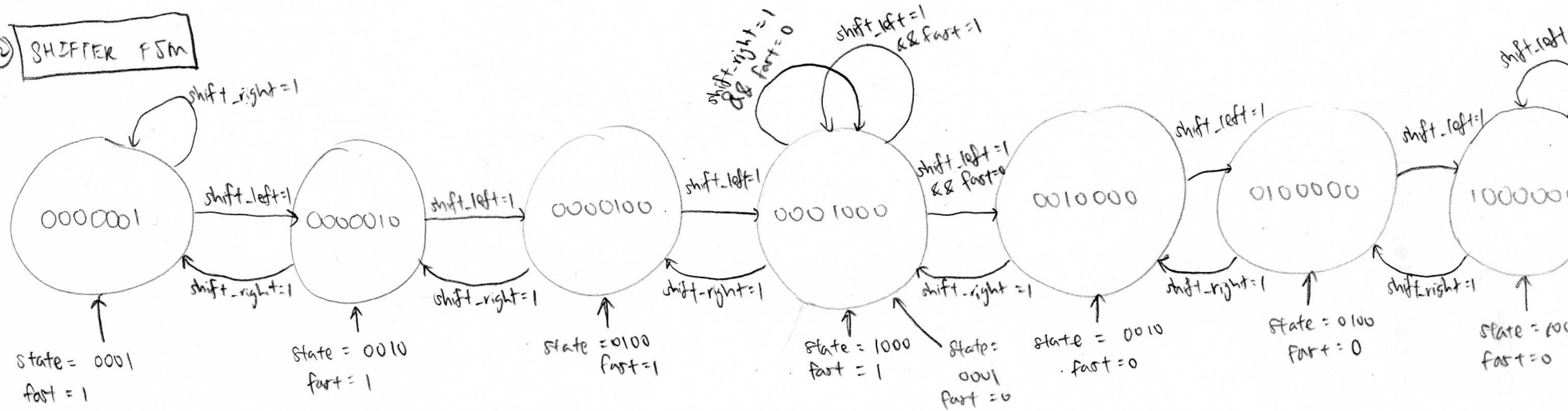


① MASTER FSM

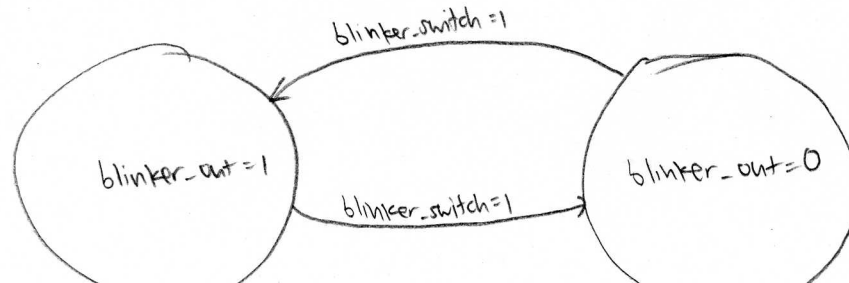


* goes back to leftmost state if reset is pressed at any state.

② SHIFTER FSM

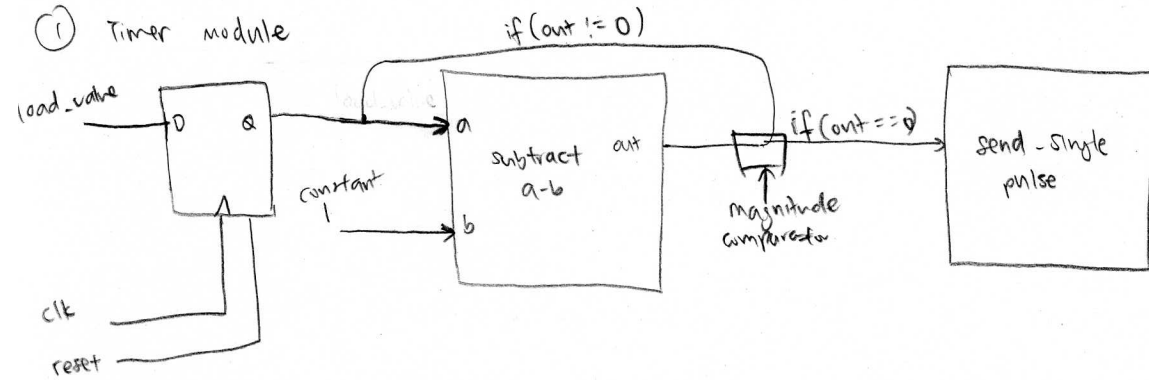


③ BLINKER FSM



Block-diagram of Timer and 32 bit counter module

① Timer module



② 32 bit counter module

