EE108a Lab 3 Synthesis Report – Readme

Group 18, Fall 2012

1. Fastest speed: 3.492ns

Slowest path (critical path): bpu\_down/debounce/state/q\_0 & bicycle\_fsm/slowBlink/shifter\_instance/flipflop/q\_1

1. Number of slice flip flops used: 130

Number of 4 input LUTs: 243

1. Screenshot included: flipflop.png
2. For just the bicycle\_fsm portion of the design,

Fastest speed: 3.433ns

Slowest path (critical path): bicycle\_fsm/slowBlink/shifter\_instance/flipflop/q\_1

Number of slice flip-flops used: 58

Number of 4 input LUTs: 125