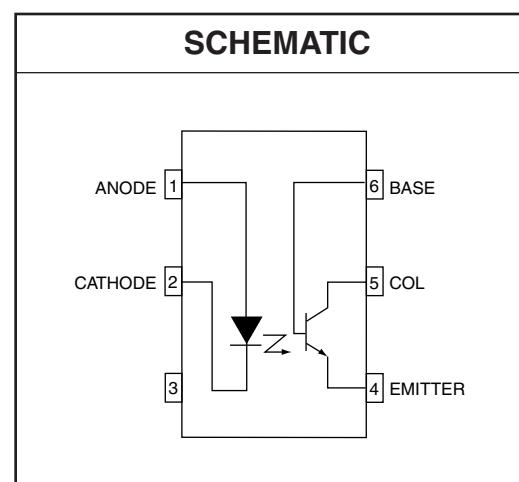
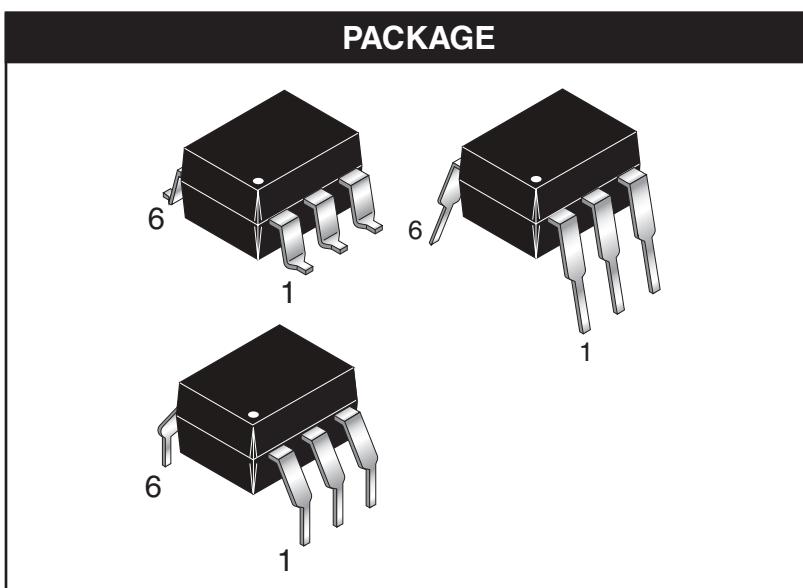


SL5500

SL5501

SL5504

SL5511



DESCRIPTION

The SL5500, SL5501, SL5504 and SL5511 are optically coupled isolators each consisting of an infrared emitting GaAs diode and a silicon NPN phototransistor with accessible base. These devices are housed in 6-pin dual-in-line packages (DIP).

FEATURES

- High output/input DC current transfer ratio
- Low saturation voltage
- High isolation voltage of 5.3 kV RMS
- UL recognized (File # E90700)
- VDE recognized (File # 94766)
- Ordering option '300' (e.g. SL5500.300)

APPLICATIONS

- Power supply regulators
- Digital logic inputs
- Microprocessor inputs
- Appliance sensor systems
- Industrial controls



6-PIN PHOTOTRANSISTOR OPTOCOUPERS

SL5500**SL5501****SL5504****SL5511**

Parameters	Symbol	Value	Units
TOTAL DEVICE			
Storage Temperature	T_{STG}	-55 to +150	°C
Operating Temperature	T_{OPR}	-55 to +100	°C
Lead Solder Temperature	T_{SOL}	260 for 10 sec	°C
Total Power Dissipation at $T_A = 25^\circ\text{C}$ Ambient	P_D	260	mW
Derate Linearly from 25°C		3.3	mW/°C
EMITTER			
Continuous Reverse Voltage	V_R	3	V
Continuous Forward Current	I_F	100	mA
Forward Current - Peak (10 μs pulse, $\delta = 0.01$)	$I_F(pk)$	3.0	A
Total Power Dissipation $T_A = 25^\circ\text{C}$ Ambient	P_D	150	mW
Derate Linearly from 25°C		2.0	mW/°C
DETECTOR			
Collector to Emitter Voltage (open base)	V_{CEO}	30	V
SL5500, SL5501, SL5511		80	
SL5504			
Collector to Base Voltage (open emitter)	V_{CBO}	70	V
SL5500, SL5501, SL5511		120	
SL5504			
Emitter to Collector Voltage (open base)	V_{ECO}	7	V
Emitter to Base Voltage (open collector)	V_{EBO}	7	V
DC Collector Current	I_C	100	mA
Detector Power Dissipation @ $T_A = 25^\circ\text{C}$ Ambient	P_D	150	mW
Derate Linearly from 25°C		2.0	mW/°C

SL5500

SL5501

SL5504

SL5511

ELECTRICAL CHARACTERISTICS (T_A = 25°C Unless otherwise specified.)

INDIVIDUAL COMPONENT CHARACTERISTICS

Parameters	Test Conditions	Symbol	Device	Min	Typ*	Max	Units
EMITTER							
Input Forward Voltage	I _F = 20 mA, T _A = 25 to 70°C	V _F	All		1.23	1.3	V
	I _F = 2 mA				1.10	1.2	V
Reverse Current	V _R = 3 V, T _A = 25 to 70°C	I _R	All		0.001	10	µA
DETECTOR	V _{CE} = 10 V	I _{CEO}	All		1	50	nA
Leakage Current	V _{CE} = 30 V				0.005	10	µA
Collector to Emitter	V _{CE} = 10 V, T _A = 70°C					500	nA
	V _{CB} = 30 V	I _{CBO}			0.001	50	µA
Breakdown Voltage							
Collector to Emitter	I _C = 10 µA, I _F = 0	BV _{CEO}	SL5500, SL5501, SL5511	30	100		V
			SL5504	80	110		
Collector to Base	I _C = 10 µA, I _F = 0	BV _{CBO}	SL5500, SL5501, SL5511	30	120		V
			SL5504	120	150		
Emitter to Collector	I _E = 10 µA, I _F = 0	BV _{ECO}	All	7	10		V
Emitter to Base	I _E = 10 µA, I _F = 0	BV _{EBO}	All	7	10		V

ISOLATION CHARACTERISTICS

Characteristic	Test Conditions	Symbol	Min	Typ*	Max	Units
Input-Output Isolation Voltage (note 1)	f = 60Hz, T = 1 min.	V _{ISO}	5300			V _{AC(RMS)}
Isolation Resistance	V _{I-O} = ±500 VDC	R _{ISO}	1	10		TΩ
Isolation Capacitance	f = 1 MHz, V = 0V	C _{ISO}		0.6	1.3	pF

*Typical values at T_A = 25°C

SL5500

SL5501

SL5504

SL5511

TRANSFER CHARACTERISTICS (TA = 25°C Unless otherwise specified.)

DC Characteristics	Test Conditions	Symbol	Device	Min	Typ	Max	Units
Output/Input Current Transfer Ratio	$I_F = 10 \text{ mA}, V_{CE} = 0.4 \text{ V}$	CTR	SL5500	50		300	%
	$I_F = 10 \text{ mA}, V_{CE} = 0.4 \text{ V}, T_A = 70^\circ\text{C}$		SL5500	40		300	
	$I_F = 10 \text{ mA}, V_{CE} = 0.4 \text{ V}, T_A = 25^\circ\text{C} \text{ to } 70^\circ\text{C}$		SL5501, SL5504	25		400	
	$I_F = 2 \text{ mA}, V_{CE} = 5 \text{ V}$		SL5500	40			
	$I_F = 2 \text{ mA}, V_{CE} = 5 \text{ V}, T_A = 70^\circ\text{C}$		SL5500	30			
	$I_F = 2 \text{ mA}, V_{CE} = 5 \text{ V}, T_A = 25^\circ\text{C} \text{ to } 70^\circ\text{C}$		SL5501, SL5504	15			
	$I_F = 2 \text{ mA}, V_{CE} = 5 \text{ V}, T_A = 25^\circ\text{C} \text{ to } 70^\circ\text{C}$		SL5511	25			
	$I_F = 0.5 \text{ mA}, V_{CE} = 0.4 \text{ V}, T_A = 25^\circ\text{C} \text{ to } 70^\circ\text{C}$		SL5511	20			
	$I_F = 50 \text{ mA}, I_C = 10 \text{ mA}$	V _{CE(SAT)}	SL5500			0.4	V
Collector-Emitter Saturation Voltage	$I_F = 20 \text{ mA}, I_C = 2 \text{ mA}$		SL5501, SL5504, SL5511			0.4	
AC Characteristics	Test Conditions	Symbol	Device	Min	Typ	Max	Units
Saturated Switching Times							
Turn-On Time	$R_L = 1 \text{ k}\Omega, I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}$	t _{on}	SL5500, SL5501, SL5511			20	μs
			SL5504			50	
Turn-Off Time	See Fig. 1 and Fig. 2	t _{off}	SL5500, SL5501, SL5511			50	μs
			SL5504			150	

Note

1. Device considered a two-terminal device: pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

SL5500

SL5501

SL5504

SL5511

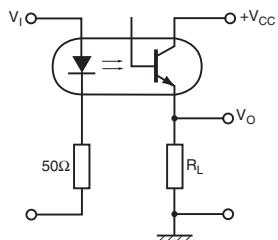


Fig. 1 Switching Circuit

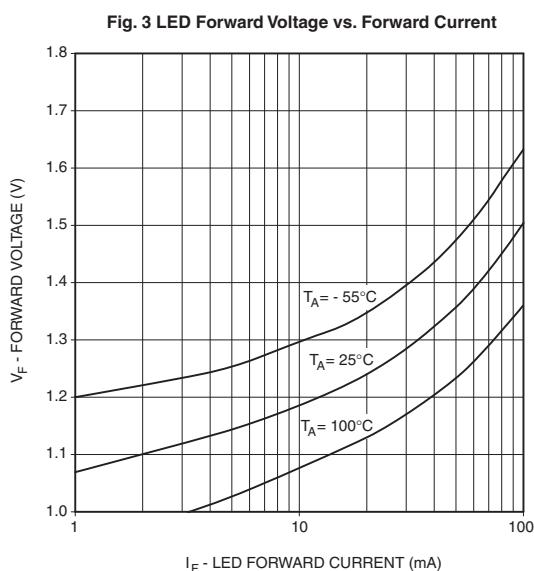


Fig. 3 LED Forward Voltage vs. Forward Current

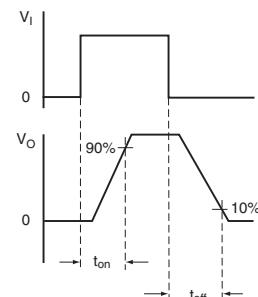


Fig. 2 Waveforms

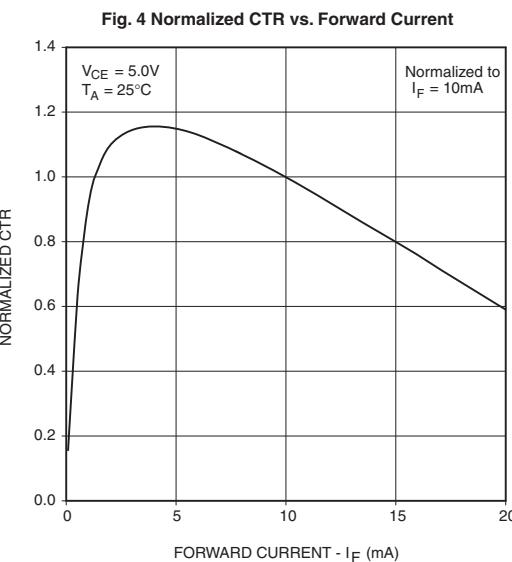


Fig. 4 Normalized CTR vs. Forward Current

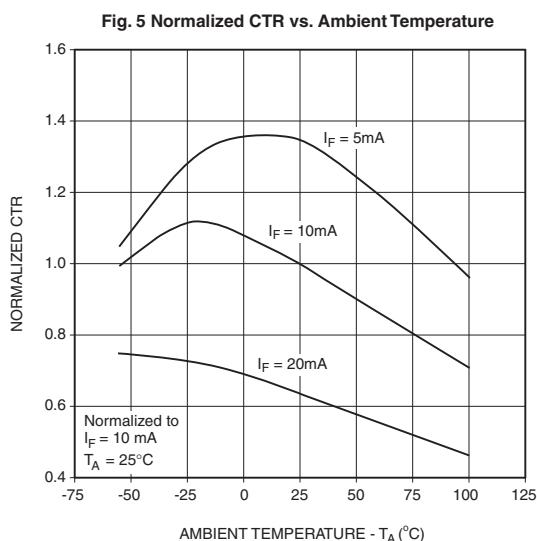


Fig. 5 Normalized CTR vs. Ambient Temperature

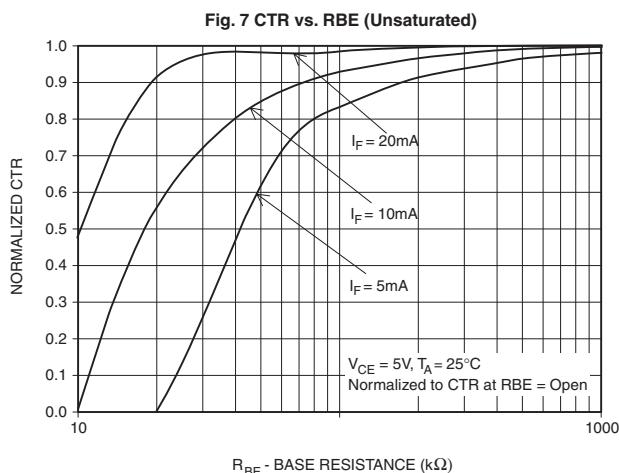


Fig. 7 CTR vs. RBE (Unsaturated)

SL5500

SL5501

SL5504

SL5511

Fig. 7 CTR vs. R_{BE} (Saturated)

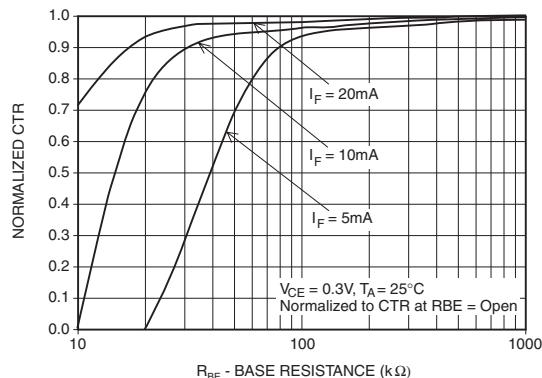


Fig. 9 Normalized t_{on} vs. R_{BE}

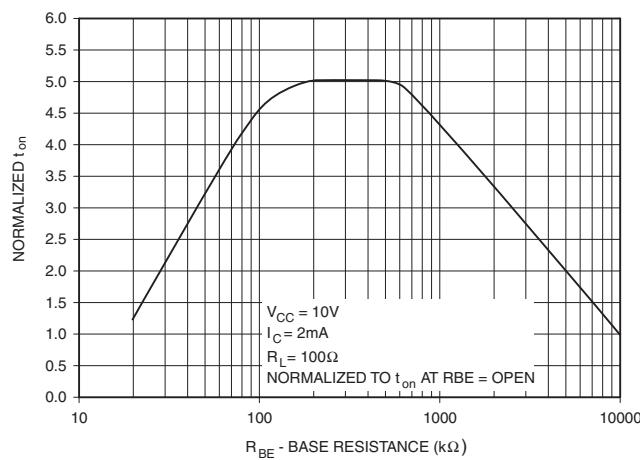


Fig. 11 Collector Emitter Saturation Voltage vs. Collector Current

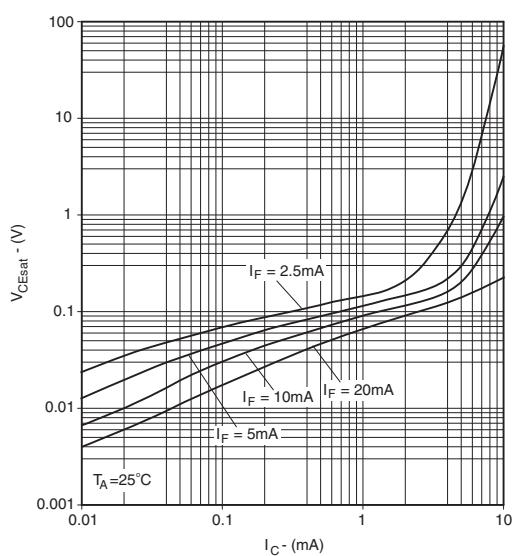


Fig. 8 Normalized t_{off} vs. R_{BE}

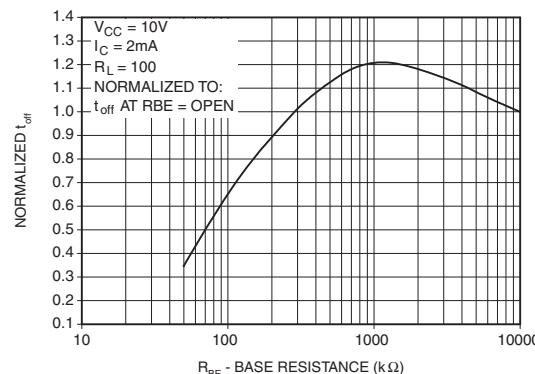


Fig. 10 Switching Speed vs. Load Resistor

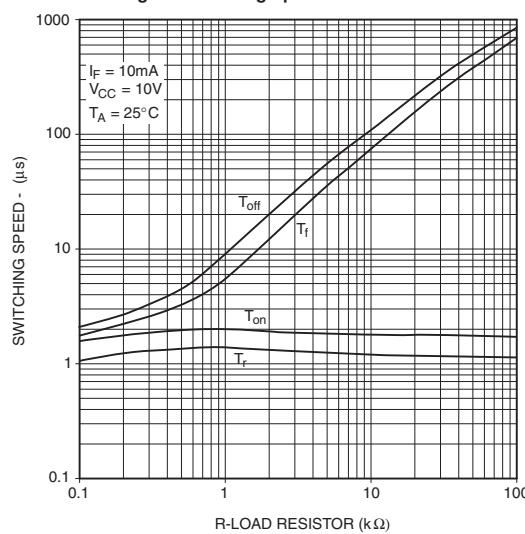
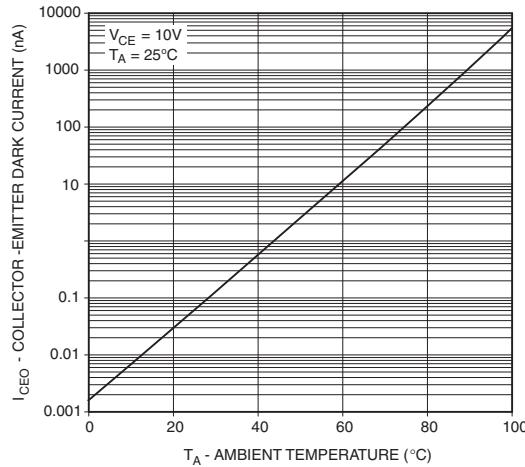


Fig. 12 Dark Current vs. Ambient Temperature



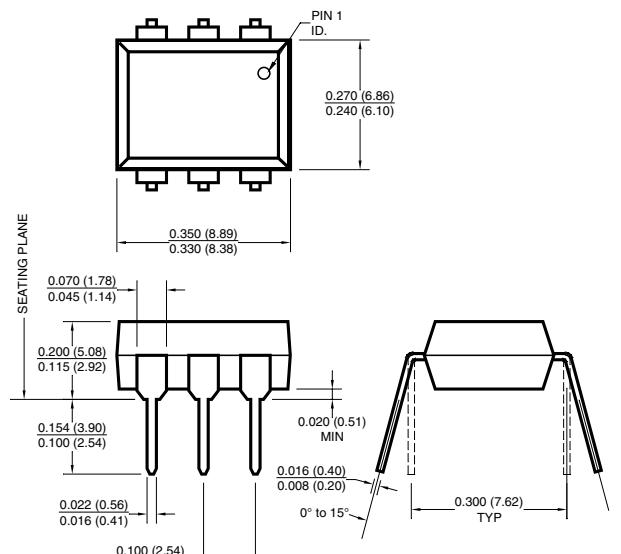
SL5500

SL5501

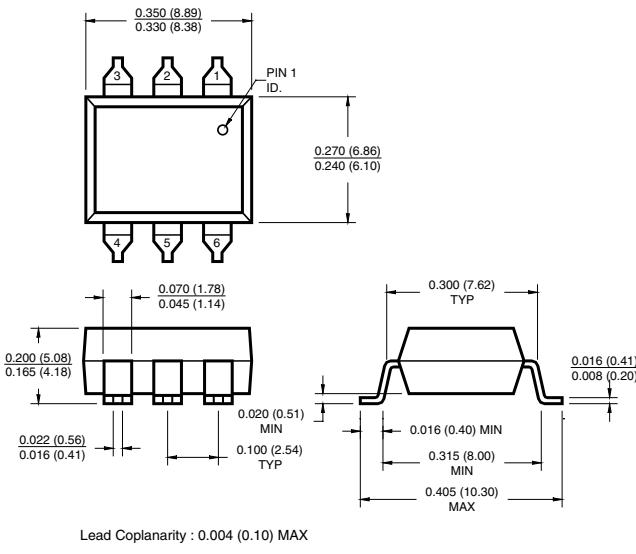
SL5504

SL5511

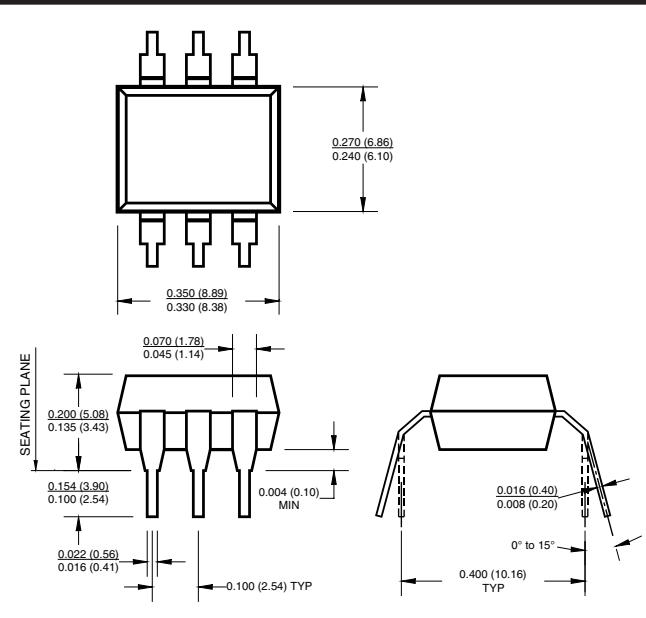
Package Dimensions (Through Hole)



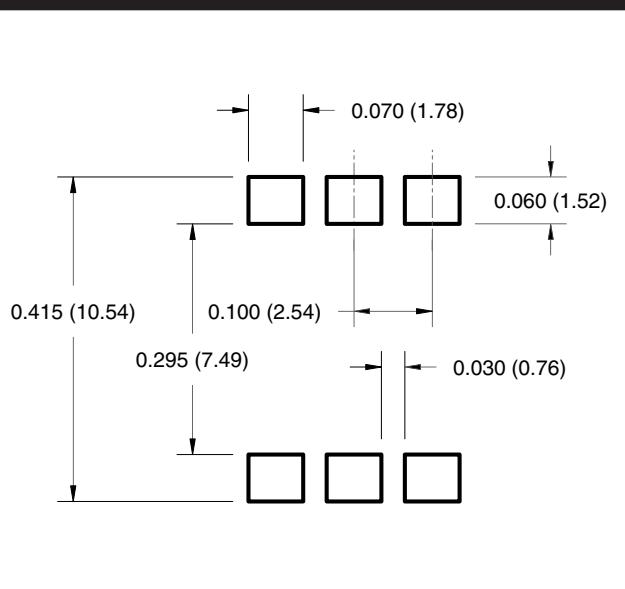
Package Dimensions (Surface Mount)



Package Dimensions (0.4" Lead Spacing)



Recommended Pad Layout for Surface Mount Leadform



Note

All dimensions are in inches (millimeters)

SL5500

SL5501

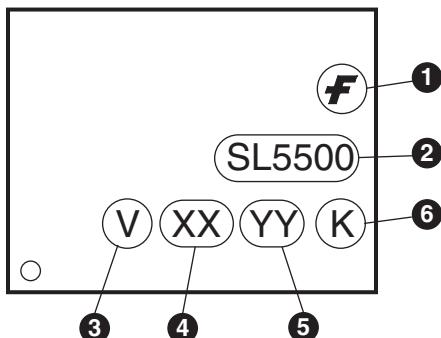
SL5504

SL5511

ORDERING INFORMATION

Option	Order Entry Identifier	Description
S	.S	Surface Mount Lead Bend
SD	.SD	Surface Mount; Tape and Reel
W	.W	0.4" Lead Spacing
300	.300	VDE 0884
300W	.300W	VDE 0884, 0.4" Lead Spacing
3S	.3S	VDE 0884, Surface Mount
3SD	.3SD	VDE 0884, Surface Mount, Tape and Reel

MARKING INFORMATION



Definitions	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)
4	Two digit year code, e.g., '03'
5	Two digit work week ranging from '01' to '53'
6	Assembly package code

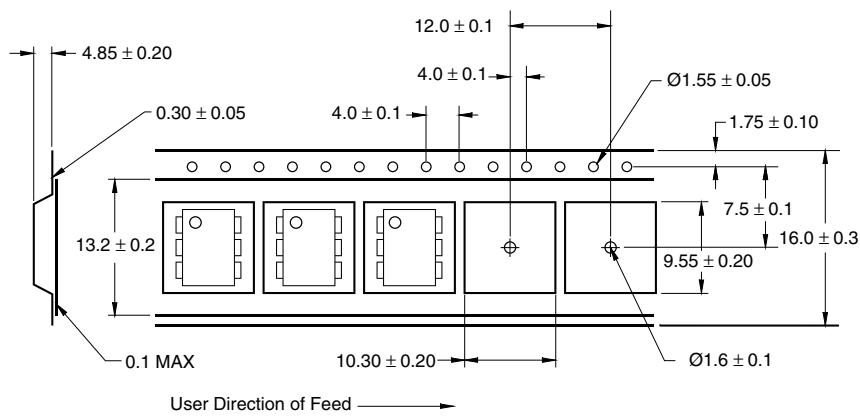
SL5500

SL5501

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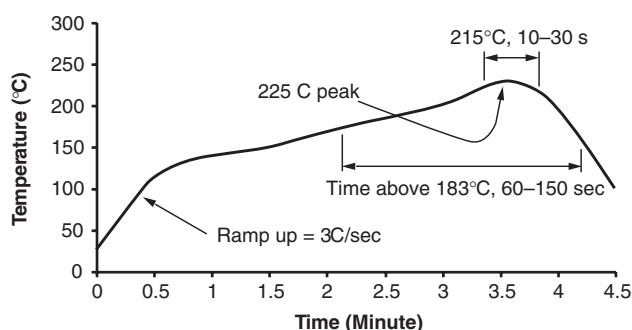
Carrier Tape Specifications ("D" Taping Orientation)



NOTE

All dimensions are in inches (millimeters)

Reflow Profile (Black Package, No Suffix)



- Peak reflow temperature: 225°C (package surface temperature)
- Time of temperature higher than 183°C for 60–150 seconds
- One time soldering reflow is recommended



6-PIN PHOTOTRANSISTOR OPTOCOUPLES

SL5500**SL5501****SL5504****SL5511**

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.