# International TOR Rectifier

## **IRF9530N**

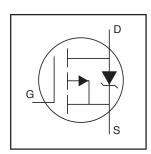
#### HEXFET® Power MOSFET

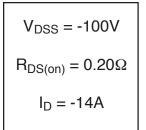
- Advanced Process Technology
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- P-Channel
- Fully Avalanche Rated

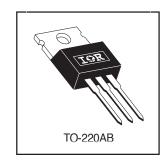
#### Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.







#### **Absolute Maximum Ratings**

Parameter		Max.	Units	
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V	-14		
I <sub>D</sub> @ T <sub>C</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ -10V	-10	A	
I <sub>DM</sub>	Pulsed Drain Current ①	-56		
P <sub>D</sub> @T <sub>C</sub> = 25°C	Power Dissipation	79	W	
	Linear Derating Factor	0.53	W/°C	
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V	
E <sub>AS</sub>	Single Pulse Avalanche Energy@	250	mJ	
I <sub>AR</sub>	Avalanche Current①	-8.4	A	
E <sub>AR</sub>	Repetitive Avalanche Energy①	7.9	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	-5.0	V/ns	
TJ	Operating Junction and	-55 to + 175		
T <sub>STG</sub>	Storage Temperature Range		°C	
	Soldering Temperature, for 10 seconds	300 (1.6mm from case )		
	Mounting torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)		

#### **Thermal Resistance**

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case		1.9	
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
$R_{\theta JA}$	Junction-to-Ambient		62	

## Electrical Characteristics @ $T_J = 25$ °C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	-100			V	$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		-0.11		V/°C	Reference to 25°C, I <sub>D</sub> = -1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance			0.20	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -8.4A ④
V <sub>GS(th)</sub>	Gate Threshold Voltage	-2.0		-4.0	V	$V_{DS} = V_{GS}, I_{D} = -250\mu A$
9 <sub>fs</sub>	Forward Transconductance	3.2			S	$V_{DS} = -50V, I_{D} = -8.4A$
	Drain-to-Source Leakage Current			-25	μΑ	V <sub>DS</sub> = -100V, V <sub>GS</sub> = 0V
I <sub>DSS</sub>	Brain to Godice Leakage Current			-250	μΑ	$V_{DS} = -80V, V_{GS} = 0V, T_{J} = 150$ °C
1	Gate-to-Source Forward Leakage			100	nA	V <sub>GS</sub> = 20V
I <sub>GSS</sub>	Gate-to-Source Reverse Leakage			-100	IIA	V <sub>GS</sub> = -20V
Qg	Total Gate Charge			58		I <sub>D</sub> = -8.4A
Q <sub>gs</sub>	Gate-to-Source Charge			8.3	nC	$V_{DS} = -80V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge			32		$V_{GS}$ = -10V, See Fig. 6 and 13 $\oplus$
t <sub>d(on)</sub>	Turn-On Delay Time		15			$V_{DD} = -50V$
t <sub>r</sub>	Rise Time		58			$I_D = -8.4A$
t <sub>d(off)</sub>	Turn-Off Delay Time		45		ns	$R_G = 9.1\Omega$
t <sub>f</sub>	Fall Time		46			$R_D = 6.2\Omega$ , See Fig. 10 $\oplus$
L <sub>D</sub>	Internal Drain Inductance		4.5			Between lead,
			7.5		nH	6mm (0.25in.) from package
L <sub>S</sub>	Internal Source Inductance		7.5			and center of die contact
C <sub>iss</sub>	Input Capacitance		760			$V_{GS} = 0V$
Coss	Output Capacitance		260		pF	$V_{DS} = -25V$
C <sub>rss</sub>	Reverse Transfer Capacitance		170			f = 1.0MHz, See Fig. 5

#### **Source-Drain Ratings and Characteristics**

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			-14		MOSFET symbol
	(Body Diode)		-14	Α	showing the	
I <sub>SM</sub>	Pulsed Source Current			-56	] ^	integral reverse
	(Body Diode) ①		-30		p-n junction diode.	
V <sub>SD</sub>	Diode Forward Voltage			-1.6	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = -8.4A, V <sub>GS</sub> = 0V ④
t <sub>rr</sub>	Reverse Recovery Time		130	190	ns	$T_J = 25^{\circ}C$ , $I_F = -8.4A$
Q <sub>rr</sub>	Reverse RecoveryCharge		650	970	nC	$di/dt = -100A/\mu s \oplus$
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> +L <sub>D</sub> )				

#### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )
- @ Starting T  $_{J}$  = 25°C, L = 7.0mH  $$R_{G}=25\Omega,\,I_{AS}=$  -8.4A. (See Figure 12)
- ③  $I_{SD} \le -8.4 A$ ,  $di/dt \le -490 A/\mu s$ ,  $V_{DD} \le V_{(BR)DSS}$ ,  $T_{J} \le 175 ^{\circ} C$
- ④ Pulse width  $\leq$  300 $\mu$ s; duty cycle  $\leq$  2%.

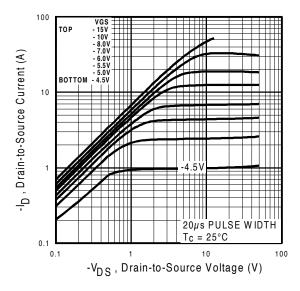


Fig 1. Typical Output Characteristics

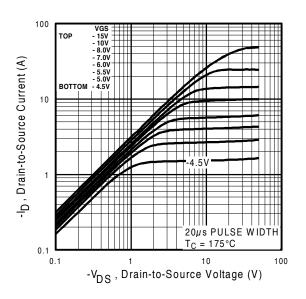


Fig 2. Typical Output Characteristics

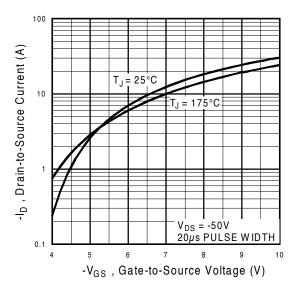
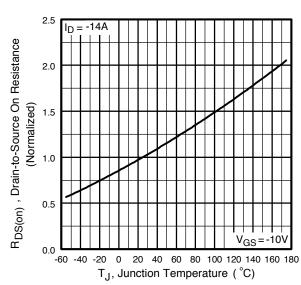
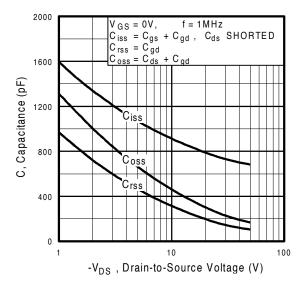


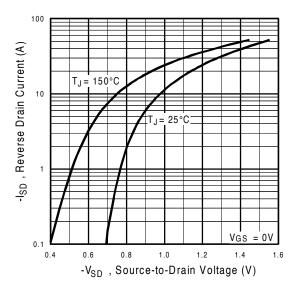
Fig 3. Typical Transfer Characteristics



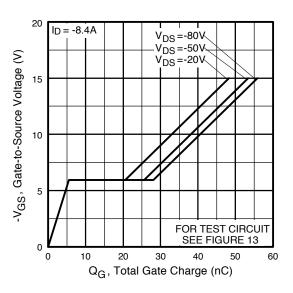
**Fig 4.** Normalized On-Resistance Vs. Temperature



**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



**Fig 7.** Typical Source-Drain Diode Forward Voltage



**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage

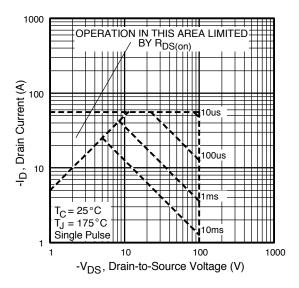


Fig 8. Maximum Safe Operating Area

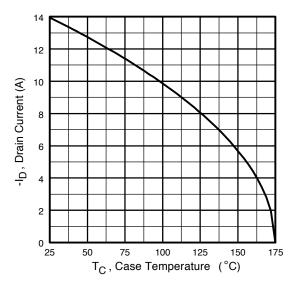


Fig 9. Maximum Drain Current Vs. Case Temperature

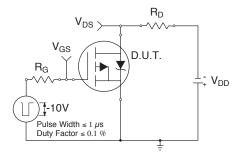


Fig 10a. Switching Time Test Circuit

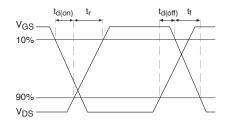


Fig 10b. Switching Time Waveforms

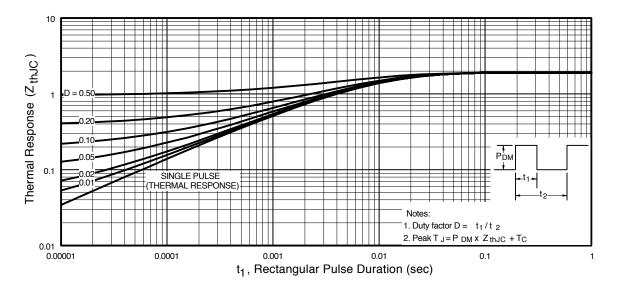


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

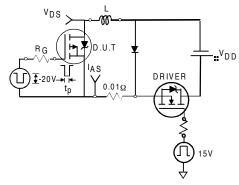


Fig 12a. Unclamped Inductive Test Circuit

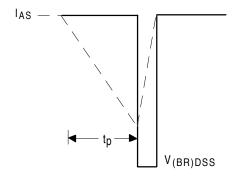


Fig 12b. Unclamped Inductive Waveforms

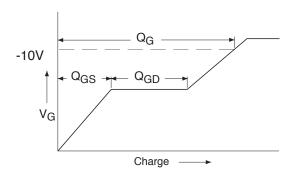


Fig 13a. Basic Gate Charge Waveform

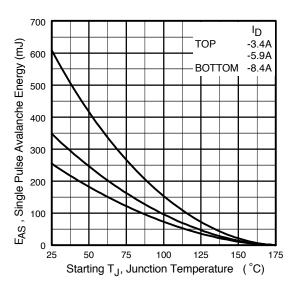


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

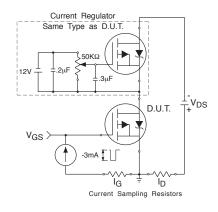
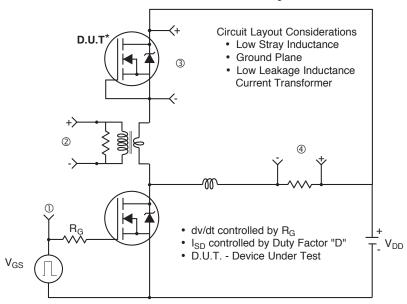
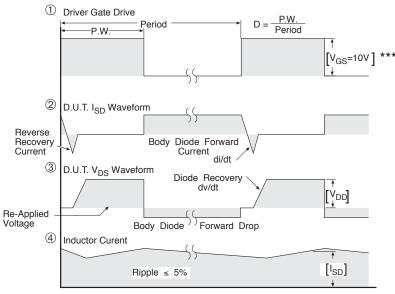


Fig 13b. Gate Charge Test Circuit

## Peak Diode Recovery dv/dt Test Circuit



<sup>\*</sup> Reverse Polarity of D.U.T for P-Channel



\*\*\*  $V_{GS} = 5.0V$  for Logic Level and 3V Drive Devices

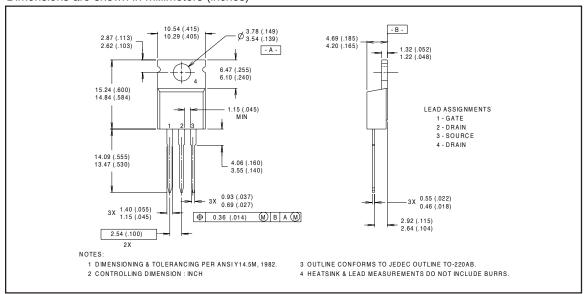
Fig 14. For P-Channel HEXFETS

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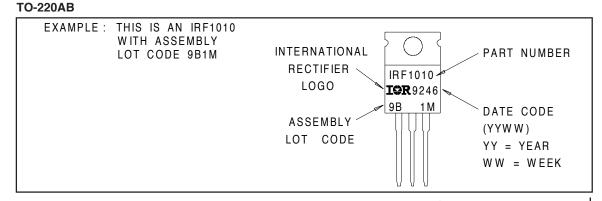
#### Package Outline

#### **TO-220AB Outline**

Dimensions are shown in millimeters (inches)



## Part Marking Information



## International Rectifier

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Note: For the most current drawings please refer to the IR website at: <a href="http://www.irf.com/package/">http://www.irf.com/package/</a>