



PHASE LOCKED LOOP

STEPHEN LEONE EE '16 AND ELI SOFFER EE '16, ADVISOR: TOBY CUMBERBATCH

We designed and tested a phase locked loop with the Cadence Design Suite. This included actually specifying the precise locations of semiconductor material for the fabrication of our phase locked loop on an integrated circuit. Phase locked loops take an input signal and closely track its frequency allowing for a variety of applications including the demodulation of FM radio signals and keeping the components of a computer synchronized.

More Info: ee.cooper.edu/~soffer/pll.pdf