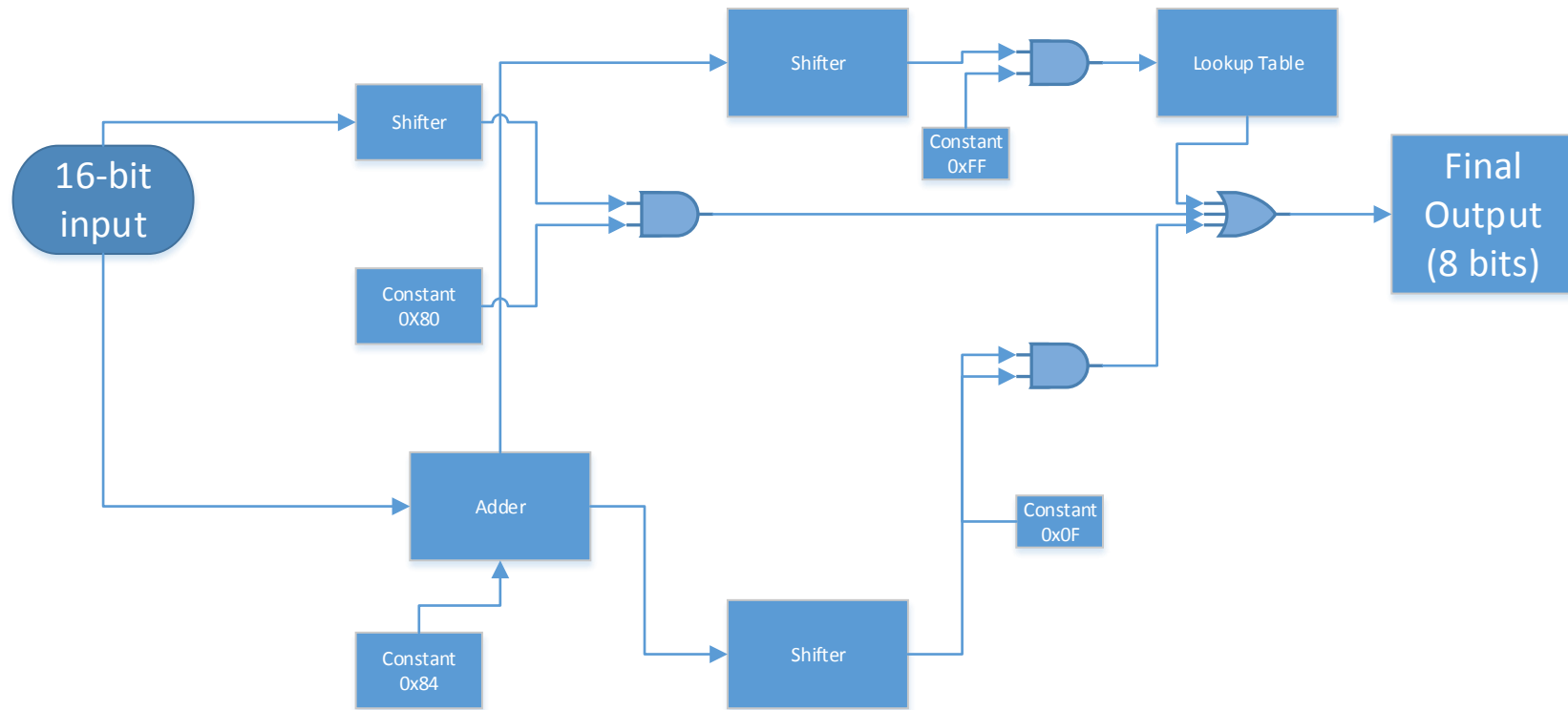


Basic
State and
Flow
Diagram



Dataflow of
Hardware
Design

