```
Bitvector t
                                                         PlanNode
                                 - Operator _op
                                 - PlanNode * _next
                                 - EquivalenceClass t * left
                                 - EquivalenceClass_t * _right

    bool commutativityEnabled

                                 - bool leftAssociativityEnabled
                                 - rightAssociativityEnabled
                                  _exchangeEnabled
                                 + PlanNode()
                                 + getSignature(): Bitvector_t
                                 + getRelations(): Bitvector t
                                 + getNeighbors(): Bitvector t
                                 + getLast(): PlanNode &
                                 + setLast(PlanNode *) : void
                                 + set(Operator, EquivalenceClass t*, EquivalenceClass *)
                                 + hasNext(): bool
<<enumeration>>
                                 + concat(PlanNode *) : void
                                 + getSignature(): Signature
     Operator
                     <<uses>>
JOIN
                                 + getNext() : PlanNode *
SCAN
                                 + print(ostream &) : ostream
SIZE OF ENUM
                                 + hasLeft(): bool
                                 + hasRight(): bool
                                 + getSize(): unsigned int
                                 + getOperator(): Operator
                                 + getOperatorAsString(): string
                                 + getRight() : EquivalenceClass_t &
                                 + r(): EquivalenceClass_t &
                                 + getLeft(): EquivalenceClass_t &
                                 + I(): EquivalenceClass t &
                                 + setLeft(EquivalenceClass t *): void
                                 + setRight(EquivalenceClass_t *) : void
                                 + disableAllRules() : PlanNode *
                                 + disableAllAndEnableCommutativity(): PlanNode *
                                 + isCommutativityEnabled(): bool
                                 + isLeftAssociativityEnabled(): bool
                                 + isRightAssociativityEnabled(): bool
                                 + isExchangeEnabled(): bool
```

+ debug(): void