

csStateBehavior (v0.99) - Teensy 3.6 Pinout Map

ground			GND	1	24	Vin (3.6 to 6.0 volts)	used for op-amp rail			
visual stimulus UART		RX1	0	2	23	Analog GND				
visual stimulus UART		TX1	1	3	22	3.3V (250 mA max)	used for gen power rail			
neopixel RGB/RGBW strip(s)			PWM	2	4	21	A9	PWM	Touch	lick sensor A
			PWM	3	5	20	A8	PWM	Touch	lick sensor B
	SCL2	CAN0TX	PWM	4	6	19	A7	PWM	CS0 MOSI1	RX1
	SDA2	CAN0RX	PWM	5	7	18	A6	PWM	CS0 SCK1	gen analog input 4
frame counter (input)	MISO1	TX1	PWM	6	8	17	A5		SCL0	
y-galvo (input)			PWM	7	9	16	A4		SDA0	
other user UART	SCL0	MOSI0	RX3	PWM	8	15	A3	PWM		gen analog input 3
other user UART	SDA0	MISO0	TX3	PWM	9	14	A2	PWM		gen analog input 2
reward pump UART		CS0	RX2	PWM	10	13	A1	PWM		gen analog input 1
reward pump UART		CS0	TX2	PWM	11	12	A0	PWM		gen analog input 0
	MOSI0				12	11				
	MISO0				13	10				
				3.3V	14	9				
					15	8				
reward trigger (out)				24	16	9	A22	DAC1		DAC2: 5V w/ Op-Amps
session start signal (scope trigger; out)				25	17	8	A21	DAC0		DAC1: 5V w/ Op-Amps
session end signal (out)			TX1	26	18	7	39	A20	MISO0	
session start (input)	SCK0	RX1		27	19	6	38	A19	PWM	SDA1
load cell data (dt)	MOSI0			28	20	5	37	A18	PWM	SCL1
load cell clock (clk)		CAN0TX	PWM	29	21	4	36	A17	PWM	DAC5/6 Latch
reward trigger (input)		CAN0RX	PWM	30	22	3	35	A16	PWM	digital encoder input
led switch	CS1	RX4	A12	31	23	2	34	A15	CAN1RX	SDA0 RX5
DAC3/4 Latch	SCK1	TX4	A13	32	24	1	33	A14	CAN1TX	SCL0 TX5

Legend:

Digital Outputs	i2c	Power Related
Digital Inputs (3.3V)	Analog Outputs (+2 via I2C)	
UART/Serial Pins (3.3V)	Analog Inputs (3.3V max)	