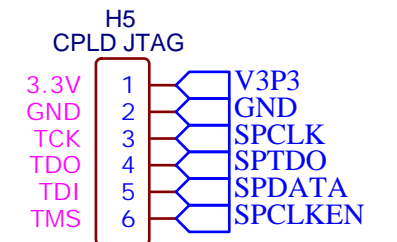
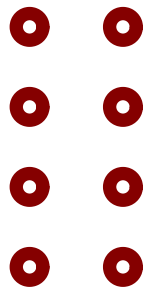


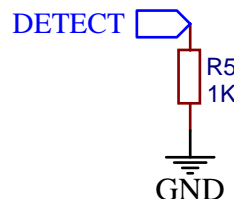
Rev 3



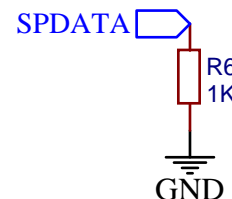
Mounting Holes



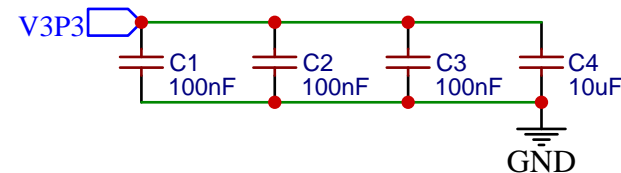
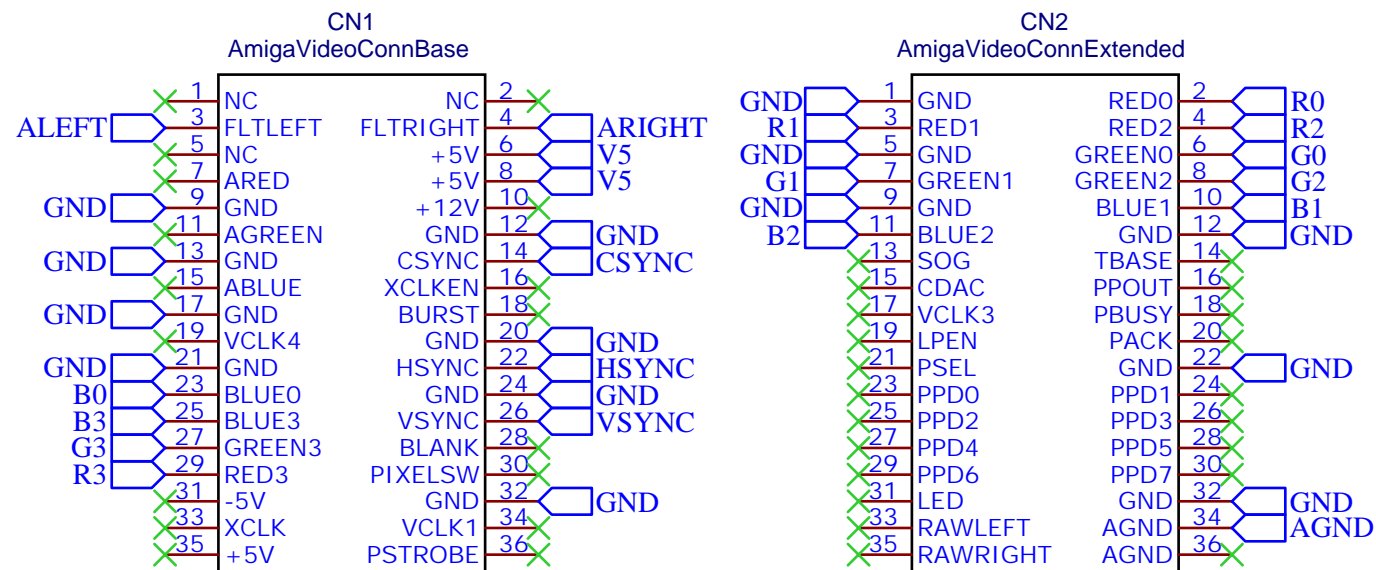
Not sure if DETECT should be pulled down (LinuxJedi CPLD) or run to Pi GPIO (RGBtoHDMI) so my circuit will do both.




Need to verify Pulldown
is required on SPDATA
(LinuxJedi's schematic)

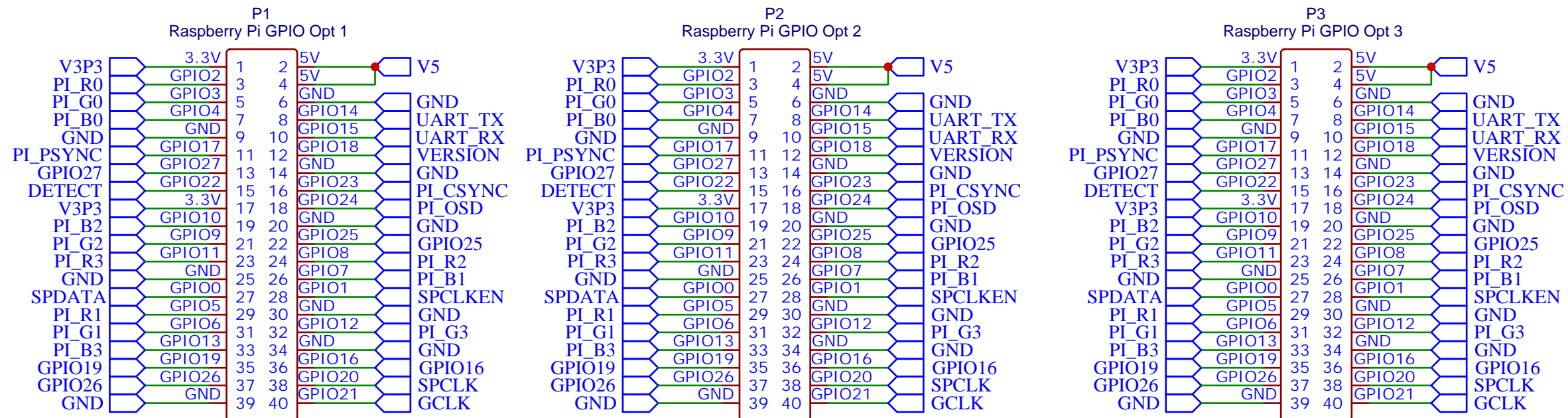


A diagram of a 3-to-1 multiplexer. On the left, three inputs are listed vertically: CSYNC, HSYNC, and VSYNC. Each input is connected to a corresponding input of a multiplexer symbol, which has three parallel horizontal lines on the left and a single line on the right. The output of the multiplexer is a single line that branches into three separate outputs, each labeled CSYNC, HSYNC, and VSYNC respectively.



TITLE: A2000 Video Slot to HDMI - CPLD + Edge		REV: 3.0
	Company: eebugs.com	Sheet: 1/3
	Date: 2021-05-16 Drawn By: cdhooper	

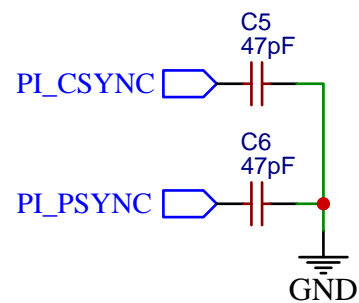
Raspberry Pi connector options



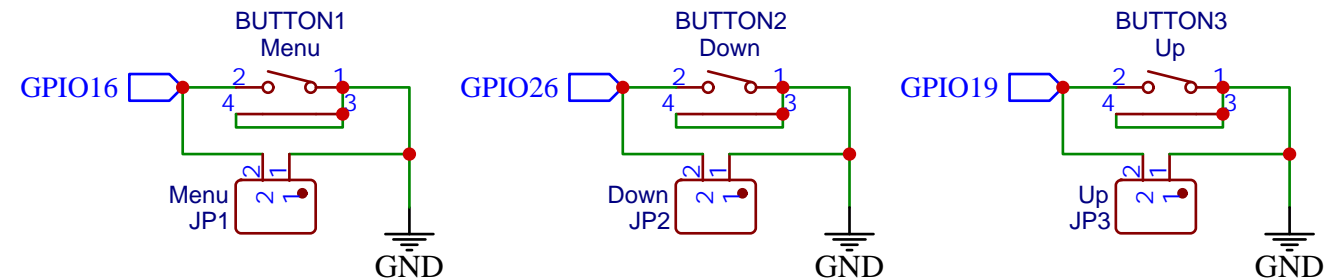
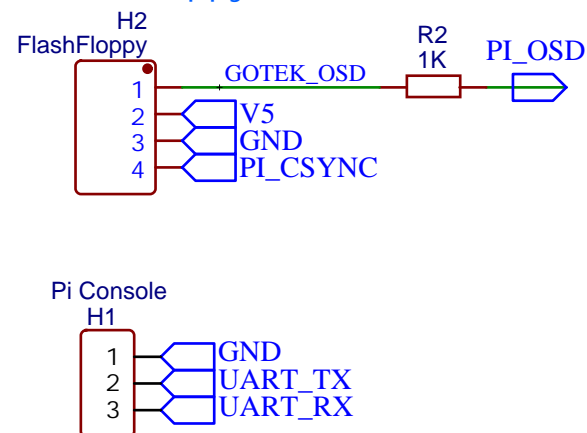
Note that signals are identical between these connectors. These connectors are for face-down boards.

Although not obvious here, this connector is from the bottom side because it's for a face-up board.

Options in case of bus noise

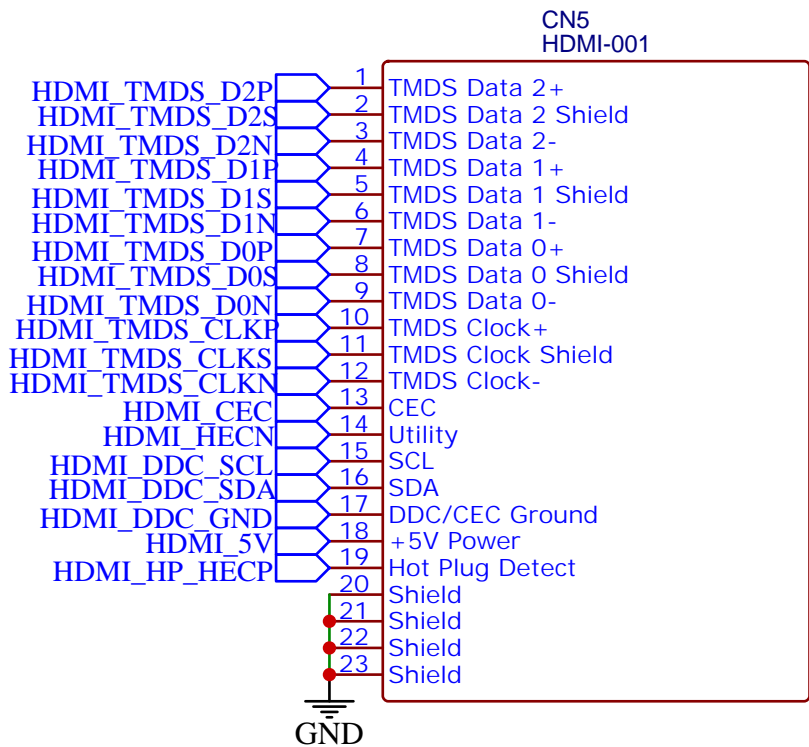


FlashFloppy OSD



	GPIO	Short Press	Long Press	In Menu
Button1	GPIO16	OSD Menu	Toggle Scanlines	Select
Button2	GPIO26	Capture	Toggle Artifacts	Cursor Down / +
Button3	GPIO19	Genlock	Sample Calibrate	Cursor Up / -

HDMI Female (rear)

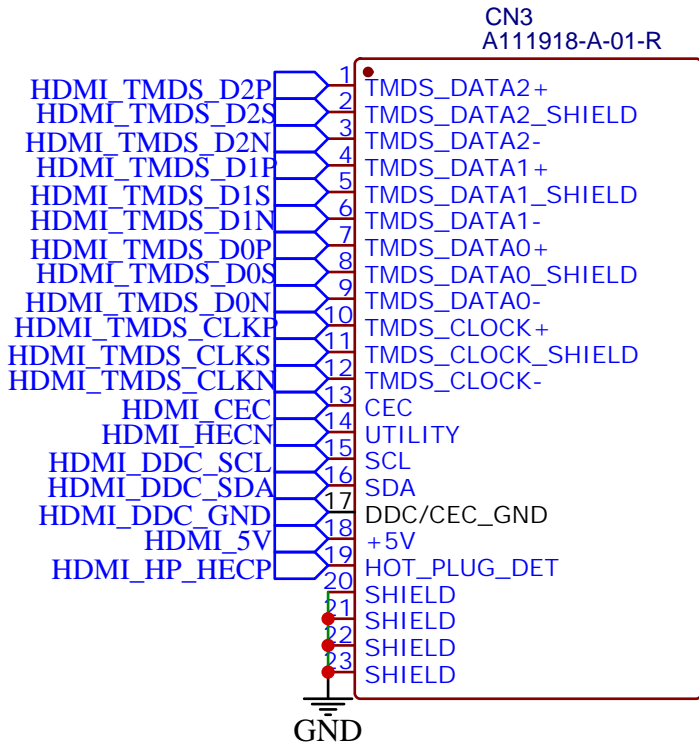


NOTES

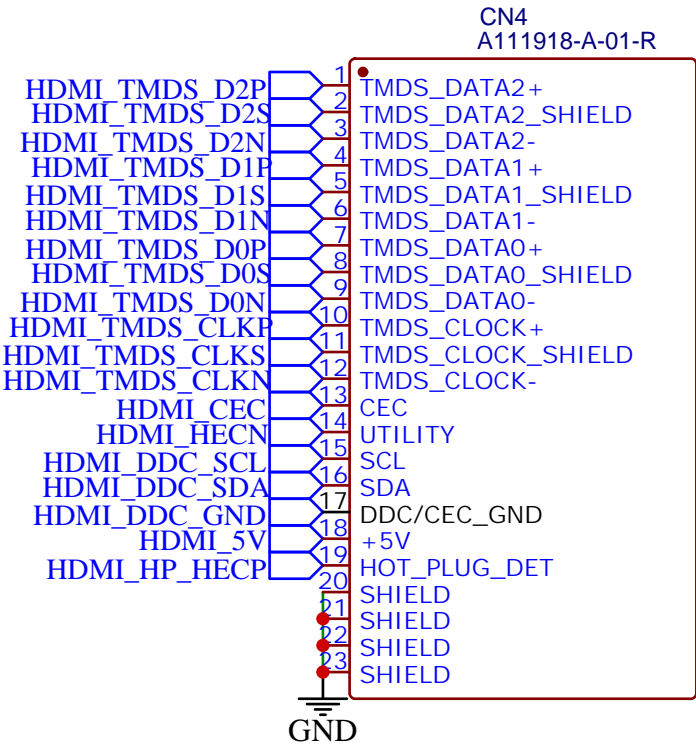
HDMI female inner dimensions: 14mm x 4.55mm
HDMI male outer dimensions: 13.90mm x 4.45mm

HDMI Female (rear) footprint is overlapped on the PCB with HDMI Female Raised (rear).

HDMI Female Raised



HDMI Female Raised (rear)



HDMI Male Edge

