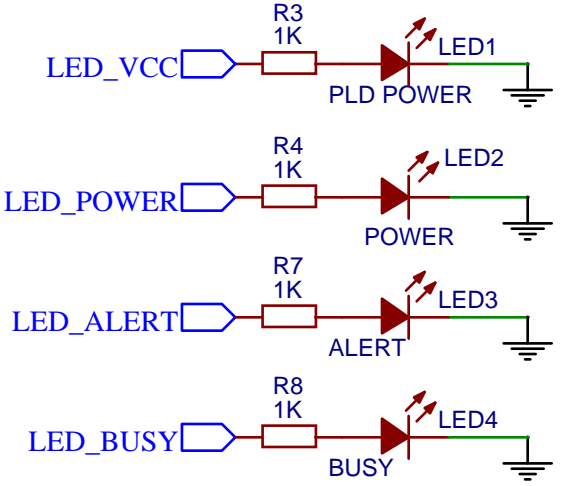
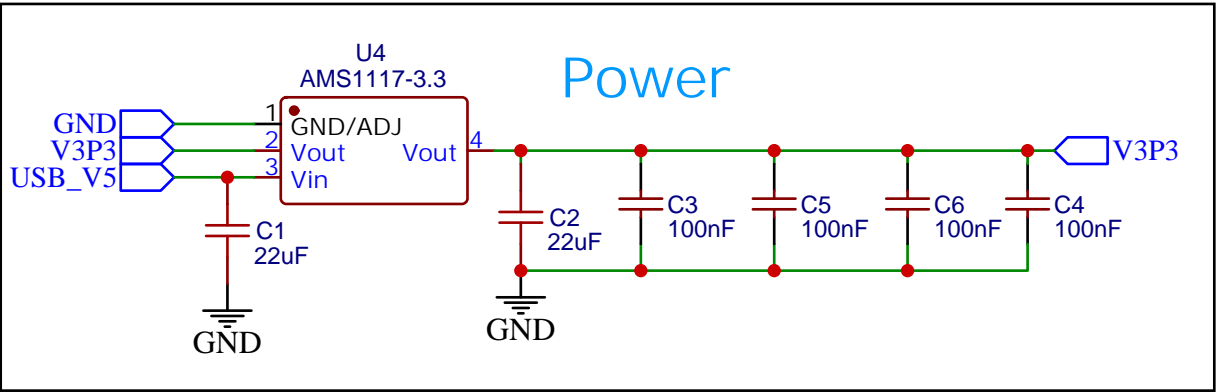
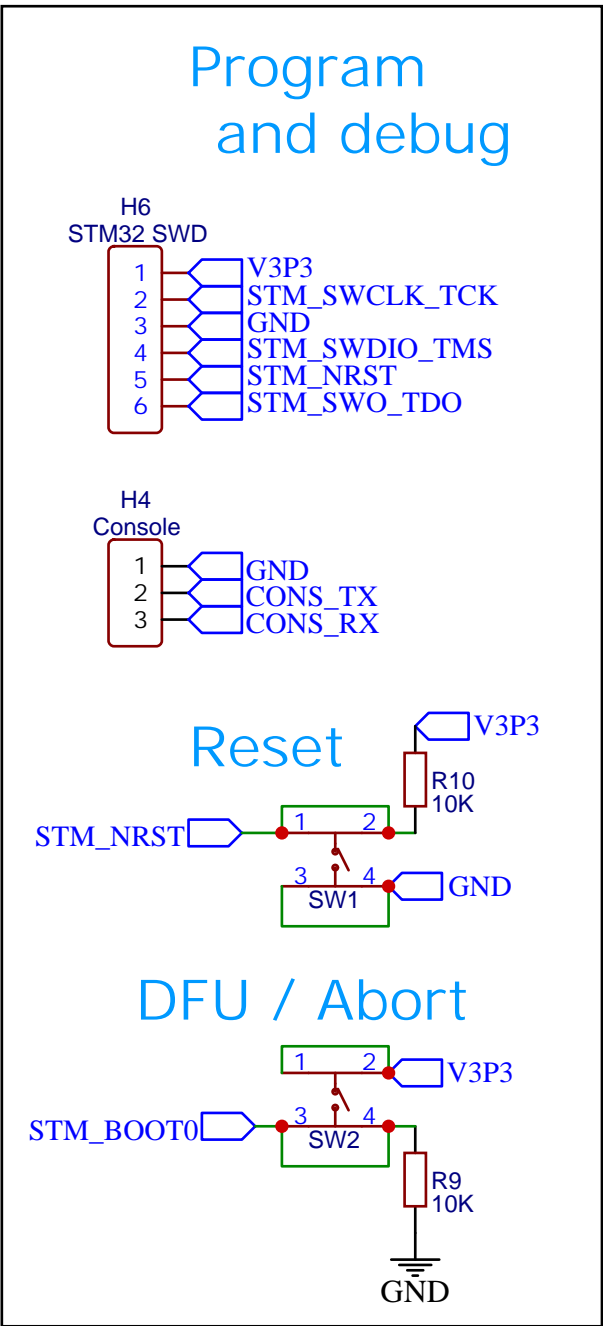
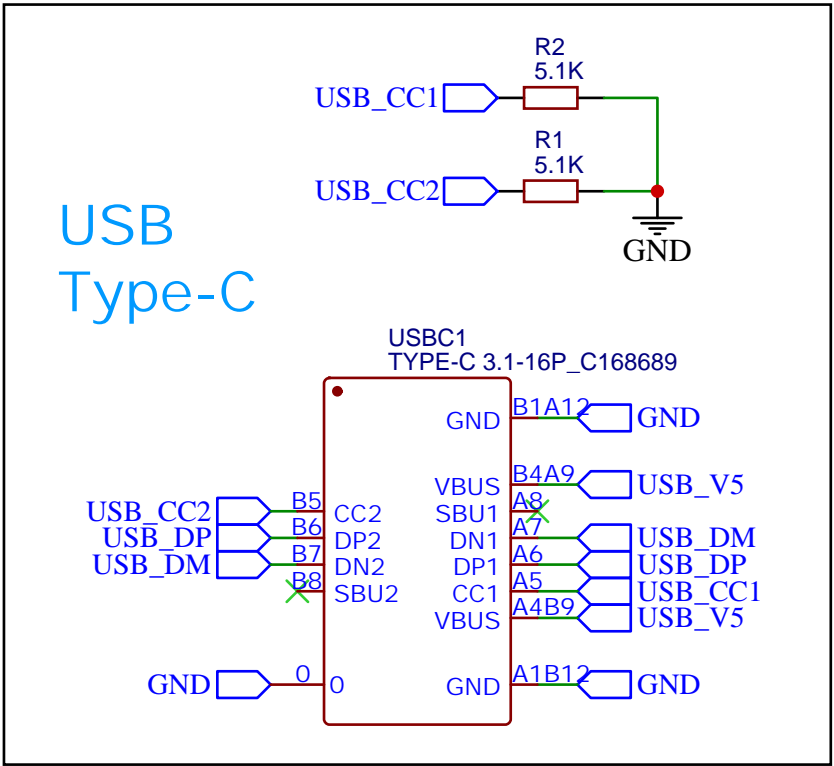
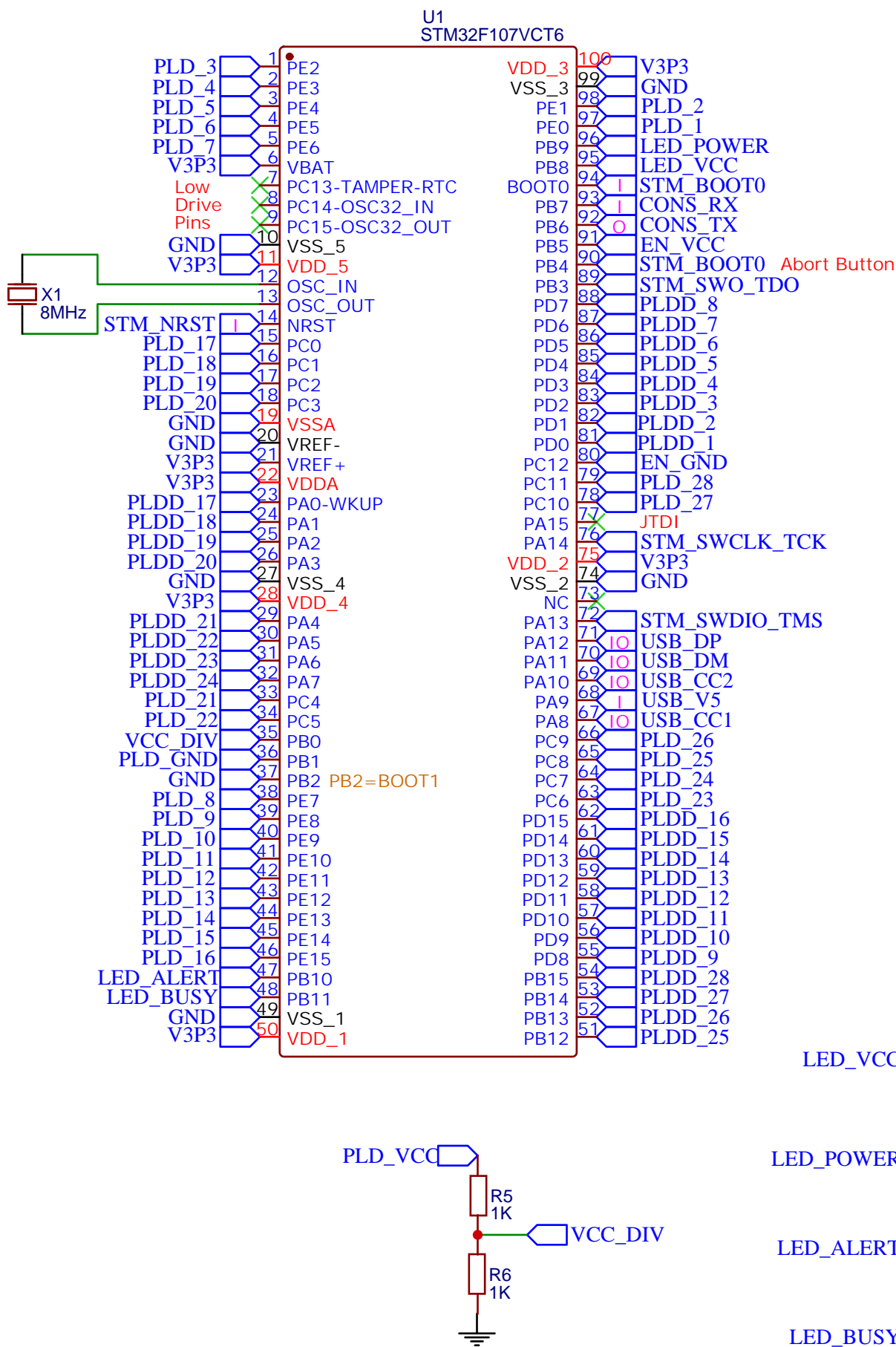


# Brutus 28 - PAL logic brute force decoder

Rev 2 2023-10-26



TITLE:

STM32

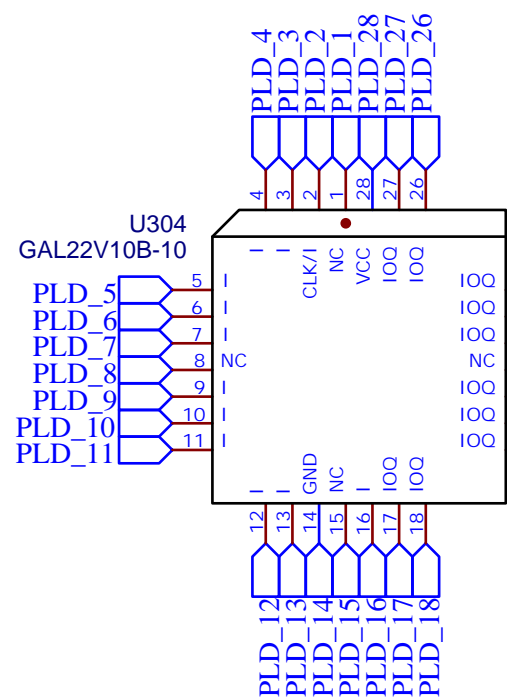
REV: 2.0



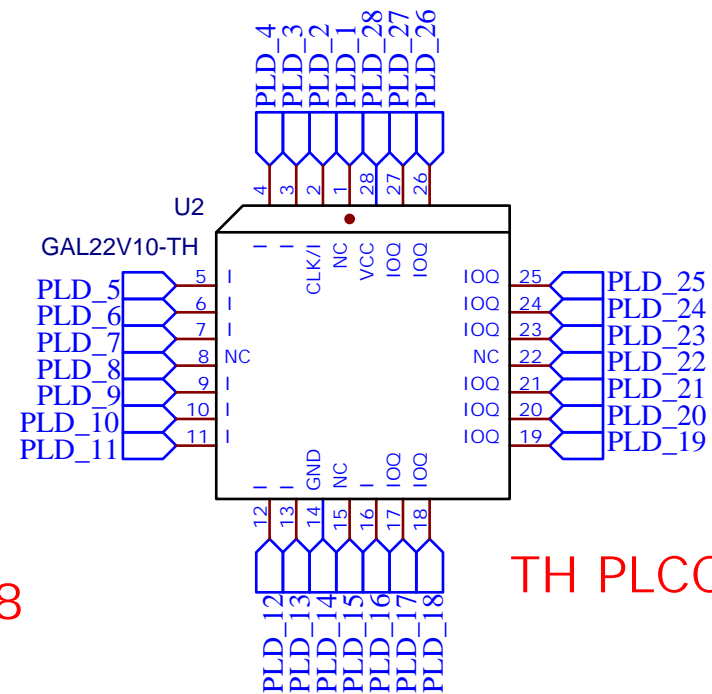
Company: eebugs.com

Sheet: 1/4

Date: 2023-10-26 Drawn By: cdhooper



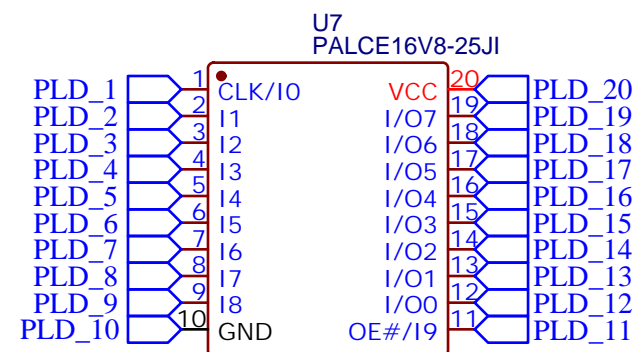
SMD PLCC28



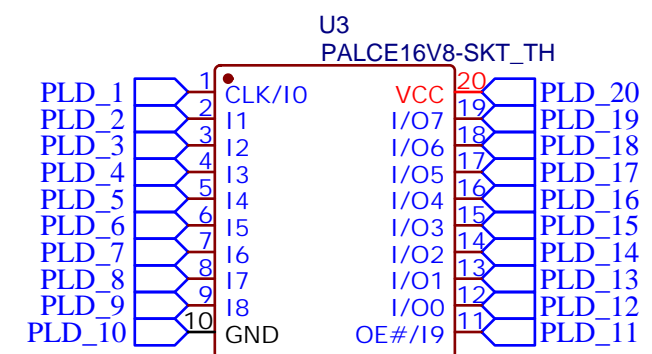
TH PLCC28

For PLCC28  
GAL22V10, jumper  
pin 14 to GND and  
pin 28 to VCC and  
set H5 to 1-2 (5V)

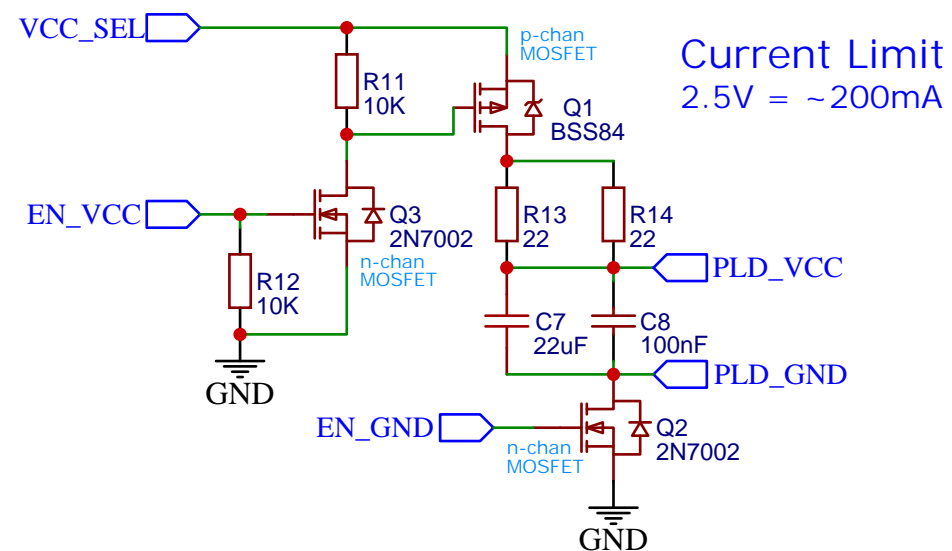
Below circuit allows  
isolation of both  
PLD VCC and PLD GND  
so that software can  
detect jumper positions  
and possibly also detect  
which socket is occupied.



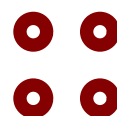
SMD PLCC20



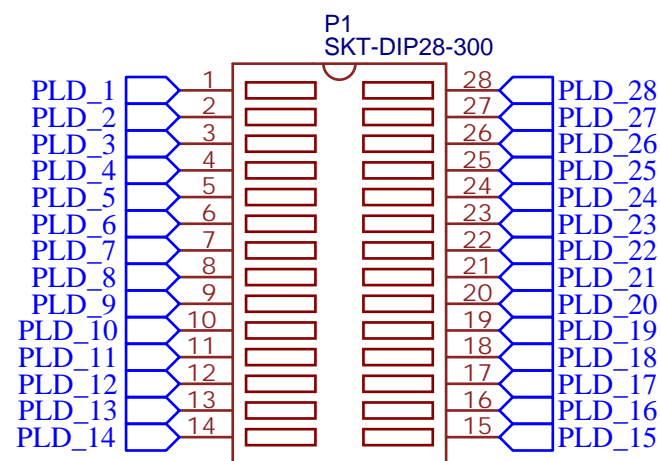
TH PLCC20



Mounting  
Holes

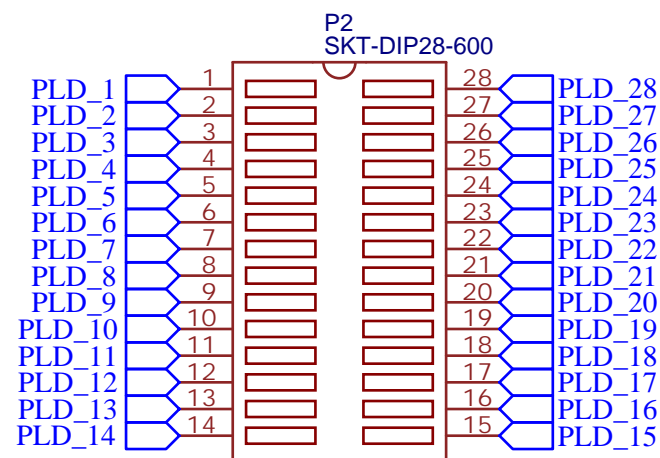


TITLE: PLD		REV: 2.0
EasyEDA	Company: eebugs.com	Sheet: 2/4
	Date: 2023-10-26	Drawn By: cdhooper



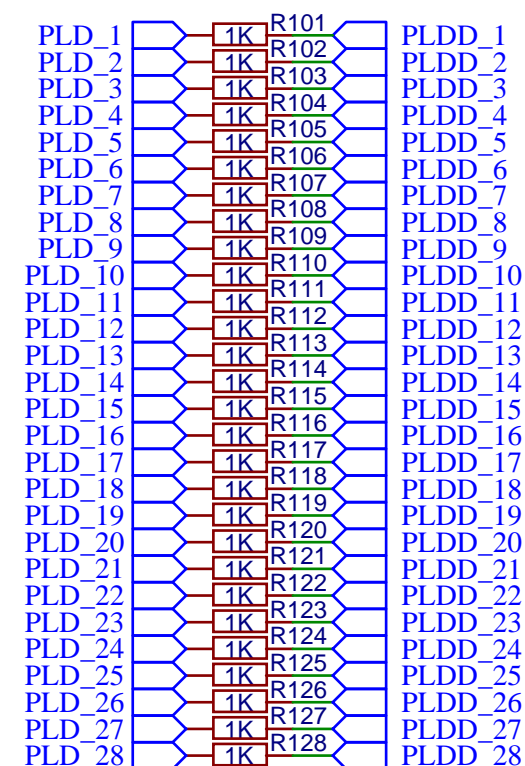
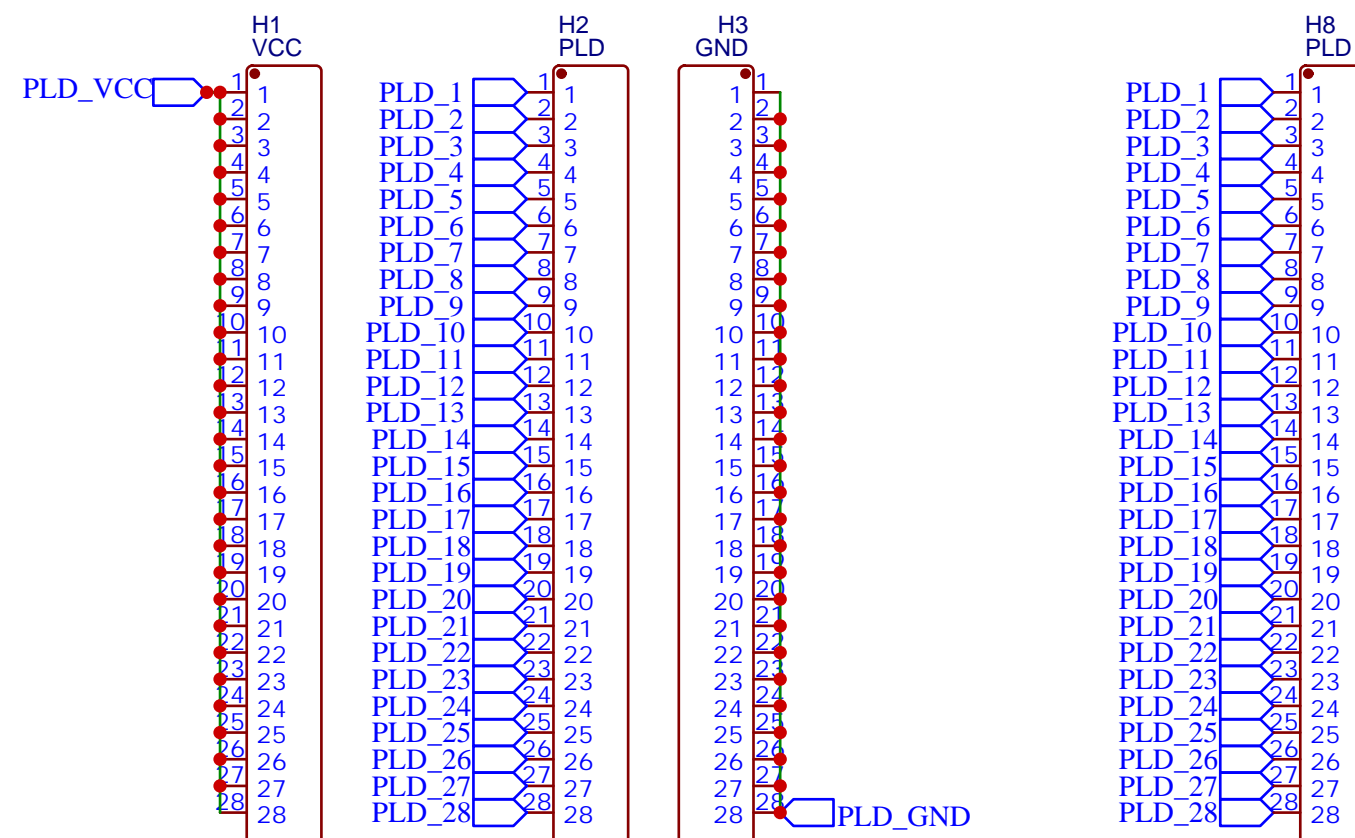
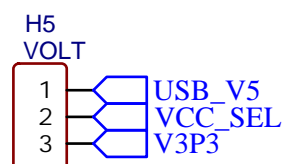
For DIP24  
GAL22V10, jumper  
pin 12 to GND and  
pin 24 to VCC and  
set H5 to 1-2 (5V)

DIP-28 300mil

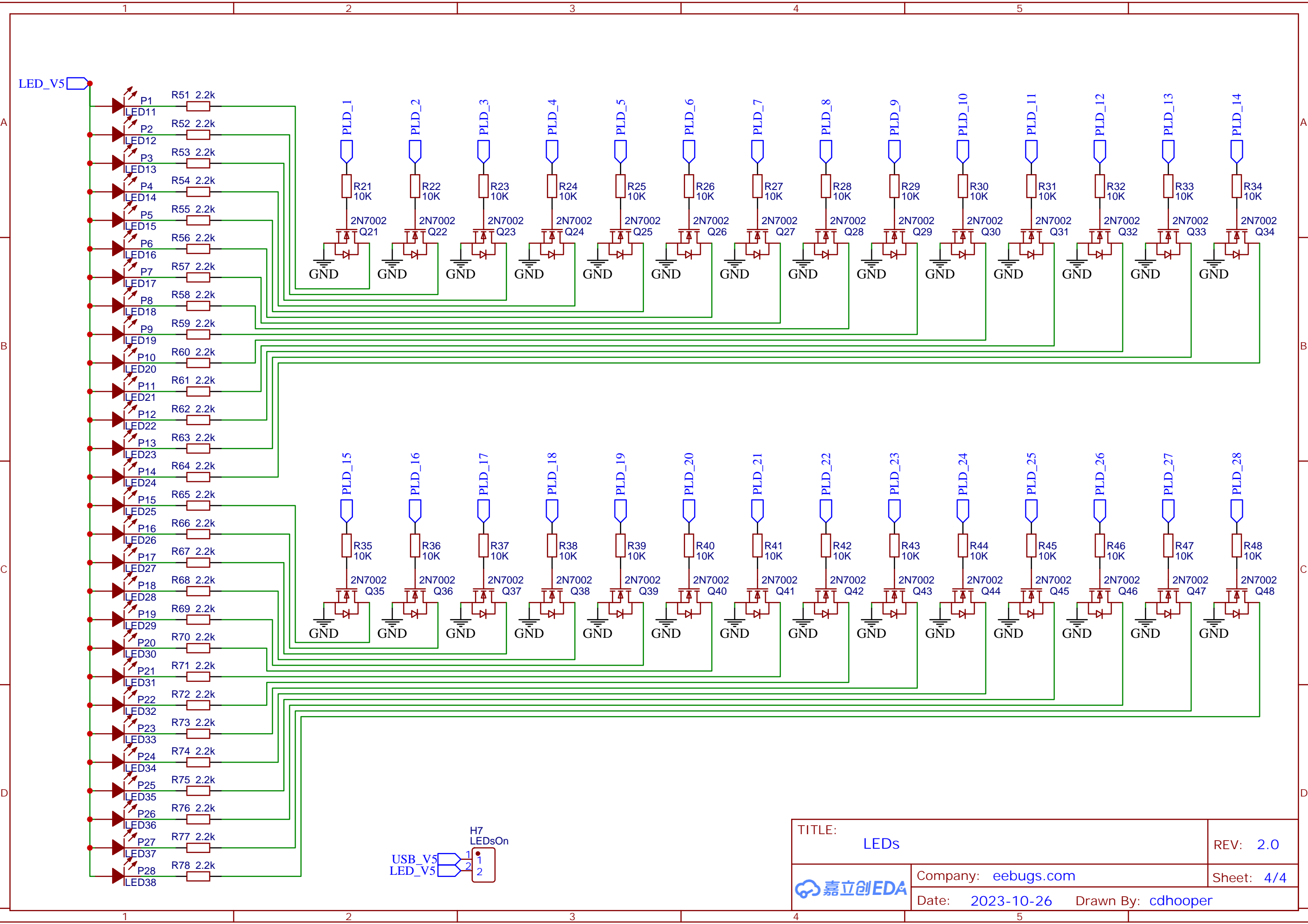


DIP-28 600mil

Socket power selection



TITLE: DIP		REV: 2.0
	Company: eebugs.com	Sheet: 3/4
	Date: 2023-10-26	Drawn By: cdhooper



TITLE: LEDs		REV: 2.0
嘉立创EDA	Company: eebugs.com	Sheet: 4/4
	Date: 2023-10-26	Drawn By: cdhooper