Atmel-WinCupl Examples

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Design File	Device Type (mnemonic)	Comments
Adder	G16V8	4-bit asynchronous adder (ripple carry)
ATF8255	F1502plcc44	24 I/O port expander, with Data Direction Register Control
Barrell22	G22V10	8-bit registered barrel shifter with .SP capability
Bench25b	V2500BLCC	State machine design
Ce_tff2	F1508PLCC84	T Flip Flop implementation using .CE
Clock	F1500A	Example shows Pin/PT clock, CE features
Cnt10	V750C	Two 10-bit counter
Cnt16	F1500A	16-bit counter
Cnt25b	V2500B	3 instances of 16-bit counter
Comreg	F1508PLCC84	Buried Register and Combinatorial output in a MC
Control	V2500	Frame grabber Controller
Convert	F1500A	D/T synthesis using the Register select keyword
Count10	G16V8A	4-bit UP/DOWN Decade counter with Synch. Clear
Ex375	V750C	20-bit counter
Expander	F1500A	Declaring and assigning Prod. term expanders
Fastlat	F1508PLCC84	Fast Latch Input feature
Gates	G16V8A	Simple use of NOT, EX-OR, AND, OR
Gcount	V750	8-bit Pseudo Grey Code Counter
Gcount_0	V750C	8-bit Pseudo Grey Code Counter
Iodecode	G20V8ALCC	Chip Select Signal generator for I/O functions, Memory
JKTest	f1504plcc44	J-K Flip Flop
Join2500	V2500LCC	Example of term joining
LatchSyn	F1502plcc44	Synthesis of Combinational Pin LATCH, and LE and D latches
Led32ser	F1502plcc44	LED driver, Serial BUS CLK.DAT.STR to 32 segment Latched drive;
Lookup	G22V10CPLCC	Use of TABLE keyword (calculate Circumference of Circle)
MTrafSig	V750BLCC	Traffic Control Signal
Mux	F1500A	Simple 2-1 Mux using Combinatorial Logic
Paporp	F1500AT	Use of Property Statements
PinKeep	F1500AT	Pin Keeper feature
Property	F1500AT	Usage of Property Statement
Pwm8x4	F1502plcc44	Quad 8 BIT PWM DACS, uC 8 Bit Busable, 4 Bytes IO mapped;
RegLatch	F1500A	D F/F and Level triggered latch implementation
Reset	F1500A	Pin Controlled and PT reset feature
Roulette	F1500A	Roulette Example
Shift16	F1500A	16-bit loadable shift register
T885	V750LCC	74885 (8-bit magnitude comparator)
T885_0	V750CLCC	74885 (8-bit magnitude comparator)
Testcpu	F1500A	10 bit counter using T Flip Flops
tim_cnt	F1508PLCC84	16 bit Timer Counter
Ttl	G20V8A	Multiple TTL chip representation using $Macros from $Include file
Xorcom	F1504plcc44	Combinatorial Output and EX-OR feature