## HCMOS 5x3.2mm 2.85V SMD Oscillator



Model: F520L Series

**RoHS Compliant / Pb Free** 

Rev. 5/7/2009

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http://www.foxonline.com/need\_a\_sample.htm



## **FEATURES**

## **OPTIONS**

- 2.85V Operation
- 5.0V (F550L) Version Available
- HCMOS Output
- · Standby Function
- Tape and Reel (1,000 pcs. STD)

PART NUMBER SELECTION Learn More - Internet Required						
Part Number	Model	Frequency	Operating	Frequency		
	Number	Stability <sup>1</sup>	Temperature (°C)	Range (MHz)		
661-Frequency-xxxxx	F520L	±100PPM	-10 ~ +70	1.544 ~ 80.000		
662-Frequency-xxxxx	F520LR	±100PPM	-40 ~ +85	1.544 ~ 80.000		
663-Frequency-xxxxx	F525L	±50PPM	-10 ~ +70	1.544 ~ 80.000		
664-Frequency-xxxxx	F525LR	±50PPM	-40 ~ +85	1.544 ~ 80.000		
499-Frequency-xxxxx	F526L	±25PPM	-10 ~ +70	1.544 ~ 80.000		
659-Frequency-xxxxx	F526LR	±25PPM*	-40 ~ +85	1.544 ~ 80.000		
520-Frequency-xxxxx	F528L	±20PPM*	-10 ~ +70	1.544 ~ 80.000		

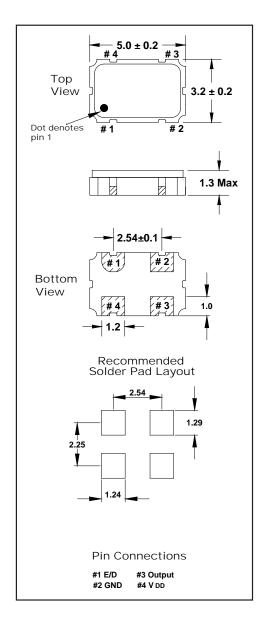
• ELECTRICAL CHARACTERISTICS						
PARAMETERS	MAX (unless otherwise noted)					
Frequency Range (Fo)	1.544 ~ 80.000 MHz					
Storage Temperature Range (Tstg)	-55°C ~ +125°C					
Supply Voltage (VDD)	2.85V ± 5%					
Input Current (IDD)						
1.544 ~ 32.000 MHz	12mA					
32.000+ ~ 50.000 MHz	20mA					
50.000+ ~ 80.000 MHz	40mA					
Output Symmetry (50% VDD)	45% ~ 55%					
Rise Time (10% ~ 90% VDD) (TR)	5nS					
Fall Time (90% ~ 10% VDD) (T <sub>F</sub> )	5nS					
Output Voltage (Vol.)	10% Vdd					
(Voh)	90% Vdd Min					
Output Current (IoL)	4mA Min					
(Іон)	-4mA Min					
Output Load (HCMOS)	15pF					
Standby Current	10μΑ					
Start-up Time (Ts)	10mS					
Output Disable Time <sup>2</sup>	150nS					
Output Enable Time <sup>2</sup>	10mS					

<sup>&</sup>lt;sup>1</sup> Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration. \*Excludes Shock/Vibration

Note: A  $0.01\mu F$  bypass capacitor should be placed between VDD (Pin 4) and GND (Pin 2) to minimize power supply line noise.

Drawing is for reference to critical specifications defined by size measurements.

Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary. All specifications subject to change without notice.



ENABLE / DISABLE FUNCTION					
INH (Pin 1)	OUTPUT (Pin 3)				
OPEN <sup>2</sup>	ACTIVE				
'1' Level Vih ≥ 70% Vdd	ACTIVE				
'0' Level VIL ≤ 30% VDD	High Z				

 $<sup>^{2}\,\</sup>mathrm{An}$  internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

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TAPE SPECIFICATIONS (millimeters)							
MODEL	Α	В	С	D	Е	F	STD Reel QTY
F520L Series	Ø1.5	4.0	8.0	5.5	12.0	1.5	1,000

REEL SPECIFICATIONS (millimeters)								
MODEL	G	Н	I J		K	L	M	
F520L Series	2.0	Ø13	Ø <b>21</b>	Ø62	Ø180	13.5	2.0	

