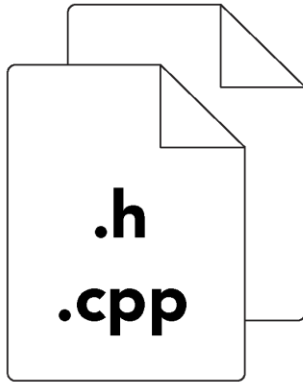
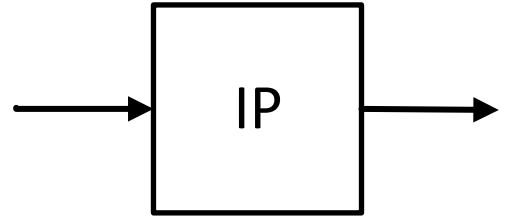


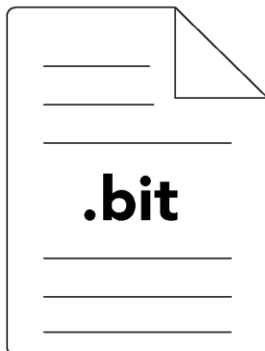
Modélisation SystemC



Synthèse et exportation IP



Génération d'un Bitstream



Implémentation sur Nexys 4

