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# Chris Wilson

## Vocational Experience

2022 – Present **Principal**, *Common Ground Electronics*, Castro Valley, CA
*Boutique embedded systems engineering services firm*

2022 **Technical Advisor**, *Tempo Automation*, San Francisco, CA

2018 – 2021 **Senior Technical Product Manager**

2018 **Technical Product Manager**
*Software-Accelerated PCBA Manufacturing*
- Conducted user, market, and competitive research to identify product opportunities in the customer experience and inform the product roadmap communicated to c-level leadership.
- Responsible for the design, planning, execution, and launch of real-time order status tracking and PCB design visualization features in the customer portal, contributing to a 19% increase in NPS from 2018 to 2019.
- Owned an initiative to reduce the time-to-RFQ by redesigning the bill of materials (BOM) editor, resulting in a 23% reduction in the median BOM issue resolution time.
- Established technical credibility with key customers by participating in IPC-2581 technical committee meetings, ultimately leading to investment from Lockheed Martin (Series C).

2010 – 2018 **Electronics Design Engineer**, *Cisco Systems*, San Jose, CA
*Industrial Internet of Things (IIoT) solutions for Smart Grid*
- Co-designed Cisco's first industrial IOx "fog" compute module, enabling customers to run custom IoT applications on Cisco 1000 Series Connected Grid Routers.
- Lead electronics design engineer for IEEE 802.15.4g hardware reference designs used by Cisco DevNet partners to develop 3rd-party Cisco Resilient Mesh End Point (CRME) devices.
- Developed the world's largest closed-circuit mesh network testbed consisting of over 5000 IoT hardware endpoint devices, unlocking CI/CD workflows and remote development/debug/testing for internal firmware development teams.

2007 – 2010 **Electronics Design Engineer**, *Arch Rock (acquired by Cisco Systems)*, San Francisco, CA
*Pioneer in IP-based wireless sensor network technology*
- Responsible for transition to agile in-house hardware design and manufacturing. Adopted industry standard EDA, DFM, PLM tools and methodology to scale hardware development from prototype to production.
- Designed and launched 802.15.4 2.4GHz PhyNet™ wireless sensors and router network interface cards for enterprise-scale wireless sensor networks.

2006 **Undergraduate Researcher**, *Berkeley Wireless Research Center*, Berkeley, CA
*Pre-competitive, public domain research*
- Implemented distributed adaptive duty cycling algorithm in nesC for Telos wireless sensor motes running TinyOS 1.x operating system.

2004 **Interim Engineering Intern**, *Qualcomm*, San Diego, CA
*CDMA Technologies form factor accurate (FFA) baseband team*
- Developed framework for an intranet website used to track internal development of FFA hardware.

## Personal Projects

2009 – 2015 **Owner**, *Flying Camp Design*, Castro Valley, CA
*Open-source hardware design*
- Designed open-source hardware boot-strap loader (BSL) programmer for TI MSP430 MCUs.
- Developed open-source cross-platform BSL GUI utility in Python.

2010 – 2014 **Partner**, *Moteware*, Berkeley, CA

*Open-source electronics disseminator for research groups and education*

- Founded with a group of former graduate students at UC Berkeley.
- Helped manage sales, support, IT, and manufacturing.

## Skills & Interests

Product Jira, Confluence, Git/Github, Python

Electronics KiCad, Cadence Concept & Allegro, OrCAD Capture, Autodesk EAGLE, Arena PLM, Oracle Agile PLM, lab safety, PCBA bring-up & rework

Interests Embedded systems, traveling, mountain biking, surfing

## Education

2003 – 2007 **B.S. Electrical Engineering and Computer Science**, *University of California Berkeley*, Berkeley, CA

Awards: Edward Frank Kraft Scholarship