

CFB600-300S SERIES 600 WATT 2:1 INPUT DC-DC CONVERTERS

FEATURES

- * 600W Isolated Output
- * Efficiency to 91%
- * Fixed Switching Frequency
- * Input Under-Voltage Protection
- * Over Temperature Protection
- * Over Voltage/Current Protection
- * Remote On/Off
- * Industry Full-Brick Package
- * UL 60950-1 Approval
- * Fully Isolated 3000VAC
- * Off-Line Systems Using PFC Front-Ends



MODEL	INPUT	OUTPUT	OUTPUT CURRENT INPUT CURR		URRENT	% Eff.	CAPACITIVE	
NUMBER	VOLTAGE	VOLTAGE	MIN.	MAX.	NO LOAD	FULL LOAD		LOAD MAX.
CFB600-300S1	2 180-425 VDC	12 VDC	0 mA	50 A	10 mA	2.24 A	89.5	10000uF
CFB600-300S2	180-425 VDC	24 VDC	0 mA	25 A	10 mA	2.21 A	90.5	10000uF
CFB600-300S4	3 180-425 VDC	48 VDC	0 mA	12.5 A	10 mA	2.20 A	91	8000uF

NOTE:

- 1. Nominal Input Voltage 300 VDC.
- 2. The output terminal required a minimum capacitor 470uF to maintain specified regulation.
- 3. Measure at Nominal Input Voltage.

SPECIFICATIONS

All Specifications Typical At Nominal Line, Full Load, and 25°C Unless Otherwise Noted

INPUT SPECIFICATIONS:

Input Voltage Pange	300V 180-425V
	Module on 480V
input over voltage i rotection	Module off 500V
Under Voltage Lockout	. 300Vin Power Up
Chack Vollago Lookout	300Vin Power Down 160V
Positive Logic Remote On/Off (no	
Input Filter	Capacitive
OUTPUT SPECIFICA	TIONS:
Voltage Accuracy	±1.5% max.
Transient Response:25% Step Lo	oad Change<500µs
External Trim Adj. Range (note4)	60-110%
Load share Accuracy	$\pm 10\%$ at 50% to 100% Full Load
Auxiliary output voltage/current	10±3Vdc/20mA max.
Ripple & Noise, 20MHz BW (note	3)
12V	75mV RMS max., 150mV pk-pk max.
24V	120mV RMS max., 240mV pk-pk max.
48V	200mV RMS max., 480mV pk-pk max.
Temperature Coefficient	±0.03%/°C
Short Circuit Protection	Continuous
• ,	±0.2% max.
	±0.5% max.
	nge, % Vo nom 115-140%
	105% -125% Nominal Output
Start up time	40ms typ.

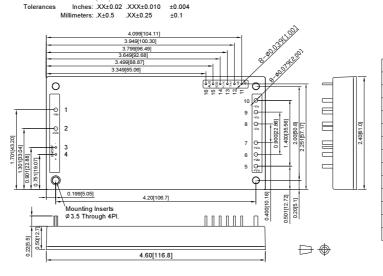
GENRAL SPECIFICATIONS:

Efficiency		9	See T	able
Isolation Voltage	Input/Output	3000	OVAC	min.
	Input/Case	2500	VAC	min.
	Output/Case	500	VAC	min.
Isolation Resistance		10 ⁷	ohm	min.
Switching Frequency		20	0KHz	typ.
Operating Case Temperatu	re	-40°C	to 10	ე0°С
Storage Temperature		. -55 ℃	to +10	05℃
Thermal Shutdown, Case Te	emp		105°C	typ.
Humidity	95% RH max	. Non c	onden	ısing
MTBF MIL-STD-217	7F, GB, 25℃, Full Load	42	0Khrs	typ.
Dimensions 4.6	60×2.40 x0.50 inches (116.	.8x61.0	x12.7	mm)
Case Material	Aluminum Baseplate	with Pl	astic C	Case
Weight			230g	typ.

NOTE:

- 1. Measured from high line to low line.
- 2. Measured from full load to zero load.
- 3. Output ripple and noise measured with min. capacitor 470uF and 1uF ceramic capacitor across output.
- 4. The output adjustment circuit and trim equations show as figure1 and figure2.
- 5. Logic compatibility open collector refer to -Vin
- 6. Suffix "N" to the model number with negative logic remote on/off Module off >3.5Vdc to 75Vdc or open circuit
- 7. An external input capacitor 330uF for all models are recommended to reduce input ripple voltage.

CASE FB



PIN CONNECTIONS			
PIN NUMBER	FUNCTION		
1	-V Input		
2	+V Input		
3	-On/Off		
4	+On/Off		
5 - 7	+V Output		
8 - 10	-V Output		
11	-S		
12	+S		
13	TRIM		
14	PC/NC		
15	IOG		
16	AUX		

Output Voltage = TRIM Terminal

Voltage * Nominal Output Voltage

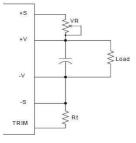


Figure 1 The schematic of output voltage adjusted by using external resistor and/or variable resistor.

The output voltage can be determined by below equations:

$$Vf = \frac{1.24 \times (\frac{Rt \times 33}{Rt + 33})}{7.68 + \frac{Rt \times 33}{Rt + 33}}$$

$$Vout = (Vo + VR) \times Vf$$

Unit: KΩ

Vo: Nominal Output Voltage Rt =6.8K Ω

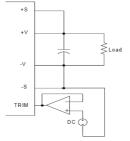


Figure 2 The schematic of output voltage adjusted by using external DC