

EEEE 281 Experiment 5:

RC Circuits

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To: Section 2B TA: Ryan Tatu, Jorge Wang, and Liam Callaham
Date: Performed: 9 February 2019 Due: 16 February 2019
Subject: Lab5-RC Circuits
Lab Partner(s): (edit)

Component	Percentage of Grade	Score	Comment
Report Formatting	20		
Theory: Quoting Step Response Derivation	5		
Hand Calculation: Rise Time Derivation	5		
Hand Calculation: τ /Rise Time Calculations	5		
PSPICE: Setup Conditions	5		
PSPICE: Data and Figures	10		
PSPICE: Discussion of Simulation	10		
Hardware: Experimental Setup	10		
Hardware: Experimental Data and Tables	10		
Hardware: Discussion of Results	20		
Total Score:			
Graded By:			

Abstract

The abstract section should contain a summary of what was performed in the lab and should be between 100-200 words. This should succinctly rephrase the purpose slide (slide 2 and lab packet). It should also refer to the data collected. How many circuit topologies were investigated (2 in this lab)? What theory/data is observed for each circuit (Hand calculation of rise time, PSPICE simulation extraction of rise time, Oscilloscope trace extraction of rise time).

1 Introduction

1.1 Derivation and Hand calculations

In this section, (do not submit as an outline)

1. Quote equation for the step response, $v_c(t)$, in the series RC circuit.
2. Define the time constant in equation form.
3. (**From Prelab**) Calculate the time constant for the four resistors/capacitor combinations listed in the lab handout. Include the information in a Table (See Table 1.1.1 below).
4. Determine the optimal pulse width, period, and frequency for each RC circuit and include in Table 1.1.1.
5. Derive an expression for the rise time ($t_{10\%}$ to $t_{90\%}$). Calculate the theoretical rise time for each RC combination. EDIT Table 1.1.1 below.

Table 1: Hand calculation of time constant, pulse width, optimal waveform generator period and frequency for each series circuit in the lab.

R (k Ω)	C (μ F)	τ (s)	Pulse Width (s)	Optimal Period (s)	Optimal Frequency (Hz)	Rise Time (s)
1	0.01	10^{-5}	10^{-4}	2.00×10^{-4}	5000	2.20×10^{-5}
10	0.01	10^{-4}	10^{-3}	2.00×10^{-3}	500.0	2.20×10^{-4}
100	0.01	0.001	0.01	0.02	50.0	2.20×10^{-3}
1	0.01	0.01	0.1	0.2	5.0	0.02197

1.2 PSPICE Simulation of RC Circuit

In this section, which is to be done as a part of the prelab. **Use the section heading above (1.2 PSPICE Simulation of RC Circuit), BUT DO NOT write the rest of the section as an outline.**

Begin by providing a 1 paragraph description of the PSPICE setup. Was a DC simulation used, transient simulation, etc.? Which **libraries** and **PSPICE elements** were used in the simulation? You can borrow from the text of your first tech memo here. If you do so, please be sure to cite the tech memo. Note the libraries used. You can find the information when you look at the properties of each element. There will be a reference to a “.olb” file. This is the library name.

Include a description of the Vpulse supply that was added to the circuit. Specifically identify the values that were adjusted (t_r , t_f , etc.). Explain the settings used for the transient simulation. What was the **Run to**

Time value chosen to be? Why? How was the Monte Carlo extraction of rise time performed (iterations, analysis function, etc.)? How were parameters used for the laboratory.

1.2.1 PSPICE Results for Circuits 1 and 2

1. Show the schematic diagram from PSPICE of the simulated circuits (Figure 1.2.1). You only need to include the schematic for the $10\text{ k}\Omega$ resistor/ $0.01\text{ }\mu\text{F}$ capacitor combination. You can show this based on the parameters on the schematic.

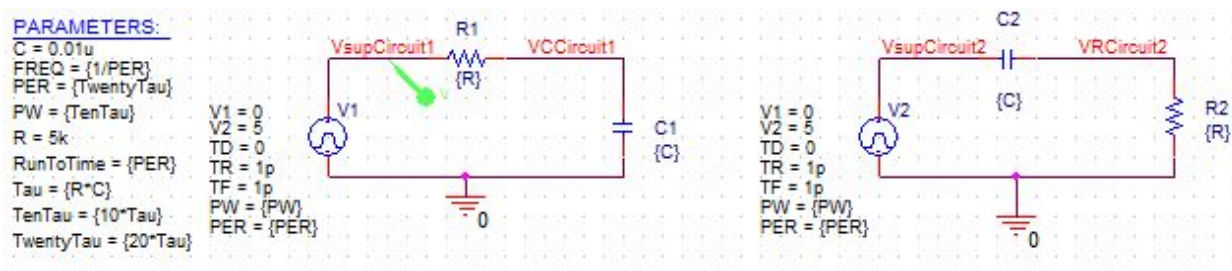
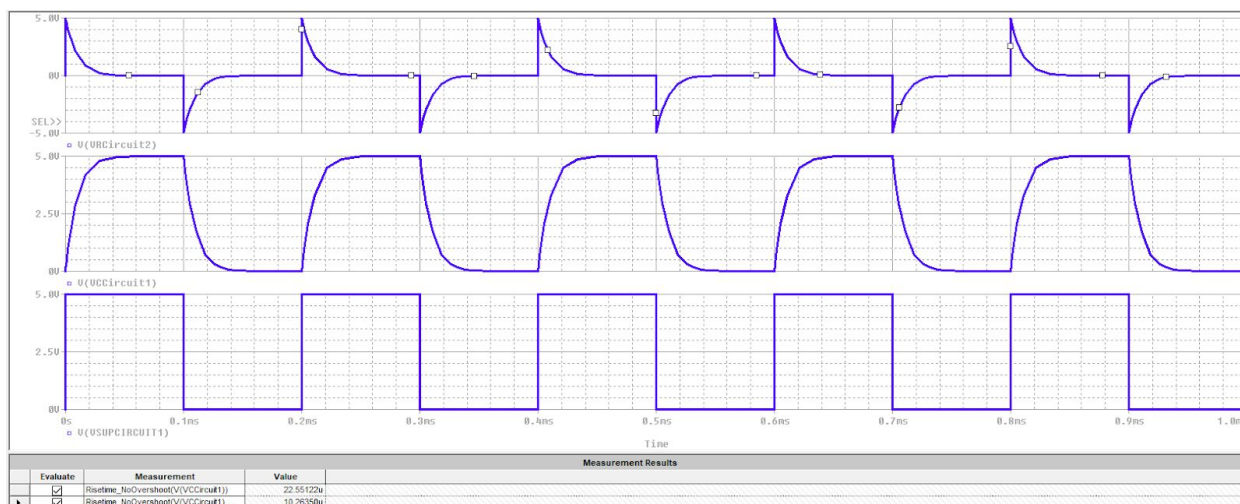


Figure 1: Schematic diagram in PSPICE of circuits 1 and 2.

2. Show the transient simulation and rise time extraction for each of the four circuits (Figs. 1.2.2 to 1.2.5). **Have the rise time clearly labeled.**
 - (a) Make sure that the picture from PSPICE mirrors the figures in the prelab presentation. You should thicken the line, and label the rise time on the figure (font should be legible).
 - (b) Caption should indicate the resistance and capacitance, as well as the extracted rise time.
 - (c) Provide a short discussion of the simulation results.

Figure 2: Transient simulation in PSPICE of circuits 1 and 2 for a $1\text{ k}\Omega$ resistor/ $0.01\text{ }\mu\text{F}$ capacitor. The rise time was XXX .

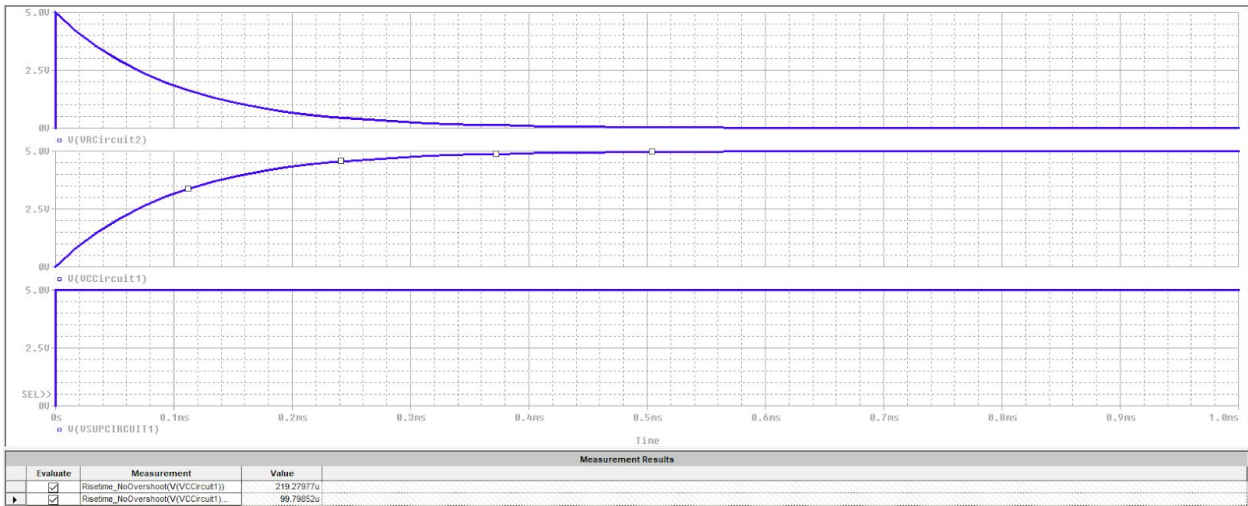


Figure 3: Transient simulation in PSPICE of circuits 1 and 2 for a 10 kΩ resistor/0.01 μF capacitor. The rise time was XXX .

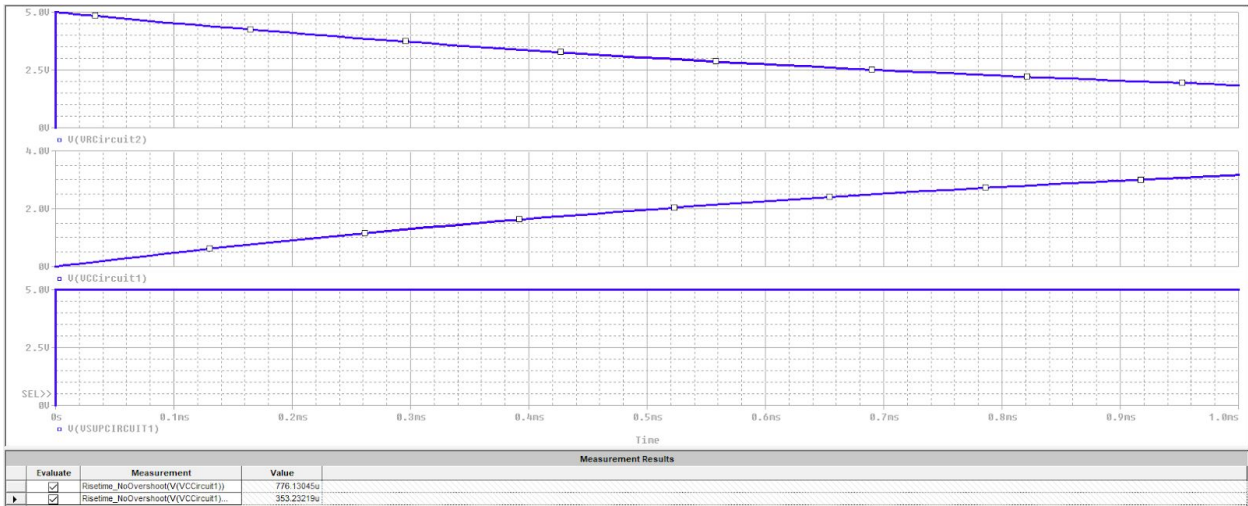


Figure 4: Transient simulation in PSPICE of circuits 1 and 2 for a 100 kΩ resistor/0.01 μF capacitor. The rise time was XXX .

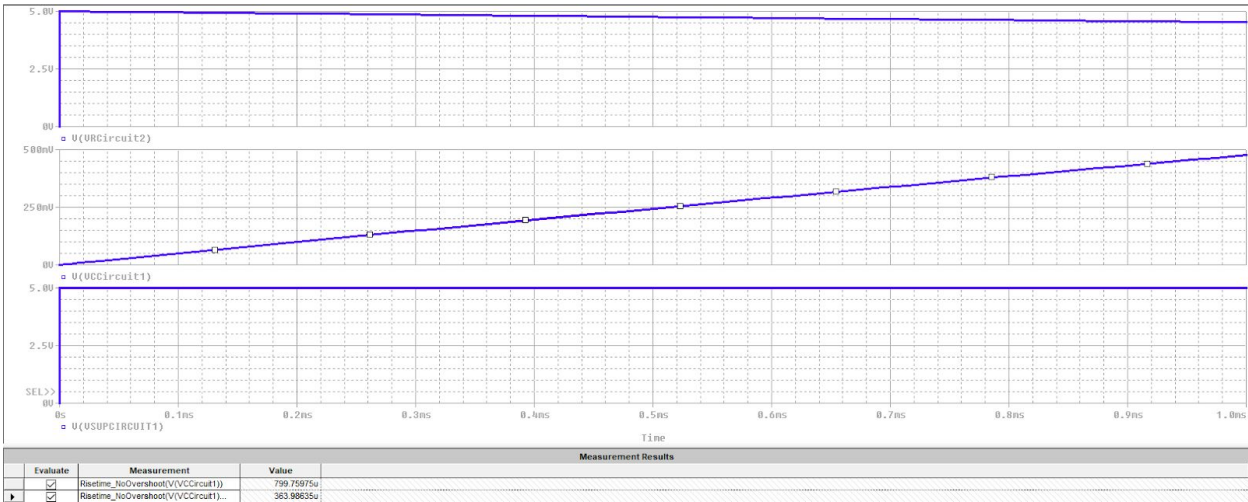


Figure 5: Transient simulation in PSPICE of circuits 1 and 2 for a 1M Ω resistor/0.01 μF capacitor. The rise time was XXX.

3. Summarize the Rise Time from all simulations in Table 1.2.1.

Table 2: Rise time extraction from PSPICE for each circuit in the lab.

Resistance (k Ω)	Capacitance (μF)	Rise time (μs)
1	0.01	22.55
10	0.01	219.28
100	0.01	776.13
1	0.01	799.76

1.2.2 PSPICE Circuit 1: Monte Carlo

Include the Monte Carlo Analysis for the $10\text{ k}\Omega$ resistor/ $0.01\text{ }\mu\text{F}$ capacitor combination.. A figure should be included showing the histogram output and the 100 iterations. Provide a short explanation of the variation in rise time. Make sure that there is at least 1 paragraph to explain this section.

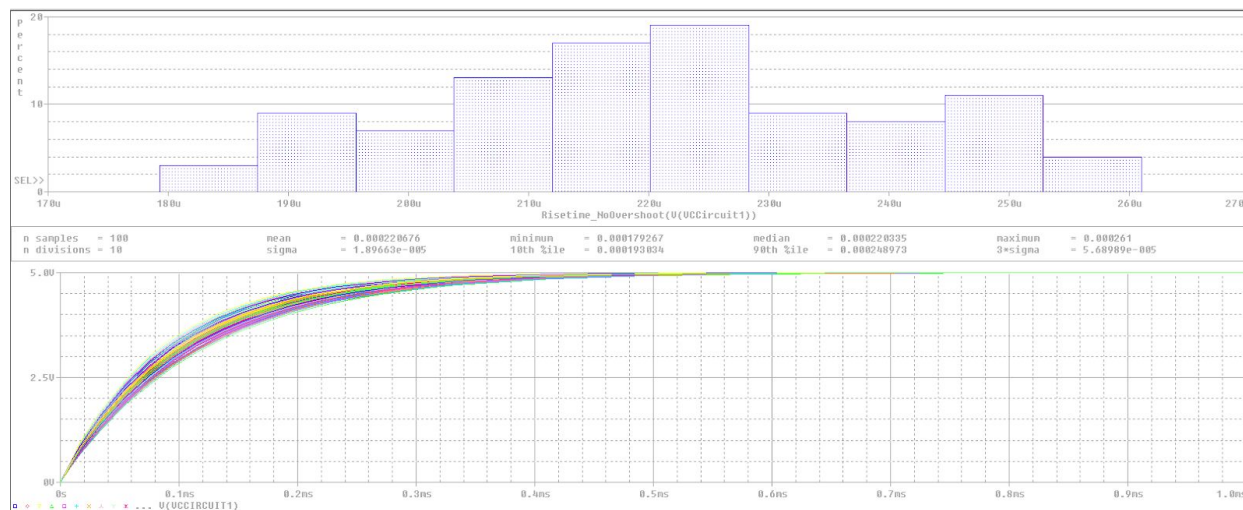


Figure 6: Monte Carlo extraction of the rise time for the $10\text{ k}\Omega$ resistor.

2 Hardware

This section of the report should present what was done in hardware. A reader should be able to recreate an experiment from the detail present. One section discusses the equipment used in the experiment. The remaining sections discuss the results for each circuit.

2.1 Equipment Used in the Laboratory

Write a short paragraph to detail the equipment used in the laboratory, and specific model numbers. Ideally, you should create a table of the equipment which should be referred to in text (See Table 2.1.1 as an example). The room location where the experiment was performed should be included. Note that this should be a part of all Tech Memos, as it is an essential piece for other users to replicate your experiment. **As you will be likely using the same equipment throughout the term, once the text/tables are established, you may reuse the information with the permission of your instructor/TA. Again, cite your first lab report as a reference.**

Table 3: Equipment/Software required for Lab 2.

Item	Tool	Room
Simulation	OrCAD Capture CIS	All Open EE Labs
DC Power Supply	Agilent E3630A	09-3170

DC Power Supply	Agilent E3631A	09-3200
Multimeter	Agilent E34401A	09-3170, 09-3200
Waveform Generator	Agilent 33120A	09-3170, 09-3200
Oscilloscope	Textronix TDS2012C	09-3200
Oscilloscope	Agilent DSO 3102A	09-3170

2.2 Hardware results from RC Circuit 1 (Voltage measured across the capacitor)

1. Show the Oscilloscope trace (Fig. 2.2.1) for the $10\text{ k}\Omega$ resistor/ $0.01\text{ }\mu\text{F}$ capacitor combination. **Have the rise time clearly labeled.** Note that the lab pack ONLY calls for this trace. Make sure that the plot clearly shows the date that the picture was taken. **Include the rise time in the figure caption as well.**

Figure 7: Oscilloscope trace of Circuit 1 for $10\text{ k}\Omega$ resistor/ $0.01\text{ }\mu\text{F}$ capacitor. A rise time of XXX is observed.

Summarize the results for all four circuits in Table 2.2.1.

Table 4: Rise time extraction from hardware for each circuit in the lab.

Resistance (Ω)	Capacitance (μF)	Rise time (s)
1	0.01	
10	0.01	
100	0.01	
1	0.01	

2. Briefly discuss the results and whether they agree with the hand calculations and PSPICE.

2.3 Hardware results from RC Circuit 2 (Voltage measured across the resistor)

1. Show the Oscilloscope (Fig. 2.3.1) trace for circuit. **Have the rise time clearly labeled.** Make sure that the plot clearly shows the date that the picture was taken. **Include the rise time in the figure caption as well.**

Figure 8: Oscilloscope trace of Circuit 2 for 10 k Ω resistor/0.01 μ F capacitor. A rise time of XXX is observed.

2. Summarize the results in Table 2.3.1.

Table 5: Hardware rise time extraction for Circuit 2.

Resistance (Ω)	Capacitance (μ F)	Rise time (s)
10	0.01	

3. Provide an explanation of the shape of the output

3 Conclusions

Summarize what was achieved in the experiment. This section should be similar to the abstract in tone, and often is a rephrasing of the abstract. It should summarize whether the experiment matched theoretical calculations and PSPICE extraction. It should be about 100-150 words. The conclusion differs from the discussion in that it is a brief summary of the discussion section (were the experiments successful?).

4 Acknowledgements

Acknowledge **any** source of help received in the experiment/writing the report. This should certainly include your lab partner/teaching assistant/instructor. It may also include other classmates/study partners. State briefly what the nature of the help was. **Your report should include references to appropriate pages in the text, as well as any other sources, websites/etc. consulted in the preparation of the report.**

References

- [1] C.K. Alexander, and M.K.O. Sadiku, *Fundamentals of Electric Circuits, 4th Edition*, McGraw Hill, pp. xx-yy(EDIT), 2009.
- [2] S. Rommel, *EEEE 281 Lab 5 Lecture notes*, slides xx-yy, Spring 2015.