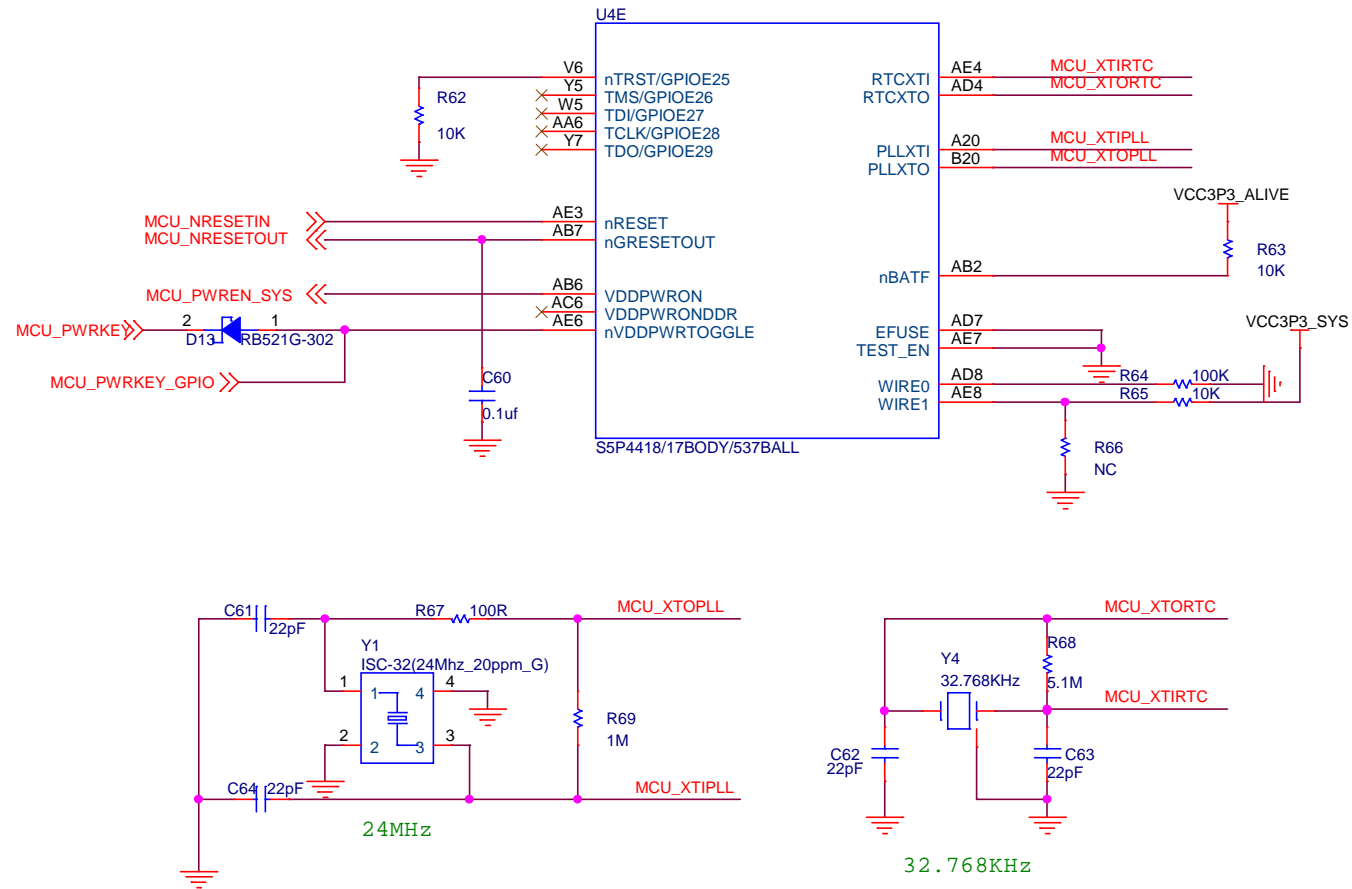


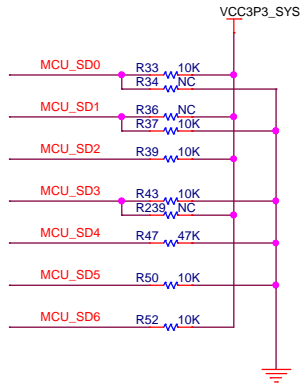
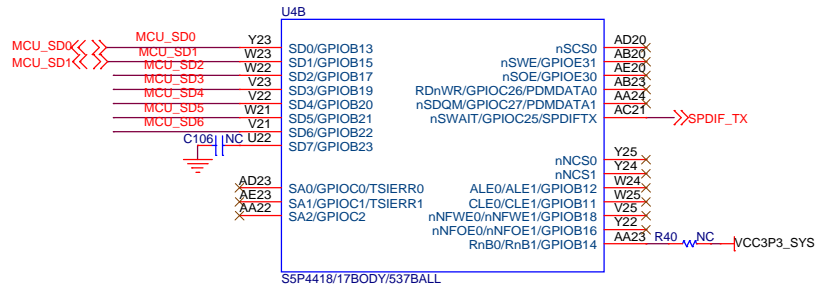
Title			
4818/6818 Core			
Size	Document	Number	Rev
A3	<Doc>		V1
Date:	Saturday, January 07, 2017	Sheet	2 of 9

S5P4418/6818 - SYSTEM CONTROL



Title 4818/6818 Core				
Size A4	Document Number <Doc>			Rev V1
Date:	Saturday, January 07, 2017	Sheet	3 of 9	

S5P4418/6818 - NAND & eMMC



BOOT MODE OPTION

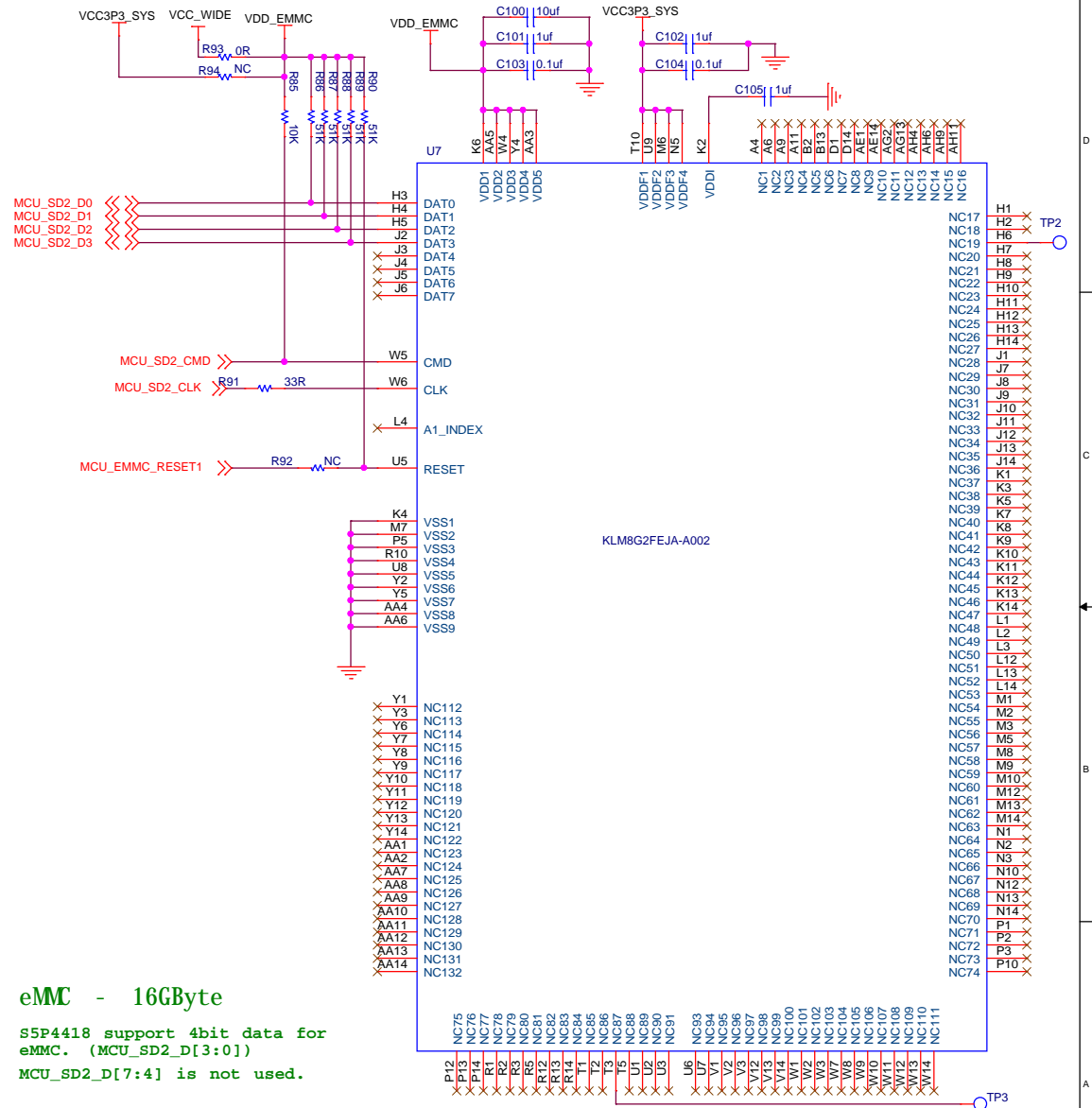
	eMMC	SPI	USB	NAND
MCU_SD0	HIGH	LOW	LOW	HIGH
MCU_SD1	LOW	LOW	HIGH	HIGH
MCU_SD2	HIGH	HIGH	HIGH	HIGH
MCU_SD4	LOW	HIGH		
MCU_SD5	LOW	LOW		

Boot media port select

(SPI, eMMC)	CH0	CH1	CH2
MCU_SD3	LOW	HIGH	LOW
MCU_CAM1_D3	LOW	LOW	HIGH

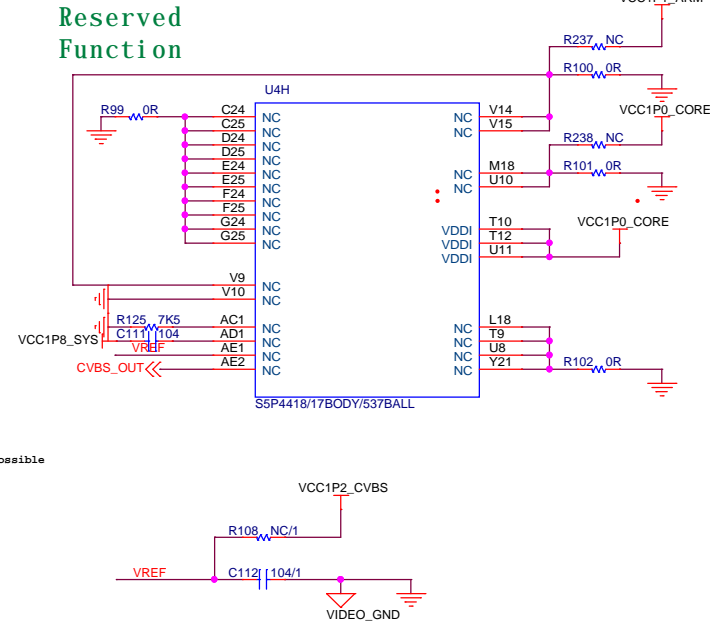
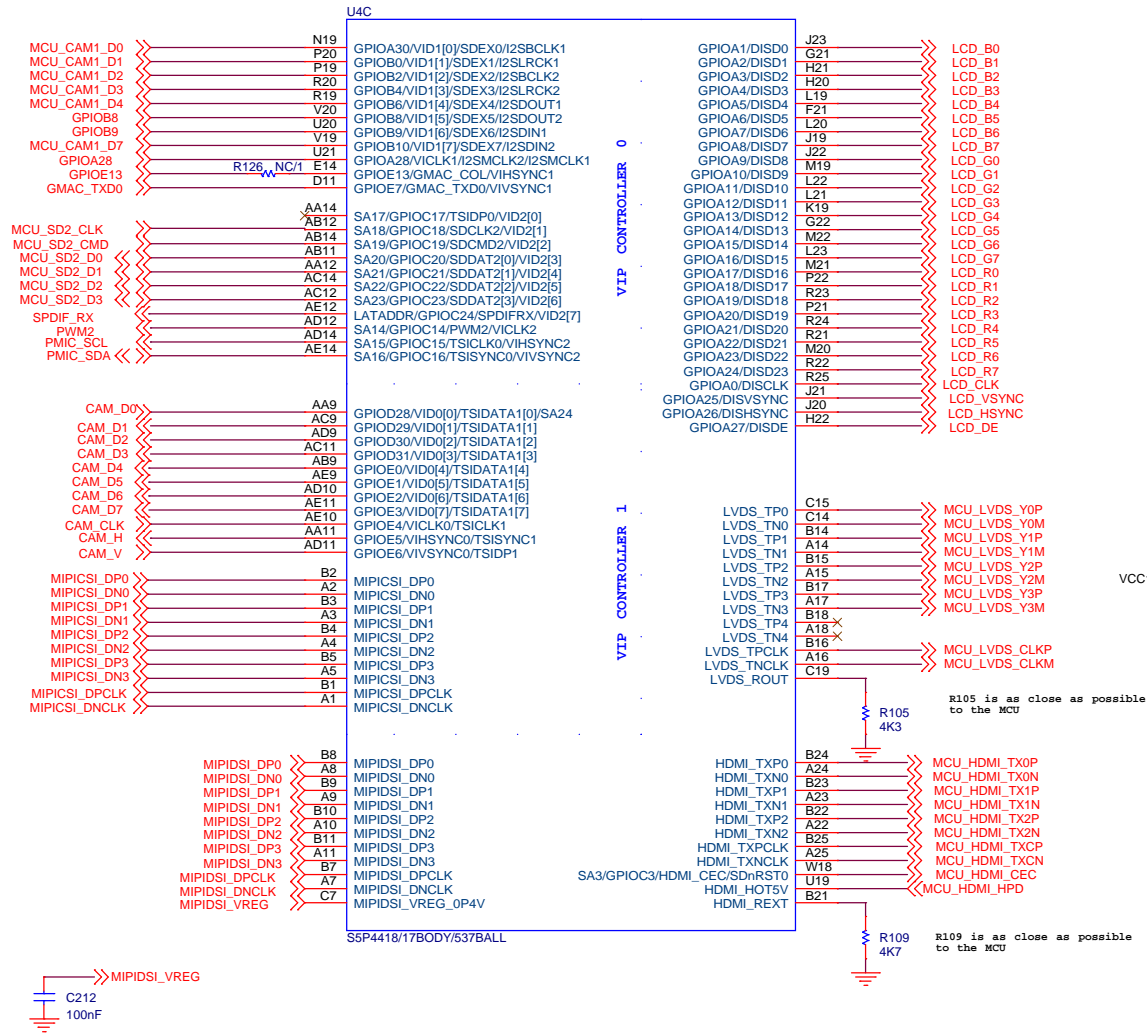
eMMC - 16GByte

S5P4418 support 4bit data for eMMC. (MCU_SD2_D[3:0])
MCU_SD2_D[7:4] is not used.



Title		
4818/6818 Core		
Size	Document Number	Rev
B	<Doc>	V1
Date:	Saturday, January 07, 2017	Sheet 5 of 9

S5P4418/6818 - DISPLAY & CAMERA



Title				
4818/6818 Core				
Size B	Document Number <Doc>			Rev V1
Date:	Saturday, January 07, 2017	Sheet	6 of 9	

S5P4418/6818 - PERIPHERALS

