

CELIA HERMOSO DÍAZ

Embedded Systems Master's Student at EIT Digital

@ celiahermosodiaz@gmail.com

+34 622111640

in celiahermosodiaz

celiahermoso.com



EXPERIENCE

Master thesis at Ericsson Research

Telefonaktiebolaget LM Ericsson

January 2023 - Present

Stockholm (Sweden)

- Performance evaluation of UE relay in 6G networks
- Analysis of coverage, capacity, and user throughput

Embedded Systems Engineer

Luna Glasses

November 2022 - April 2023

Netherlands (remote)

- Implementation of low light enhancement algorithms with OpenCV on a real-time platform, Luna smart Glasses, for visually impaired people.
- Embedded software development for Raspberry Pi and MIPI camera with v4l2

Research intern at EPFL Power Electronics Laboratory

École Polytechnique Fédérale de Lausanne (EPFL)

June 2021 - August 2021

Lausanne (Switzerland)

- Developed a Graphic User Interface for a Design Optimization tool of Medium Frequency Transformers

Worldwide Product Marketing Engineering Intern

Texas Instruments, Department of Marketing

July 2020 - September 2020

Munich (Germany)

- Portfolio analysis of Low Power DCDC components

EDUCATION

Double Master's Degree in Embedded Systems

KTH Royal Institute of Technology

Eindhoven University of Technology

September 2021 - Present

- European Institute of Technology and Innovation EIT Digital Master School dual degree program in Embedded Systems
- Summer School on Solutions for Healthier Digital Cities, University of Rennes I (France)

Exchange Program in Electronics Engineering

École Polytechnique Fédérale de Lausanne (EPFL)

September 2020 - July 2021

Lausanne (Switzerland)

- **Bachelor End Project** at EPFL Spacecraft Team. Responsible for the Electrical Power System of CHESS, a constellation of 3U satellites in Low Earth Orbit

Bachelor's Degree in Telecommunications Engineering

University of Granada

September 2017 - July 2021

Granada (Spain)

SKILLS

C	● ● ● ● ●
CUDA	● ● ● ● ●
LaTeX	● ● ● ● ●
MATLAB	● ● ● ● ●
OrCAD	● ● ● ● ●
Python	● ● ● ● ●
SOLIDWORKS	● ● ● ● ●
VHDL	● ● ● ● ●

TECHNICAL PROJECTS

- Implementation and Evaluation of Second Order Derivative Edge Detection Algorithms on FPGAs
- FIFO library and image processing for a CompSOC platform
- Optimization techniques for 1D and 2D convolution implementation with CUDA
- Logistic control of silicon wafers in an EUV ASML wafer scanner with the mCRL2 toolset
- Wireless Sensor Network design for aquaponics greenhouse monitoring application using Cooja simulator in Contiki OS
- **EPFL Spacecraft Team.** Design and Simulation of the Electrical Power System of 3U CubeSat in SSO and Elliptical Orbit for CHESS mission
- Designed a communications system for a mission to Europa at the Ladybird Guide to Spacecraft Communications program at ESA
- Mechanical design of printed circuits boards using SOLIDWORKS

ENTREPRENEURIAL PROJECTS

- **Innovation Space Center of Eindhoven University of Technology.** Start up development. SEEK provides advice on current assistive technologies for improving visually impaired people independent living.
- Design of a new corporate business initiative for recycled E-bike batteries.

LANGUAGES

Spanish	Native
English	FCE B2 and TOEFL 101
French	Diplôme d'études B2

ADDITIONAL INTERESTS

Leadership. Empower high school female students toward STEM majors in college through a summer school