Power Mac G5 -- Single Processor Developer Note

2004-10-19

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Introduction to Power Mac G5 -- Single Processor Developer Note

This developer note describes the single processor, 64-bit Power Mac G5. The note provides information about the internal design of the computer, its input-output and expansion capabilities, and issues affecting compatibility.

This developer note is intended to help hardware and software developers design products that are compatible with the Macintosh products described here. If you are not already familiar with Macintosh computers or if you would simply like additional technical information, refer to Appendix A, "Supplemental Reference Documents", (page 59) for additional information.

Organization of This Document

The information is arranged in four chapters and two appendixes:

- Chapter 1, "Overview to the Single Processor Power Mac G5", (page 11) gives a summary of the features of the Power Mac G5 single processor computer, describes the physical appearance of the enclosure, and lists compatibility issues of interest to developers.

- Chapter 2, "Architecture", (page 19) describes the internal organization of the computer. It includes a functional block diagram and descriptions of the main components on the logic board.

- Chapter 3, "Input and Output Devices", (page 29) describes the built-in I/O devices and the external I/O ports.

- Chapter 4, "Expansion", (page 53) describes the expansion slots on the logic board and provides specifications for the expansion modules.

- Appendix A, "Supplemental Reference Documents", (page 59) provides sources of additional information about the technologies used in the single processor Power Mac G5.

- Appendix B, "Conventions and Abbreviations", (page 65) lists standard units of measure and other abbreviations used in this developer note.

Overview to the Single Processor Power Mac G5

The single processor, 64-bit desktop Power Mac G5 uses a PowerPC G5 processor and is intended for use in content creation, desktop publishing, multimedia, scientific and technical applications, and other activities that require high performance.

New Hardware Features

Here is a list of the new hardware features of the single processor Power Mac G5. A list of all hardware features is provided in "Hardware Features Summary" (page 11).

- **Microprocessor**: The single processor Power Mac G5 has a 1.8 GHz PowerPC G5 processor. For more information, see "PowerPC G5 Processor" (page 21).

- **Processor system bus**: The processor interface system bus runs at one-third the speed of the system processor. For more information, see "Processor Bus" (page 21).

- **Memory bus**: The main memory bus is 128 bit 400 MHz. For more information, see "Main Memory Bus" (page 22).

- **DDR SDRAM**: The computer has four DIMM slots for 184 pin DIMMs (dual inline memory modules) using unbuffered DDR (double data rate) SDRAM 64-bit, 400 MHz (PC3200) devices for a maximum of 4 GB. For more information, see "Main Memory Bus" (page 22).

- **Graphics card**: The computer comes standard with the Nvidia GeForce Fx 5200 Ultra with 64 MB RAM. Available as build-to-order options are the ATI Radeon 9800 XT with 256 MB RAM, the ATI Radeon 9600 XT with 128 MB RAM, the Nvidia GeForce 6800 Ultra DDL with 256 MB RAM, and the Nvidia GeForce 6800 GT DDL with 256 MB RAM. For more information, see "Graphics Cards" (page 27).

- **Hard disks**: An internal 7200 rpm Serial ATA (SATA) hard disk occupies one of two drive bays and functions as the default boot disk. A second SATA drive is available as a build-to-order option. The single processor Power Mac G5 comes standard with an 80GB SATA hard drive, and 160GB and 250GB SATA drives are available as build-to-order options For more information, see "Serial ATA Interface" (page 26).

Hardware Features Summary

Here is a list of the hardware features of the single processor Power Mac G5. The major features are described more fully later in this note. To view technical Marketing documents, refer to http://www.apple.com/power-mac/.

- **Microprocessor**: The single processor Power Mac G5 computer has a 1.8 GHz PowerPC G5 processor. For more information, see "PowerPC G5 Processor" (page 21).

- **Processor system bus**: The processor interface system bus runs at one-third the speed of the system processor. For more information, see "Processor Bus" (page 21).

- **Memory caches**: The PowerPC G5 microprocessor used in the computer has an internal 512 KB level 2 cache.

- **Memory bus**: The main memory bus is 128 bit 400 MHz. For more information, see "Main Memory Bus" (page 22).

- **DDR SDRAM**: The computer has four DIMM slots for 184 pin DIMMs (dual inline memory modules) using unbuffered DDR (double data rate) SDRAM 64-bit, 400 MHz (PC3200) devices for a maximum of 4 GB. For more information, see "Main Memory Bus" (page 22).

- **AGP 8x Pro card slot**: The computer has an AGP 3.0 8x Pro accelerated graphics card installed in this slot. For more information, see "Accelerated Graphics Port Bus" (page 22).

- **PCI expansion slots**: The computer supports three 64-bit 33 MHz PCI slots. For more information, see "PCI Expansion Slots" (page 23).

- **Hyper Transport bus**: The computer supports the high-speed bus architecture between the memory controller and device I/O. For more information, see "HyperTransport Technology" (page 23).

- **Graphics card**: The computer comes standard with the Nvidia GeForce Fx 5200 Ultra with 64 MB RAM. Available as build-to-order options are the ATI Radeon 9800 XT with 256 MB RAM, the ATI Radeon 9600 XT with 128 MB RAM, the Nvidia GeForce 6800 Ultra DDL with 256 MB RAM, and the Nvidia GeForce 6800 GT DDL with 256 MB RAM. For more information, see "Graphics Cards" (page 27).

- **Hard disks**: An internal 7200 rpm Serial ATA (SATA) hard disk occupies one of two drive bays and functions as the default boot disk. A second SATA drive is available as a build-to-order option. The single processor Power Mac G5 comes standard with an 80GB SATA hard drive, and 160GB and 250GB SATA drives are available as build-to-order options. For more information, see "Serial ATA Interface" (page 26).

- **Power supply**: The computer comes with a 350 W power supply.

- **SuperDrive**: The computer has a SuperDrive (DVD-R/CD-RW drive) connected to the Ultra DMA ATA/100 bus. For more information, see "SuperDrive" (page 36).

- **Combo drive**: Instead of a SuperDrive, the computer has an optional build-to-order combination DVD-ROM/CD-RW drive connected to the Ultra DMA ATA/100 bus. For more information, see "Combo Drive" (page 37).

- **High-speed USB 2.0**: The computer has three external USB 2.0 ports: two on the rear of the enclosure and one on the front. All ports support both high-speed and classic USB speeds. For more information, see "USB Ports" (page 29).

- **Ethernet**: The computer has a built-in Ethernet port for 10Base-T/UTP, 100Base-TX, or 1000Base-TX Gigabit operation. The Ethernet port is auto-sensing and self-configuring to allow use of either a cross-over or straight-through cable. For more information, see "Ethernet Port" (page 34).

- **AirPort Extreme Card**: Available as a build-to-order option, the 54 Mbps AirPort Extreme Card provides a wireless LAN connection. For more information, see "Wireless LAN Module" (page 24).

- **Bluetooth**: Available as a build-to-order option, the fully-integrated Bluetooth module enables short-range wireless connections between desktop and laptop computers and a host of other peripheral devices. For more information, see "Bluetooth Technology" (page 39).

- **FireWire ports**: The computer has two FireWire 400 ports and one FireWire 800 port. For more information, see "FireWire Controllers" (page 25).

- **Modem**: The computer has a built-in Apple 56 Kbps modem. The modem supports K56flex and V.90 and V.92 modem standards. For more information, see "Internal Modem" (page 37).

- **Keyboard**: The computer comes with a full-size USB Apple Keyboard. The keyboard is also a bus-powered USB hub with two USB ports. For more information, see "Keyboard" (page 40).

- **Mouse**: The computer comes with a USB Apple Mouse, with optical tracking. For more information, see "Mouse" (page 43).

- **Boot ROM**: The computer comes with 1 MB of boot ROM.

- **S/PDIF digital audio**: The computer implements S/PDIF (Sony/Phillips Digital Interface) digital audio I/O via TOSLink optical input and digital output connectors on the rear panel. For more information, see "Sound System Overview" (page 26).

- **Analog audio line-in**: Rear panel analog audio line-in port for self-powered microphones or other audio equipment. For more information, see "Audio Line-in" (page 45).

- **Analog audio line-out**: Rear panel analog audio line-out port for externally-powered speakers or other audio devices with line-level outputs. For more information, see "Audio Line-out" (page 45).

- **Headphone and internal speaker**: The front panel includes a headphone mini-jack and the system includes a front mono speaker. For more information, see "Headphone Jack" (page 46).

- **DVI to video adapter**: The computer supports S-video and composite video from an optional adapter connected to the DVI port. For more information, see "DVI to Video Adapter" (page 50).

- **Voltage switching**: Auto-ranging voltage switching accepts 100 - 240V.

- **Fan speed control**: The speeds of the fans are controlled by thermal/wattage factors and are automatically set as low as possible to minimize noise.

- **Dimensions and weight**: The dimensions of the single processor Power Mac G5 are 20.12" (511 mm) high by 8.11" (206 mm) wide by 18.70" (475 mm) deep and weighs approximately 36 pounds.

- **Energy saving**: Processor performance can be set to automatic (default), highest, or reduced via the Energy Saver pane in System Preferences. See "Power Management" (page 15) for more detail.

Features of the Enclosure

The single processor Power Mac G5 computer's enclosure is a tower design. Figure 1-1 (page 14) illustrates the front of the enclosure and Figure 1-2 (page 14) illustrates the rear of the enclosure.

Figure 1-1   Single processor Power Mac G5 front view

Optical drive

Power button/Status light

Headphone jack

⚡ USB 2.0 port

ᵗᶠ FireWire 400 port
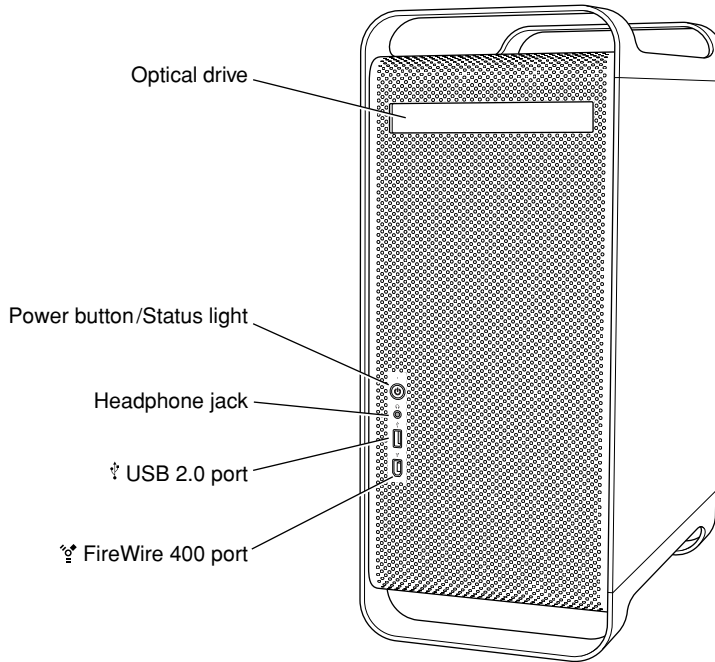
Figure 1-2   Single processor Power Mac G5 rear view

Door latch

DVI display port

Power socket

Access covers for PCI expansion slots

ADC display port

📶 AirPort antenna port

✳ Bluetooth antenna port

Optical digital audio out port

Optical digital audio in port

◀)) Audio line-out port

▶◀ Audio line-in port

⚡ USB 2.0 ports (2)

ᵗᶠ FireWire 400 port

ᵗᶠ FireWire 800 port

↔ Ethernet port

☎ Internal modem port (on some models)
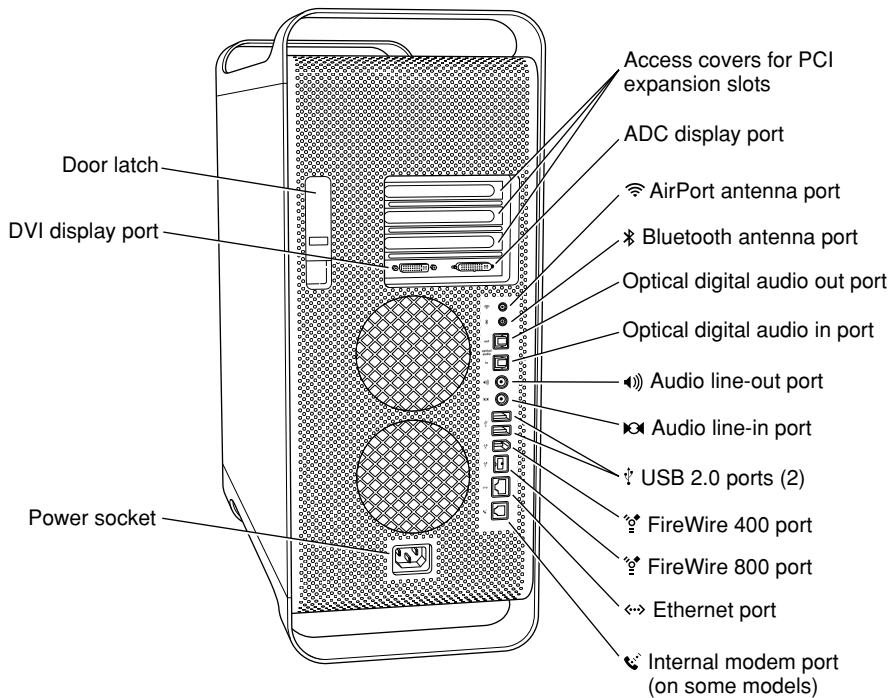
The enclosure has space for two hard disk drives and one optical device. See "Hard Disk Drives" (page 36).

To access the main logic board to install PCI cards or additional memory, refer to "Expansion" (page 53). For full details regarding opening and accessing the computer, refer to the *Power Mac G5 User's Guide* that was shipped with your computer.

⚠️ **Warning:** Opening the enclosure side panel impacts the thermal performance of the computer. Do not run the enclosure with the side panel open. If the air deflector inside the enclosure is removed, the system will take action to prevent over-heating.

System Software

The single processor Power Mac G5 computer ships with Mac OS X 10.3.5 installed as the default operating system. The classic environment can be used to run Mac OS 9 applications. Install the Classic application from the "Additional Software and Apple Hardware Test" CD shipped with your computer.

To retrieve information from the I/O Registry, use the APIs in the header file IOKit/IOKitLib.h.

Computer Identification

Rather than reading the box flag or the model string and then making assumptions about the computer's features, applications that need to find out the features of the computer should use I/O Registry calls to test for the features they require.

Asset management software that reports the kind of computer it is run on can obtain the value of the property at `Devices:device-tree:compatible` in the IODeviceTree plane of the I/O Registry. The model string is the first program-usable string in the array of C strings in the `compatible` field. For the single processor Power Mac G5, the value of the model property is `PowerMac9,1`.

Power Management

The power management techniques implemented in the single processor Power Mac G5 are described in the following paragraphs.

Processor Power Step

To lower power consumption, heat generation, and fan noise, the single processor Power Mac G5 computer incorporates an automatic power management technique called power stepping. Power stepping is designed to run at high processor and bus speeds and high voltage when the demand on the processor is high, and to run at low processor and bus speeds and low voltage when the demand on the processor is low. The stepped processor speed is either 900 MHz or 1.8 GHz. Switching between different processor/bus speeds and voltages is achieved by a very fast transition that is designed to minimally impact system or application performance and typically operates seamlessly to the user.

In addition, the single processor Power Mac G5 computer allows the user to control processor and bus. The options for specifying either high, reduced, or automatic processor and bus speeds are located at System Preferences>Energy Saver>Options; then select Automatic (default), Highest, or Reduced.

> **Note:** For best performance when using professional applications with high demand on the system (such as Final Cut Pro or Logic), you may want to set the processor/bus speed option to highest.

If the single processor Power Mac G5 computer detects a system temperature that is too high, due to high ambient temperatures or other factors, it will automatically reduce the processor and bus speeds to half speed, regardless of the selected setting. If the temperature continues to be too high, the system will enter sleep mode to prevent permanent damage.

Processor States

The following processor states are defined:

- Run: The system is running at maximum processing capacity with the processor running at full speed.

- Idle: The system is idling; this is the default state. All clocks are running and the system can return to running code within a few nanoseconds. If the system has no work to do, it will be in idle mode.

  From idle, the processor will enter deep nap state when the system is power-stepped to half frequency. In this state, the processor's internal frequency is reduced to 1/64 of the maximum frequency. The processor frequency is returned to normal on exit from idle. The exit from idle takes a bit longer than the regular idle nap but is still in the nanoseconds.

System Modes

The Macintosh system has the following power-saving modes, which are set in the Energy Saver pane. If the system does not support full sleep, it will use the less efficient doze mode.

- Run: The system is fully functional in normal operating mode with all components powered and operating.

- Doze (default): The power to the disk drive motors and the display is turned off, but the power supply and fans are still on. PCI cards that don't support full system sleep (some SCSI cards, some graphics cards) will cause the machine to doze rather than fully sleep.

- Full sleep: The main power supply is shut down. A trickle supply provides auxiliary power to the PCI slots and keeps the DRAM state preserved for a quick recovery. The processor is powered off with its state preserved in DRAM. All non-essential clocks in the system are suspended. This mode allows the computer to meet Energy Star sleep requirements while providing the ability to start up without rebooting. This system may be awakened by administrative network packets, keyboard or mouse activity, USB device insertion or removal, or SMU scheduled wakeup.

  > **Note:** While in sleep mode, the computer emits no noise and the power light pulses slowly.

- Off: The single processor Power Mac G5 meets FEMP (Federal Energy Management Program) requirements and supports FireWire ports functioning as repeaters when operating on AC power.

Velocity Engine Acceleration

The Velocity Engine is the vector processing unit in the PowerPC G5 microprocessor. System software has been modified to take advantage of the accelerated processing that the Velocity Engine makes possible and to support low-level operations using the Velocity Engine.

For complete information on the Velocity Engine, refer to the following Apple website:

http://developer.apple.com/hardwaredrivers/ve/index.html

Architecture

This chapter describes the architecture of the single processor Power Mac G5. It includes information about the major components on the logic boards: the microprocessor, the other main ICs, and the buses that connect them to each other and to the I/O interfaces.

Block Diagram and Buses

The architecture of the single processor Power Mac G5 is based on the PowerPC G5 microprocessor, and two custom ICs: the U3L memory controller and bus bridge and the Shasta I/O controller.

Figure 2-1 (page 20) is a simplified block diagram of the single processor Power Mac G5. The diagram shows the U3L and Shasta ICs and the buses that connect them together.

Figure 2-1   block diagram
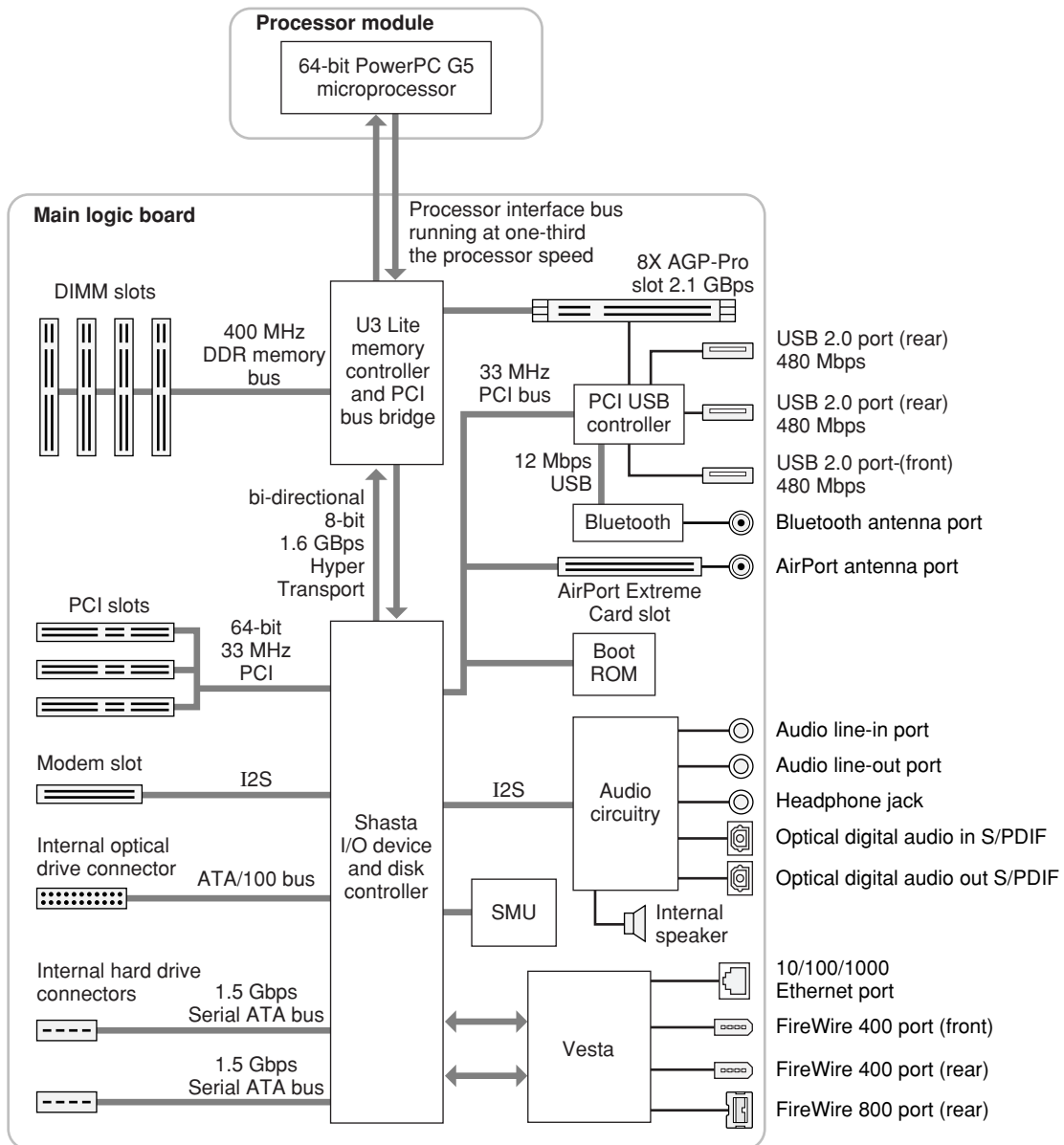
The single processor Power Mac G5 has the following data buses, not counting the processor's dedicated interface to the backside cache.

■   Processor bus: 600 MHz (running at one-third the speed of the processor), 2 unidirectional buses that deliver 64-bit data throughput connecting the processor to the U3L IC

■   Memory bus: 400 MHz, 128-bit bus connecting the main DDR SDRAM memory to the U3L IC

■   8x AGP Pro bus: 266/533 MHz, 32-bit bus connecting the AGP graphics card to the U3L IC

■   PCI bus: supports three 64-bit 33 MHz PCI slots

■   Internal PCI bus: 33 MHz, 64-bit bus supports the Shasta I/O controller, the boot ROM, the AirPort Extreme Card slot, and the USB controller

- Serial ATA (SATA) buses: support 1.5 Gbps internal hard drive connectors

- Ultra DMA ATA/100 bus: supports internal optical drive

- HyperTransport: high-speed, bidirectional, point-to-point link for integrated circuits supports bidirectional data rates up to 1.6 GBps

The remainder of this chapter describes the architecture of the U3L memory controller and bridge IC, the Shasta I/O controller IC, and the USB controller.

PowerPC G5 Processor

The PowerPC G5 used in the single processor Power Mac G5 has the following features:

- 64-bit PowerPC implementation with 42-bit physical memory addressing

- core runs at three times the bus speed

- superscalar execution core supporting more than 200 in-flight instructions

- two independent double-precision floating point units

- Velocity Engine: 128-bit-wide vector execution unit

- 64K L1 instruction cache, 32K L1 data cache per processor

- built-in 512 KB backside L2 cache per processor

- two independent, unidirectional 600 MHz frontside buses supporting 4.8 GBps data throughput

For more information, see the reference at "PowerPC G5 Microprocessor" (page 59).

Bridge and Memory Controller

The U3L custom IC is at the heart of the single processor Power Mac G5. It provides the bridging functionality among the processor, the memory system, HyperTransport bus to the PCI-based I/O system, and the AGP slot.

Processor Bus

The processor bus is a 600 MHz bus connecting the processor to the U3L IC. The bus has 32-bit wide data running in both directions. The processor has 42-bit wide addresses.

The single processor Power Mac G5 system controller is built with 130-nanometer SOI technology. This point-to-point architecture provides each subsystem with dedicated bandwidth to main memory. The U3L I/O implements an independent processor interface. The processor clock rate is either 900 MHz or 1.8 GHz (see "Power Management" (page 15)). The processor clock is derived from a PLL which multiplies the reference clock by preset intervals of 12 times.

Out-of-order completion allows the memory controller to optimize the data bus efficiency by transferring whichever data is ready, rather than having to pass data across the bus in the order the transactions were posted on the bus. This means that a fast DDR SDRAM read can pass a slow PCI read, potentially enabling the processor to do more before it has to wait on the PCI data.

Main Memory Bus

The single processor Power Mac G5 main memory bus connects the main memory to the U3L IC via the 128-bit data bus. The memory modules are 400 MHz (PC3200) DDR SDRAM DIMMs. Maximum system memory is 4 GB.

Standard supported DIMM sizes are 128, 256, 512 MB, and 1 GB. The DIMMs must be unbuffered and installed in pairs of the same size.

For more information about memory DIMMs and installation, see "RAM Expansion" (page 53).

Accelerated Graphics Port Bus

The accelerated graphics port (AGP 8x Pro) bus is a 266/533 MHz, 32-bit bus connecting the AGP card to the U3L IC. DDR data is transmitted at both edges of the clock for peak transfers of 2.1 GBps.

The AGP bus is an enhanced PCI bus with extra functionality to burst texture data and other graphics across the port up to 8 times faster than a 66 MHz PCI port. The single processor Power Mac G5 computer's AGP implementation is compatible with version 3 of the AGP specification and with the Pro version of AGP. AGP 3.0 enables deeply pipelined memory read and write operations and demultiplexing of address and data on the bus.

To further improve the performance of the AGP bus, the U3L IC supports a graphics address remapping table (GART). Because the virtual memory system organizes main memory as randomly distributed 4 KB pages, DMA transactions for more than 4 KB of data must perform scatter-gather operations. To avoid this necessity for AGP transactions, the GART is used by the AGP bridge in the U3L to translate a linear address space for AGP transactions into physical addresses in main memory.

The U3L IC also supports a DMA Address Relocation Table (DART) that provides the same functions for AGP as does the GART, except that the functions are for devices attached to HyperTransport. Most device drivers do not require special knowledge of the DART because IOKit will configure it automatically if the driver uses IOMemoryDescriptors.

For more information on the graphics cards installed in the AGP slot, refer to "Graphics Cards" (page 27).

> **Note:** The AGP bus is 1.5 V only and is not backward compatible. Older AGP cards will not work in the single processor Power Mac G5 computer.

Internal PCI Bus

An internal 33 MHz PCI bus connects the Shasta I/O controller to the boot ROM, the AirPort Extreme Card slot, and the USB controller. The U3L IC used in the single processor Power Mac G5 supports the PCI write combining feature. This feature allows sequential write transactions involving the Memory Write or Memory

Write and Invalidate commands to be combined into a single PCI transaction. For memory write transactions to be combined, they must be sequential, ascending, and non-overlapping PCI addresses. Placing an `eieio` or `sync` command between the write commands prevents any write combining.

PCI Expansion Slots

The single processor Power Mac G5 computer provides three 64-bit 33 MHz PCI slots that interface to the Shasta I/O.

Each slot has room for a full size 12.335-inch or short 6.926-inch card. The slots are numbered from 2 to 4 on back panel, on the PCB, and in the Apple System Profiler. Slot one is the AGP. A total of 90 W is allocated to the three PCI slots and the AGP slot.

> **Note:** The single processor Power Mac G5's build-to-order ATI Radeon 9800 XT, the Nvidia GeForce 6800 GT DDL, and Nvidia GeForce 6800 Ultra DDL graphics cards occupy both the AGP slot and the adjacent PCI slot. As a result, you can install PCI expansion cards in the slots labeled 3 and 4 but not in slot 2. To learn more about installing PCI cards, refer to "Chapter 3, Working Inside Your Computer" in the *Power Mac G5 User's Guide* that came with your computer.

The connectors to the PCI slots are 3.3 V keyed and support 32-bit and 64-bit buses. The connectors include a PME signal which allows a PCI card to wake the computer from sleep.

> **Note:** 5 V keyed or signalling cards do not work in the Power Mac G5 computer.

The slots (12.335 inch) have a capture feature which is at the end of the slot. If a card exceeds the short length it is recommended that the long length be used rather than an intermediate length, to assure the card stays secure if and when the system is in shipment.

For more information, refer to

HyperTransport Technology

The DDR HyperTransport is an advanced chip-to-chip communications technology that provides a high-speed, high-performance, point-to-point link for integrated circuits. HyperTransport provides a universal connection that reduces the number of buses within a system.

The HyperTransport bus between the U3L IC and the Shasta IC is 400 MHz DDR, 8 bits wide in both directions, supporting total of 1.6 GBps bidirectional throughput.

For more information on the HyperTransport technology, go to the World Wide Web at

http://www.hypertransport.org

PCI USB Controller

The single processor Power Mac G5 CPU uses a PCI USB controller ASIC with one Enhanced Host Controller Interface (EHCI) function and two Open Host Controller Interface (OHCI) functions. The controller has a total of five ports available to support the internal AGP slot, three external USB ports, and Bluetooth. If connected to classic-speed USB devices, the two rear panel ports are connected to separate OHCI controllers.

The five USB ports comply with the Universal Serial Bus Specification 2.0. The USB register set complies with the EHCI and OHCI specifications. For more information, see "USB Ports" (page 29).

Shasta I/O Controller

The functions of the Shasta I/O controller are described in the following sections.

DMA Support

The Shasta ICs provide DB-DMA (descriptor-based direct memory access) support for the following I/O channels:

- Ultra ATA/100
- Ethernet interface
- FireWire interface
- I2S channel to the sound subsystem
- Serial ATA

The DB-DMA system provides a scatter-gather process based on memory-resident data structures that describe the data transfers. The DMA engine is enhanced to allow bursting of data files for improved performance.

Wireless LAN Module

The optional AirPort Extreme wireless LAN module connects via the PCI interface.

The AirPort Extreme Card wireless LAN module contains a media access controller (MAC), a digital signal processor (DSP), and a radio-frequency (RF) section. The module has an antenna port on the rear of the computer.

The AirPort Extreme Card is based on the IEEE 802.11g standard. The card transmits and receives data at up to 54 Mbps and is compatible with Apple AirPort systems as well as other 802.11b and 802.11g Wi-Fi certified products. For information about its operation, see "AirPort Extreme Card" (page 38).

The build-to-order Bluetooth connectivity comes off the USB controller. See "Bluetooth Technology" (page 39) for more information.

Modem Slot Support

The Shasta IC supports the modem and provides DB-DMA (descriptor-based direct memory access) support for the modem slot interface. The modem is connected via an I2S interface.

The internal hardware modem is a separate module that contains a modem controller IC, a data pump, and the interface to the telephone line (DAA). For more information about the modem, see "Internal Modem" (page 37).

Boot ROM

The boot ROM consists of 1 MB of on-board flash EEPROM. The boot ROM includes the hardware-specific code and tables needed to start up the computer using Open Firmware, to load an operating system, and to provide common hardware access services.

Ethernet Controller

The Vesta IC provides the Ethernet functions and includes an Ethernet media access controller (MAC) and PHY.

The MAC implements the link layer. It is connected to a PHY via Vesta and provides 10Base-T/UTP, 100Base-TX, or 1000Base-TX operation over a standard twisted-pair interface. The Ethernet port is auto-sensing and self-configuring to allow use of either a cross-over or straight-through cable. The operating speed of the link is automatically negotiated by the PHY and the bridge, router, hub, switch, or other Mac or PC to which the Ethernet port is connected.

For information about the Ethernet port, see "Ethernet Port" (page 34).

FireWire Controllers

The Shasta IC provides the FireWire functions and includes a FireWire controller that supports both IEEE 1394b (FireWire 800) with a maximum data rate of 800 Mbps (100 MBps) and IEEE 1394a (FireWire 400) with a maximum data rate of 400 Mbps (50 MBps). The IC is backwards-compatible with 1394a (FireWire 400). The Shasta IC provides DMA (direct memory access) support for the FireWire interface.

The controller in the Shasta IC implements the FireWire link layer. Vesta is a PHY that implements the electrical signaling protocol of the FireWire interface and provides the electrical signals to the ports.

For more information, see "FireWire Ports" (page 30).

Interrupt Support

The interrupt controller for the single processor Power Mac G5 system is an MPIC cell in the Shasta IC. In addition to accepting internal interrupt sources from the I/O, the MPIC controller accepts internal interrupts from U3L and dedicated interrupt pins.

Serial ATA Interface

Based on the Serial ATA 1.0 specification, Serial ATA (SATA) is a disk-interface technology that delivers up to 1.5 Gbps of performance to each independent drive bus on the single processor Power Mac G5. It provides a scalable, point-to-point connection that allows multiple ports to be aggregated into a single controller. Serial ATA uses a thin, point-to-point cable connection that enables easy routing within a system, avoiding master/slave, daisy-chaining, and termination issues and enabling better airflow within a system.

The Shasta IC implements two Serial ATA revision one ports, each of which accommodates one independent internal hard drive.

For information about the drive bays, see "Hard Disk Drives" (page 36).

Ultra DMA ATA/100 Interface

In the single processor Power Mac G5 computer, the Shasta IC provides an Ultra DMA ATA/100 interface to support the optical drive. For information about specific drives, see "Disk Drives" (page 36).

Sound System Overview

The sound circuitry is connected to the Shasta IC by two standard I2S (inter-IC sound) buses. The Shasta IC provides DB-DMA (descriptor-based direct memory access) support for the I2S port.

The single processor Power Mac G5 circuitry consists of two main components: an analog audio CODEC IC (CODEC) and a S/PDIF receiver IC. The analog audio CODEC IC includes an A/D converter, a D/A converter, and a S/PDIF (Sony/Phillips Digital Interface) digital audio transmitter. The S/PDIF receiver IC includes an S/PDIF digital audio receiver, and a phase-locked loop (PLL).

The CODEC and the S/PDIF receiver connect to Shasta with separate I2S busses (I2Sa and I2Sc). This allows the system to record from the S/PDIF digital audio input while simultaneously recording audio from the analog line input and playing audio over the audio outputs.

Stereo signals from the audio input jack are routed to an analog line input buffer that drives the internal A/D converter in the CODEC. The output of the A/D converter is routed to the Shasta IC over the I2Sa bus.

Digital audio data from the Shasta IC is routed to the D/A converter on the analog CODEC over the I2Sa bus. The analog audio output from the D/A is routed to separate amplifiers that drive the line output, headphone output, and internal speaker.

Digital audio data from the S/PDIF receiver is routed to the Shasta IC over the I2Sc bus. The S/PDIF input always operates in a synchronous mode. The PLL in the digital audio CODEC locks on to the incoming S/PDIF data rate and drives the audio system clocks associated with the I2Sc bus in the single processor Power Mac G5, allowing bit-accurate data processing of the S/PDIF input data. There are two clock modes for the CODEC on the I2Sa bus: internal clock mode and external clock mode. In internal clock mode, the single processor Power Mac G5 is the audio clock master. In this mode, the output sample rate and A/D converter sample rate are controlled by system software; sample rates are selected with the Audio MIDI Setup utility. When in internal clock mode, the S/PDIF input sample rate is asynchronous to the output sample rate of the CODEC. During recording of the S/PDIF input, the S/PDIF input data is processed in a bit-accurate manner. For playback of S/PDIF input data in internal clock mode, audio data from the S/PDIF receiver is converted to the selected output sample rate by a Sample Rate Converter (SRC) implemented in system software. The SRC algorithm will introduce minor audio artifacts in the playthrough of the S/PDIF input stream.In external clock mode,

the output sample rate and the A/D sample rate of the Power Mac G5 rate are locked to the sample rate of the S/PDIF input stream. In this mode, the input and output sample rates are synchronous and no SRC is required during playthrough of the S/PDIF input stream.The speaker output is mutually exclusive from the line out, headphones out and S/PDIF outputs. If the speaker output is selected, the line output, the headphones output, and the S/PDIF output are disabled. If any of the line output, the headphones output, or the S/PDIF output are selected, the speaker output is disabled.

For more detail on the audio, see "Audio" (page 43).

Graphics Cards

The single processor Power Mac G5 comes with an Nvidia GeForce FX 5200 Ultra graphics card installed. The supported graphics cards are shown below:

Graphics IC	Video SDRAM	Connectors
Nvidia GeForce FX 5200 Ultra	64 MB DDR	ADC and DVI
ATI Radeon 9600 XT (build-to-order)	128 MB DDR	ADC and DVI
ATI Radeon 9800 XT (build-to-order)	256 MB DDR	ADC and DVI
Nvidia GeForce 6800 GT DDL (build-to-order)	256 MB DDR	two dual link DVIs
Nvidia GeForce 6800 Ultra DDL (build-to-order)	256 MB DDR	two dual link DVIs

The build-to-order ATI Radeon 9800 XT, Nvidia GeForce 6800 GT DDL, and Nvidia GeForce 6800 Ultra DDL cards occupy both the AGP slot and the adjacent PCI slot. For information on PCI expansion, refer to "PCI Expansion Slots" (page 56).

All graphics cards support dual displays in either extended desktop or video mirroring mode, and support digital resolutions up to 1920x1200 pixels and analog resolutions up to 1600x1200 pixels. The Nvidia GeForce 6800 graphics cards support resolutions up to 2560 by 1600 pixels.

The display memory on the AGP card is separate from the main memory. The display memory consists of 64 MB (or 128 MB and 256 MB build-to-order) of DDR devices configured to make a 128-bit data bus. The display memory cannot be expanded by the user.

For more information about the features of the graphics cards and the monitors they support, see "Video Monitor Ports" (page 47).

System Management Unit

The single processor Power Mac G5 uses an advanced system management unit (SMU) to manage the thermal and wattage conditions, while keeping the acoustic noise to a minimum.

> **Note:** There is a separate real time clock that has 56 bytes of battery backed up RAM.

The single processor Power Mac G5 system employs advanced thermal and wattage management to keep acoustic noise to a minimum. The enclosure is divided into discrete zones, each with independently controlled fans bringing in cool air from the front of the enclosure, directing it over system components and exhausting it out the rear. Temperature and power consumption are monitored by the operating system which communicates with the SMU, which in turn controls and monitors fan operation. Note that if Mac OS X is not booted, thermal management must be provided by the alternate development operating system.

The SMU monitors the wattage from zero to 100 W. The fan speeds are increased proportionally, where 100 W operates the fans at highest speed. The wattage calculation does not include the 25 V ADC power output.

The SMU controls the fans in the single processor Power Mac G5 and regulates the speeds to run each fan. The SMU derives fan speed from sensors in each thermal zone. Some of these sensors are thermal while others monitor power/wattage used in a particular area. It is important to note that there are fixed limits to fan speed in those areas where power sensors are used. At the upper limits, a fan could be fixed at a particular speed, based on power consumption, even though that zone continues to generate more heat.

If the SMU does not receive an update from the operating system within two minutes, it begins to ramp up the speed of the fans to full speed.

Input and Output Devices

This chapter describes the single processor Power Mac G5 computer's built-in I/O devices and the ports for connecting external I/O devices. Each of the following sections describes an I/O port or device.

USB Ports

The single processor Power Mac G5 has two external Universal Serial Bus (USB) 2.0 ports on the back and one on the front of the enclosure. Internal connections support the AGP slot for the Apple display and the Bluetooth module. The external USB ports are off of the USB controller connected to the PCI bus, bridged by Shasta. In addition, there are two USB 1.1 ports on the keyboard. The USB ports are used for connecting the keyboard and mouse as well as additional I/O devices such as printers, scanners, and storage devices.

All USB ports are fully compliant with the USB 2.0 specification, including support for high-speed (480 Mbps) devices using an Enhanced Host Controller Interface (EHCI). Ports are automatically routed to a companion OHCI controller when a classic-speed (full-speed or low-speed) USB device is attached to a root hub port.

Each USB rear port is connected to a separate USB root hub in classic speeds, allowing the USB ports to support 12 Mbps devices at the same time with no degradation of their performance. The USB root hubs are also connected to the internal USB modem and to the USB port on the ADC monitor connector.

The three external USB ports and the port supporting the Apple display comply with the Universal Serial Bus Specification 2.0. The two ports on the keyboard comply with the Universal Serial Bus Specification 1.1 Final Draft Revision. For low-speed and full-speed devices, the USB register set complies with the Open Host Controller Interface (OHCI) specification. For high-speed devices, the USB register set complies with the Enhanced Host Controller Interface (EHCI) specification.

For more information about USB on Macintosh computers, please refer to Apple Computer's *Accessing Hardware from Applications* and the other sources listed in .

USB Connectors

The USB ports use USB Type A connectors, which have four pins each. Two of the pins are used for power and two for data. shows the connector and shows the signals and pin assignments.

Figure 3-1   USB connector
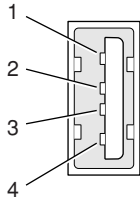
Table 3-1   Signals on the USB connector

Pin	Signal name	Description
1	VCC	+5 VDC
2	D–	Data –
3	D+	Data +
4	GND	Ground

The single processor Power Mac G5 provides 5 V power for the USB ports and up to 500 mA on each port. The power is provided in both run and sleep mode. The ports share the same power supply; a short circuit on one disables all ports until the short has been removed.

The USB ports support all USB 2.0 speeds: high-speed (480 Mbps) and classic USB speeds of full-speed (12 Mbps) and low-speed (1.5 Mbps). High-speed operation requires the use of shielded cables.

The Macintosh system software supports all four data transfer types defined in the USB specification, as well as split transactions for classic-speed devices attached to high-speed hubs.

Waking Up From Sleep

As defined in the USB-suspend mode of the USB specification, USB devices can provide a remote wakeup function for the computer (resume signalling). The USB root hub in the computer is set to support remote wakeup whenever a device is attached to the bus. The device wakes the computer by sending a RESUME event to the USB root hub. The mouse and keyboard that come with the computer use this method to wake the computer on a key press or mouse click.

FireWire Ports

The single processor Power Mac G5 has one FireWire 800 port based on IEEE 1394b and two FireWire 400 IEEE 1394a ports. Each FireWire port

- supports booting the system from a mass storage device
- supports target disk mode
- provides bus repeating capability as long as the computer is connected to AC power

■   provides power for FireWire devices both in run and sleep mode (useful for charging the iPod)

The three FireWire ports share a single power supply that can provide up to 15 watts total. The three ports are all on the same FireWire bus and can connect to up 62 other FireWire devices.

The FireWire hardware and software provided with the single processor Power Mac G5 are capable of all asynchronous and isochronous transfers defined by IEEE standards 1394a and 1394b.

> **Note:** FireWire cables are not included with this product and must be purchased independently, if required.

For more information about FireWire on Macintosh computers, please refer to the Apple FireWire website and the other sources listed in "FireWire Interface" (page 62).

FireWire 800 Connector

The FireWire 800 port on the single processor Power Mac G5 is based on IEEE 1394b and enables a 800 Mbps transfer rate. FireWire 800 uses a 9-pin connector and is backwards compatible with original 1394a (FireWire 400) devices with 6-pin or 4-pin connectors. With the appropriate cable, the 9-pin port works seamlessly with legacy FireWire devices. Cables are available to go from both 6-pin and 4-pin connectors to a 9-pin, and 9-pin to 9-pin.

The 9-pin FireWire 800 connector is shown in Figure 3-2 (page 31). Its connector signals and pin assignments are shown in Table 3-2 (page 31).

Figure 3-2   9-pin FireWire 800 connector
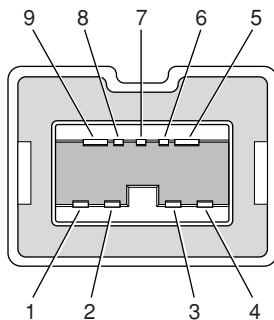
Table 3-2   Signals on the 9-pin FireWire 800 connector

Pin	Signal name	Description
1	TPB–	Twisted-pair B Minus
2	TPB+	Twisted-pair B Plus
3	TPA–	Twisted-pair A Minus
4	TPA+	Twisted-pair A Plus
5	TPA (R)	Twisted-pair A Ground Reference

Pin	Signal name	Description
6	VG	Power Ground
7	SC	Status Contact (no connection; reserved)
8	VP	Power Voltage (approximately 25 V DC)
9	TPB (R)	Twisted-pair B Ground Reference

VP (pin 8) provides up to 15 W power, shared with the other FireWire connectors. The voltage on the power pin is approximately 25 V.

The 9-pin FireWire port is capable of operating at 100, 200, 400, and 800 Mbps, depending on the device it is connected to. Using a cable with a 9-pin connector at one end and a 4-pin or 6-pin connector at the other, the 9-pin port is capable of directly connecting to all existing FireWire devices. Using a cable with 9-pin connectors at both ends, the 9-pin port is capable of operating at 800 Mbps.

The IEEE 1394b standard defines long-haul media using Cat 5 UTP and several kinds of optical fiber. The single processor Power Mac G5 is interoperable with such cables but cannot be directly connected to them. To use long-haul cables, connect the computer to a 1394b hub that has the desired kind of long-haul connectors. If the hub has a bilingual port, that port can be connected to any of the computer's FireWire ports. If the hub has a beta-only port, it can be connected only to the computer's 9-pin port.

FireWire 400 Connector

The FireWire 400 ports 1 and 2 use the six-pin connectors shown in Figure 3-3 (page 32). The connector signals and pin assignments are shown in Table 3-3 (page 32).
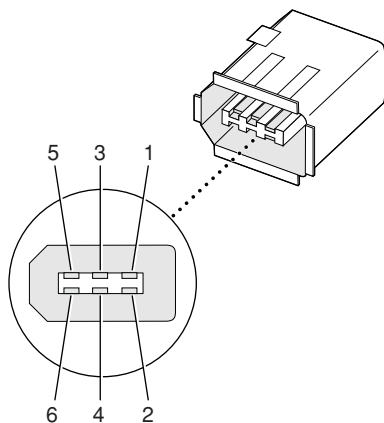
Figure 3-3 6-pin FireWire 400 connector

Table 3-3 Signals on the 6-pin FireWire 400 connector

Pin	Signal name	Description
1	Power	Power (approximately 25 V DC)

Pin	Signal name	Description
2	Ground	Ground return for power and inner cable shield
3	TPB–	Twisted-pair B Minus
4	TPB+	Twisted-pair B Plus
5	TPA–	Twisted-pair A Minus
6	TPA+	Twisted-pair A Plus
Shell	—	Outer cable shield

The power pin provides up to 15 W total power, shared with the other FireWire connectors. The voltage on the power pin is approximately 25 V.

Pin 2 of the 6-pin FireWire connector is ground return for both power and the inner cable shield. In a FireWire cable with a 4-pin connector on the other end, the wire from pin 2 is connected to the shell of the 4-pin connector.

The signal pairs are crossed in the cable itself so that pins 5 and 6 at one end of the cable connect with pins 3 and 4 at the other end. When transmitting, pins 3 and 4 carry data and pins 5 and 6 carry clock; when receiving, the reverse is true.

FireWire Device Programming

Mac OS X includes general support for the FireWire bus and specific support for various kinds of FireWire devices and protocols. Developers can use the built-in support or provide additional applications and drivers for use with their products.

The general FireWire services will configure the FireWire bus, scan the bus for new devices, and allow multiple drivers and devices to share a single FireWire interface cooperatively. The general services also publish information about the bus and the devices in the IO Registry, so that IO Kit can match protocols and drivers to each connected FireWire device.

The specific device and protocol support in Mac OS X as provided with the single processor Power Mac G5 includes the following:

- General services for Serial Bus Protocol 2 (SBP-2) and support for most mass storage devices using SBP-2, such as hard disk drives, optical drives, flash card readers, Target Disk Mode (see "Target Disk Mode" (page 34), and the iPod. Mac OS X can boot from most of these devices.

- General services for the Audio Video Control (AV/C) protocol and support for most digital video (DV) cameras and decks using this protocol, including video capture through standard QuickTime APIs.

- A QuickTime device driver for IIDC/DCAM type cameras such as the iSight.

- A network device driver supporting IP (Internet Protocol) over FireWire according to IEEE RFC 2734.

- Additional services for user-space and kernel access to all FireWire resources.

For information on writing FireWire drivers or applications, download the latest FireWire SDK from http://developer.apple.com/sdk/

Booting from a FireWire Device

The single processor Power Mac G5 can boot from a FireWire storage device that implements SBP-2 (Serial Bus Protocol) with the RBC (reduced block commands) command set.

For additional information about the FireWire interface and the Apple API for FireWire device control, see the references shown in "FireWire Interface" (page 62).

Target Disk Mode

The user has the option at boot time to put the computer into a mode of operation called Target Disk Mode (TDM). When the single processor Power Mac G5 is in Target Disk Mode and connected to another Macintosh computer by a FireWire cable, the single processor Power Mac G5 operates like a FireWire mass storage device with the SBP-2 (Serial Bus Protocol) standard. Target Disk Mode has two primary uses:

■   high-speed data transfer between computers

■   diagnosis and repair of a corrupted internal hard drive

The single processor Power Mac G5 can operate in Target Disk Mode as long as the other computer has a 1394a or 1394b FireWire port and either any version of Mac OS X or Mac OS 9 with FireWire software version 2.3.3 or later.

To put the single processor Power Mac G5 into Target Disk Mode, restart the computer and hold down the T key until the FireWire icon appears on the display. Then connect a FireWire cable from the single processor Power Mac G5 to the other computer. When the other computer completes the FireWire connection, a hard disk icon appears on its desktop.

If you disconnect the FireWire cable or turn off the single processor Power Mac G5 while in Target Disk Mode, an alert appears on the other computer.

To take the single processor Power Mac G5 out of Target Disk Mode, drag the hard disk icon on the other computer to the trash, then press the power button on the single processor Power Mac G5 until the computer powers down.

Ethernet Port

The single processor Power Mac G5 has a built-in Ethernet port that supports 10/100/1000 Mbps transfer rates. In operation, the actual speed of the link is auto-negotiated between the Ethernet PHY device that is internal to the Vesta IC and the bridge, router, hub, switch, or other Mac or PC to which it is connected. The Ethernet port is auto-sensing and self-configuring to allow connection via either a cross-over or straight-through cable.

Both CAT 5 unshielded twisted pair (UTP) and shielded twisted pair (STP) cables work with the Ethernet port. An STP cable is recommended for noisy environments or run of greater than 100 meters.

> **Note:** When connecting a single processor Power Mac G5 directly to another computer without using an Ethernet hub, a crossover cable is not required; circuits in the PHY detect the type of connection and switch the signal configuration as required.

The connector for the Ethernet port is an RJ-45 connector on the back of the computer. Table 3-4 (page 35) shows the signals and pin assignments for 10Base-T/UTP and 100Base-TX operation. Table 3-5 (page 35) shows the signals and pin assignments for 1000Base-TX operation.

Table 3-4   Signals for 10Base-T/UTP and 100Base-TX operation

Pin	Signal name	Signal definition
1	TXP	Transmit (positive lead)
2	TXN	Transmit (negative lead)
3	RXP	Receive (positive lead)
4	–	Not used
5	–	Not used
6	RXN	Receive (negative lead)
7	–	Not used
8	–	Not used

Table 3-5   Signals for 1000Base-TX Gigabit operation

Pin	Signal name	Signal definition
1	TRD+(0)	Transmit and receive data 0 (positive lead)
2	TRD–(0)	Transmit and receive data 0 (negative lead)
3	TRD+(1)	Transmit and receive data 1 (positive lead)
4	TRD+(2)	Transmit and receive data 2 (positive lead)
5	TRD–(2)	Transmit and receive data 2 (negative lead)
6	TRD–(1)	Transmit and receive data 1 (negative lead)
7	TRD+(3)	Transmit and receive data 3 (positive lead)
8	TRD–(3)	Transmit and receive data 3 (negative lead)

To interconnect two computers for 1000Base-TX operation, you must use 4-pair cable (Category 5 or 6).

The Ethernet interface in the single processor Power Mac G5 conforms to the ISO/IEC 802.3 specification, where applicable, and complies with IEEE specifications 802.3i (10Base-T/UTP), 802.3u-1995 (100Base-TX), and 802.3ab (1000Base-TX).

Disk Drives

The single processor Power Mac G5 computer has one 5.25 inch bay for optical drive access through the front panel and two 3.5 inch bays for internal hard disk drives. The optical drive is connected to the Ultra ATA/100 bus; the factory installed drive runs at 33 MHz. The standard configuration of the single processor Power Mac G5 provides a SuperDrive; a build-to-order Combo drive is available.

Hard Disk Drives

The enclosure has one drive carrier with two 3.5 inch bays for fixed-media mass storage devices. The drive carrier has data and power connectors for both drives. The two drives on the independent Serial ATA buses implement revision one ports. The single processor Power Mac G5 comes standard with an 80GB SATA hard drive, and 160GB and 250GB SATA drives are available as build-to-orderoptions. For references to SATA website information, refer to "Serial ATA" (page 61).

SCSI Drive

SCSI drives and SCSI PCI controller cards are available from third party providers. The single processor Power Mac G5 computer supports external SCSI drives only.

SuperDrive

The single processor Power Mac G5 has a tray-loading SuperDrive (combination DVD-R and CD-RW drive). The SuperDrive can read and write DVD media and CD media, as shown in Table 3-6 (page 36). The DVD-R/CD-RW drive also provides DVD-Video playback.

Table 3-6   Media read and written by the SuperDrive

Media type	Reading speed (maximum)	Writing speed
DVD-R	8x (CAV max)	8X ZONE CLV, 4x/2x/1x (CLV) depending on media type
DVD-RW	8x (CAV max)	4x/2x/1x (CLV) depending on media type
DVD-ROM	10x (CAV max)	_
CD-R	32x (CAV max)	24x ZONE CLV
CD-RW	24x (CAV max)	10x (CLV) high speed CD-RW disc
CD or CD-ROM	32x (CAV max)	–

The Apple SuperDrive writes to DVD-R 4.7 gigabyte General Use media. These discs are playable in most standard DVD players and computer DVD-ROM drives.

For compatibility information regarding recordable DVD formats, refer to

http://dvddemystified.com/dvdfaq.html#4.3

Digital audio signals from the SuperDrive can be played through the sound outputs under the control of the Sound Manager.

Combo Drive

As a build-to-order option, the single processor Power Mac G5 can have a tray-loading combination DVD-ROM and CD-RW drive.

The Combo drive can read DVD media and read and write CD media, as shown in Table 3-7 (page 37). The DVD-ROM/CD-RW drive also provides DVD-Video playback.

Table 3-7   Media read and written by the Combo drive

Media type	Reading speed	Writing speed
DVD-R	6x (CAV max)	–
DVD-RW	6x (CAV max)	-
DVD-ROM	12x DVD5 (CAV max) 8x DVD9 (CAV max)	–
CD-R	32x (CAV max)	32x (ZCLV)
CD-RW	32x (CAV max)	10x (CLV, for high speed media)
CD or CD-ROM	32x (CAV max)	–

Digital audio signals from the Combo drive can be played through the sound outputs under the control of the Sound Manager.

Internal Modem

The single processor Power Mac G5 has an internal modem module. The external I/O connector for the modem is an RJ-11 connector installed on the rear panel of the computer. The modem has the following features:

■   modem bit rates up to 56 Kbps, supporting V.90 and V.92 modem standards

■   fax modem bit rates up to 14.4 Kbps

The modem appears to the system as a I2S device that responds to standard AT commands. The modem provides a sound output for monitoring the progress of the modem connection.

AirPort Extreme Card

The single processor Power Mac G5 supports the optional AirPort Extreme Card, an internal wireless LAN module. The AirPort Extreme Card is available as a build-to-order option or as a user-installable upgrade through the Apple Store.

By communicating wirelessly with a base station, the AirPort Extreme Card can be used for Internet access, email access, and file exchange. A base station provides the connection to the Internet or the bridge between the wireless signals and a wired LAN or both. The AirPort Extreme Base Station has connectors for a wired LAN, a DSL or cable modem, and a standard telephone line using the built-in 56 Kbps modem that is available on some base stations.

An external AirPort Extreme antenna is provided and must be installed on the AirPort Extreme antenna port on the rear of the enclosure.

Complying with the IEEE 802.11 g standard, AirPort Extreme transmits and receives data at rates up to 54 Mbps. Airport Extreme is also compatible with other devices that follow the IEEE 802.11 b standard, including PC's. For more information about Wi-Fi and compatibility, see the reference at "Wireless Networks" (page 63).

> **Note:** As is the case with the existing IEEE 802.11 b standard, actual data throughput will be lower than the indicated maximum connection speeds.

Data Security

AirPort Extreme has several features designed to maintain the security of the user's data:

■ The system uses direct-sequence spread-spectrum (DSSS) technology that uses a multibit spreading code that effectively scrambles the data for any receiver that lacks the corresponding code.

■ The system can use an Access Control List of authentic network client ID values or MAC addresses (Ethernet or AirPort IDs) to verify each client's identity before granting access to the network.

■ When communicating with a base station, AirPort Extreme uses up to 128-bit encryption to encode data while it is in transit.

■ The AirPort Extreme Base Station can be configured to use NAT (Network Address Translation), protecting data from would-be Internet hackers.

■ The AirPort Extreme Base Station can authenticate users by their unique MAC addresses (AirPort IDs), preventing unauthorized computers from logging into a network. Network administrators can take advantage of RADIUS compatibility, used for authenticating users over a remote server.

As an additional data security measure, VPN can be used in conjunction with the AirPort Extreme data security.

AirPort Extreme Hardware

The AirPort Extreme Card is a wireless LAN module compliant with the IEEE 802.11g standard using both OFDM (orthogonal frequency-division multiplexing) and DSSS technologies. Using DSSS, AirPort Extreme is interoperable with other Wi-Fi certified, PC-compatible wireless LANs that conform to the 802.11b standard at speeds of 11 Mbps, 5.5 Mbps, 2 Mbps, and 1 Mbps. Using OFDM, AirPort Extreme is compatible with all 802.11g standard speeds.

The AirPort Extreme wireless interface is on the PCI bus.

AirPort Extreme Software

Software that is provided with the AirPort Extreme Card includes

- AirPort Setup Assistant (located in the Applications/Utilities folder), is an easy-to-use program that guides the user through the steps necessary to set up the AirPort Extreme Card or set up an AirPort Extreme Base Station or an AirPort Express Base Station.

- Users can switch between wireless networks and can create and join peer-to-peer networks. These functions are accessed via the AirPort - Menu-Extra pulldown, which is enabled by a checkbox in System Preferences.

- AirPort Admin Utility (located in the Applications/Utilities folder), is a utility for advanced users and system administrators. With it the user can edit the administrative and advanced settings needed for some advanced configurations.

Bluetooth Technology

Available as a fully-integrated, build-to-order option, Bluetooth is an open specification that enables short-range wireless connections between desktop and laptop computers and a host of other peripheral devices. Bluetooth support is built into Mac OS X and compliant with Bluetooth specification v1.1. It operates on a globally available 2.4 GHz frequency band (ISM band) for worldwide compatibility and has a maximum throughput of 1Mbps.

The Bluetooth technology supports the following profiles:

- synchronization —enables synchronization of devices over Bluetooth

- serial —provides a wireless serial connection to other Bluetooth devices

- dial-up networking (DUN) — enables a mobile phone to act as a modem

- object push —enables the transfer of files between Bluetooth devices

- human interface device (HID) — enables the use of Bluetooth input devices (keyboards and mice)

- Bluetooth file transfer profile (FTP) — enables browsing of the file system of other Bluetooth devices which support Bluetooth FTP.

- hardcopy cable replacement profile (HCRP) — describes how to send rendered data over a Bluetooth link to a device, such as a printer. Although other profiles can be used for printing, the HCRP is specially designed to support hardcopy applications. Note: currently, to use this profile, Apple's Bluetooth Update Version 1.5 must be installed.

- headset profile (HSP) — enables the use of Bluetooth-enabled wireless headsets for applications such as iChat. This profile does not enable use of Apple Speech Recognition due to the fact that it is a low-precision audio channel. Note: currently, to use this profile, Apple's Bluetooth Update Version 1.5 as well as Firmware Update Version 1.1 must be installed.

- synchronous, connection-oriented (SCO) — enables isochronous and voice communication, including most Bluetooth headsets.

Bluetooth is available as a build-to-order option, which is installed by Apple at the time of purchase as a fully integrated module. Accessing the Bluetooth capabilities without purchasing the integrated module will require a third-party dongle.

When the Bluetooth option is included on the factory order, the Bluetooth antenna is provided in the accessory kit and must be installed on the Bluetooth antenna port on the rear of the enclosure.

For more information on Bluetooth technology, refer to "Bluetooth" (page 63).

Keyboard

The single processor Power Mac G5 computer comes with an Apple Keyboard. It is a full-size keyboard with function keys and separate numeric keypad and editing sections.

The keyboard has an attached 1-meter cable and comes with a 1-meter extender cable for installations where the computer is located on the floor or away from the immediate desktop area.

Keyboard Features

Here is a list of the features of the Apple Keyboard.

- sloped design
- 109 keys (on the ANSI versions)
- 16 function keys
- 6 editing keys (Page Up, Page Down, Home, End, Forward Delete, and Help)
- USB HID Consumer Page Usage multimedia control keys
- full travel, standard pitch keys on alphanumeric, editing, and keypad sections, including function keys and cursor-position keys
- localized worldwide: 33 versions, standard layouts (including: ANSI, JIS, ISO)
- LED indicator on the Num Lock key
- USB hub functionality with two USB 1.1 sockets

> **Note:** There is no power key on this keyboard.

Keyboard Layout

There are localized versions of the Apple Keyboard for use in different parts of the world. The three standards used are ANSI (US and North America), JIS (Japan), and ISO (Europe).

Applications can determine which keyboard is connected by calling the Gestalt Manager and checking for the corresponding value of the `gestaltKeyboardType` selector.

Figure 3-4 (page 41) shows the keyboard layout for the ANSI keyboard.

Figure 3-4   ANSI keyboard layout
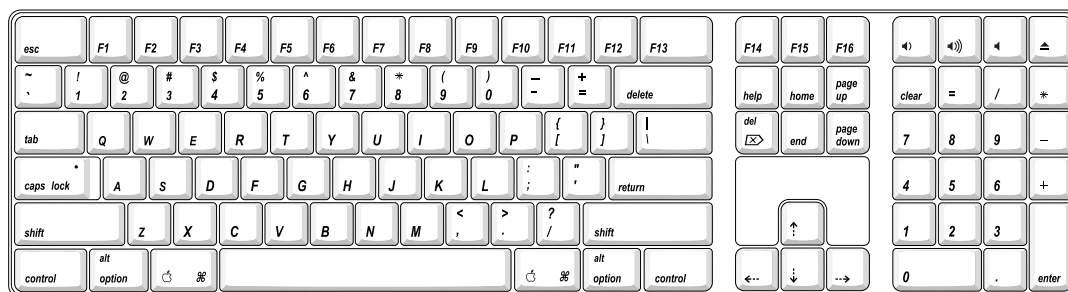
MultiMedia Control Keys

The keyboard has six multimedia keys: Volume Up, Volume Down, Mute, Brightness Up (F15), Brightness Down (F14), and Eject. Theses keys provide direct control of the features on the computer by way of the USB.

Keyboard and USB

The Apple Keyboard is designed to work with the computer by way of the USB ports. The keyboard has a captive cable with a USB Type A connector. The keyboard is a bus-powered USB hub with two USB Type A ports.

> ⚠️ **Warning:** A bus-powered hub as defined in the USB specification does not provide enough power to support a second bus-powered hub. A second bus-powered hub must be connected to a USB port on the computer or other self-powered hub, not to a port on the keyboard.

Apple provides a HID class driver for the Apple Keyboard, which supports the USB boot protocol. Other keyboards intended for use on the Macintosh platform must support the HID boot protocol, as defined in the USB Device Class Definition for Human Interface Devices (HIDs).

Programmer's Switches

The list below provides helpful keyboard shortcuts to the Finder menu.

- Control-Command-Eject: restart
- Control-Command-Option-Eject: shut down
- Control-Eject: display the dialog for shutdown, restart, and sleep
- Option-Command-Eject: put system to sleep

The key combinations are decoded in software and may not be available under some system freeze conditions.

NMI without Programmer's Switch

Current Macintosh computers do not have a programmer's switch, which allowed users to generate a non-maskable interrupt (NMI). The paragraphs below describe how to generate an NMI from a remote session for systems that do not have a physical programmer's switch.

Starting with Mac OS X 10.1.2, the OS will promote and recognize the `DB_NMI` bit in the debug parameter of the "boot-args" property from Open Firmware. When the `DB_NMI` bit is set, the user can generate a non-maskable interrupt (NMI) by pressing the system's power button. This replaces the power button's sleep or wake response. The system reads the state of the bit at boot time from the `boot-args` configuration variable. For more information about the debug flags, please see *Inside Mac OS X: Kernel Programming*.

To set the `DB_NMI` bit, enter the following command at the Terminal to display the current debug flag settings.

```
% nvram boot-args
```

Add the parameter `debug=0x4`, as follows.

```
% sudo nvram boot-args="<current settings> debug=0x4"
```

After the `DB_NMI` bit in the debug parameter is set, reboot the machine. After reboot, pressing the power button for approximately 3 seconds will generate an NMI.

Note: If the power button is pressed for more than five seconds, the system will immediately power off.

The power button will retain this functionality until Mac OS X is restarted without the `DB_NMI` bit set. To clear this bit, issue the `nvram` command omitting `debug=0x4` parameter, as follows, then restart the machine.

```
% sudo nvram boot-args=""
```

> **Note:** The debug flags bit will be cleared if you use System Preferences to change the startup disk. It may also be cleared if you perform an installation that requires a restart.

Mouse

The single processor Power Mac G5 computer comes with an Apple Mouse. The mouse case is made of polycarbonate plastic.

The Apple Mouse uses optical tracking in place of the traditional rolling ball. It works on almost any surface, though nonreflective, opaque surfaces without repetitive patterns work best.

Audio

The single processor Power Mac G5 supports a sound system with both digital and analog audio. The new digital capability features Sony/Phillips Digital Interface (S/PDIF) input and output using optical connectors. S/PDIF technology results in a clean audio signal with no added noise to or from an external audio device.

Under the control of the system software, the sound circuitry digitally creates and records sounds. The single processor Power Mac G5 can receive input only from either the analog input or the digital input. The system can simultaneously stream non-encoded output to digital and analog devices. Encoded audio, such as AC3, can be streamed only to digital audio output. The system can simultaneously record digital and analog input.

By default, when components are plugged into the headphone jack or the rear line-out, the sound system mutes the internal speaker.

The headphones, rear line-out jack, and S/PDIF output are only muted when selected in the System Preferences. Muting and sound options are set in System Preferences:Sound:Output. Selecting the "Digital Output" checkbox allows operation of the analog and digital output ports when headphones are attached, enabling monitoring of the optical digital transport.

The analog and digital sound output circuitries are not independent. Different audio streams cannot be played to the analog and digital circuitry. The selection of digital or analog output is performed through Sound pane in System Preferences.

The sound circuitry and audio device drivers handle audio data in multiple formats. Both digital and analog sound circuitry handle audio input and output data at sample rates of 32.0 kHz, 44.1 kHz, 48.0 kHz, 64.0 kHz, 88.0 kHz, and 96.0 kHz at sample depths of 16 bits and 24 bits.

If audio data sampled from another computer at a lower rate is played as output on the single processor Power Mac G5, the Core Audio (Mac OS X's OS Level Audio API) transparently up-samples the data to the currently set sampling frequency prior to sending the audio data to the sound circuitry. To maximize audio fidelity, the Core Audio samples are stored as 32-bit floating point. The Sound Manager exists as a Carbon compatibility layer, but developers are encouraged to move their applications to Core Audio for maximum performance and fidelity, because the Sound Manager is capable of representing samples only as 16-bit values.

For more information about audio API's on Mac OS X, visit the Apple audio technologies developer web page at

http://developer.apple.com/audio/

Optical S/PDIF Audio

Digital data is transmitted to and from the digital audio I/O using optical cables. The physical connectors, commonly referred to as TOSLink, are for both input and output and conform to IEC60874-17. The TOSLink friction-lock type F-05 connectors are available from pro-audio, musician's supply, hi-fi and other retailers. The 7.5 mm digital optical TOSLink input and output connectors, shown in Figure 3-5 (page 44), are located on the back of the enclosure.

Figure 3-5   Digital optical S/PDIF connector
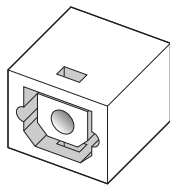
The digital I/O circuitry performs input clock recovery on an incoming data stream. To enable bit-accurate copies, select the external clock in the "Clock Selector" control in the Audio MIDI Setup.

Audio signals from the audio input jack are converted to digital data internally. All audio is handled digitally inside the computer, including audio data from the CD or DVD drive and from devices connected to the USB and FireWire ports. Audio data is converted to analog form for output to the internal speaker, the headphones, line output jacks, or external speakers.

For details on the S/PDIF digital input and output electrical specifications, refer to "Digital Optical Input/Output Specifications" (page 45).

The single processor Power Mac G5 also has the ability to lock its internal audio hardware to the incoming audio stream to synchronize the audio subsystem to an external device supplying the audio stream. This function allows audio and video to play in sync with the external audio or video device. The data format for signals transmitted over the optical cable is S/PDIF protocol IEC 60958-3.

Since no hardware sample rate converter is available to the single processor Power Mac G5, core audio services provide the sample rate conversion.

External Clocking

When the single processor Power Mac G5 is set to "External" clocking, the computer audio circuitry will track and follow the outgoing digital sampling rate and lock the internal audio hardware to the sampling rate of the external device. The external clock must be stable enough to be locked onto, otherwise the digital circuit will signal an error and the driver will fall back to using the internal clock. The digital input always follows the external clock.

Internal Clocking

When the single processor Power Mac G5 is set for "Internal" clocking, the computer audio circuitry will run using the computer's internal clock.

Digital Optical Input/Output Specifications

Based on playback of a 1 kHz, -1dBFS 24-bit sine wave playback, 24-bit 44.1 kHz output sample rate (unless otherwise specified below) the digital sound input and output have the following electrical characteristics (nominal specifications):

■ Fsi – input sample rates: 32 kHz – 96 kHz

■ bits per sample: 16-bit or 24-bit

■ SNR: >130 dB

■ THD+N (external clock mode): < -130 dB (0.00003%)

Audio Line-in

The single processor Power Mac G5 has a stereo audio line-in jack on the back panel. The audio inputs are designed to accept high-level audio signals: 2 Vrms or +8.2 dbu, which is the standard output level from CD and DVD players. The output level of some consumer audio devices is lower, often 0.316 Vrms or −10 dbV. Sound recordings made on the single processor Power Mac G5 with such low-level devices have more noise than those made with high-level devices. The user may obtain better results by connecting an amplifier between the low-level device and the computer's audio input jack.

The audio input jack is a 3.5 mm miniature phone jack with the signals connected as follows:

Tip	Left-channel audio
Ring	Right-channel audio
Sleeve	Audio ground

Based on a 1 kHz, 2VRMS sine wave input, 24-bit 44.1 kHz input format, 0 db input gain and no weighting, the line input has the following electrical characteristics:

■ maximum input voltage: 2 Vrms (+8.2 dBu)

■ input impedance: > 20 kilohms

■ bits per sample: 16-bit or 24-bit

■ channel separation: greater than 75 dB

■ frequency response: 20 Hz to 20 kHz, +0.5 dB/−3.0 dB

■ THD+N distortion: below < −78 dB (no weighting)

Audio Line-out

The single processor Power Mac G5 has a stereo output jack on the back of the enclosure. The audio output jack is suitable for connecting amplified external speakers or other high input impedance (> 1K ohms) audio equipment.

The audio output jack is a 3.5 mm miniature phone jack with the signals connected as follows:

Tip	Left-channel audio
Ring	Right-channel audio
Sleeve	Audio ground

> **Note:** For best results, headphones or other low-impedance devices should not be plugged into the line-out jack. Such devices should be plugged into the front panel headphone jack.

> **Note:** For best results, equipment plugged into the line-out jack should not connect the audio ground to other grounds, such as the chassis or "green wire" ground.

Based on a 1 kHz, -1 dBFS sine wave playback, 24-bit 44.1 kHz output format, -3 db output level and 100 kilohm load no weighting (unless otherwise specified below), the line output has the following electrical characteristics (nominal specifications):

- output voltage (full-scale output): 1.75 Vrms (+7.1 dBu)

- soutput impedance: <50 ohms

- bits per sample: 16-bit or 24-bit

- channel separation: > 65 dB

- THD+N distortion (total harmonic distortion plus noise): < −78dB (0.0125%)

- frequency response: 20 Hz to 20 kHz, +0.5 dB/−3.0 dB

Headphone Jack

The single processor Power Mac G5 has a stereo headphone jack on the front of the enclosure. The headphone jack is suitable for connecting a pair of standard headphones. When a plug is inserted into the headphone jack, the internal speaker is muted.

The headphone jack is a 3.5 mm miniature phone jack with the signals connected as follows:

Tip	Left-channel audio
Ring	Right-channel audio
Sleeve	Audio ground

> **Note:** For best results, equipment plugged into the headphone jack should not connect the audio ground to other grounds, such as the chassis or "green wire" ground.

Based on a 1 kHz sine wave playback, 24-bit 44.1 kHz output format, -3 dB output level and 100 kilohm load no weighting, (unless otherwise specified) the headphone output has the following electrical characteristics (nominal specifications):

- output voltage (full-scale output): 1.75 Vrms, (+7 dBu)

- output impedance: <50 ohms

- bits per sample: 16-bit or 24-bit

- channel separation: > 60 dB

- frequency response: 20 Hz to 20 kHz, +0.5 dB/−3.0 dB

- THD+N distortion (total harmonic distortion plus noise): < −75dB (0.017%)

Video Monitor Ports

The single processor Power Mac G5 computer comes with an Nvidia GeForce FX 5200 Ultra with 64 MB DDR SDRAM. The build-to-order graphics cards are described in "Graphics Cards" (page 27).

All of the graphics cards support dual displays in either extended desktop or video mirroring mode; for more detail, see "Dual Display Extended and Mirror Modes" (page 51).

The following sections describe the video connectors on the graphics cards.

Apple Display Connector

Some graphics cards have an Apple proprietary connector called the ADC (Apple display connector). The connector carries both digital and analog video signals as well as USB and control signals and power for an external monitor. Figure 3-6 (page 47) shows the contact configuration. Table 3-8 (page 47) lists the digital signals and pin assignments.

Table 3-9 (page 48) lists the analog signals and pin assignments.

The maximum current available from the 25 V supply for the external monitor is 3.6 A.
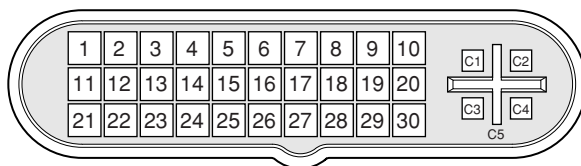
Figure 3-6   Apple display connector

Table 3-8   Digital signals on the Apple display connector

Pin	Signal name	Pin	Signal name
1	25 V Supply	16	TMDS Data1/3 Shield
2	25 V Supply	17	TMDS Data3−
3	LED	18	TMDS Data3+

Pin	Signal name	Pin	Signal name
4	TMDS Data0−	19	DDC CLock
5	TMDS Data0+	20	Clock Return
6	TMDS Data0/5 Shield	21	USB Data+
7	TMDS Data5−	22	USB Data−
8	TMDS Data5+	23	USB Return
9	DDC Data	24	TMDS Data2−
10	Vsync	25	TMDS Data2+
11	25 V Return	26	TMDS Data2/4 Shield
12	25 V Return	27	TMDS Data4−
13	Soft Power	28	TMDS Data4+
14	TMDS Data1−	29	Clock+
15	TMDS Data1+	30	Clock−

Table 3-9   Analog signals on the Apple display connector

Pin	Signal name
C1	Analog Blue Video
C2	Analog Green Video
C3	Analog Horizontal Sync
C4	Analog Red Video
C5	Analog RGB Return and DDC Return

The graphics data sent to the digital monitor use transition minimized differential signaling (TMDS). TMDS uses an encoding algorithm to convert bytes of graphics data into characters that are transition-minimized to reduce EMI with copper cables and DC-balanced for transmission over fiber optic cables. The TMDS algorithm also provides robust clock recovery for greater skew tolerance with longer cables or low-cost short cables. For additional information about TMDS, see the references shown in "Digital Visual Interface" (page 62).

DVI Connector

In addition to the ADC connector, some supported graphics cards also have a DVI-I (DVI with analog) connector; the Nvidia GeForce 6800 Ultra DDL and Nvidia GeForce 6800 GT DDL have two dual-link DVI connectors. With a DVI to ADC adapter, available separately and shown in Figure 3-7 (page 49), the DVI port on the single processor Power Mac G5 can also support any Apple flat panel display.

Figure 3-7   DVI to ADC adapter
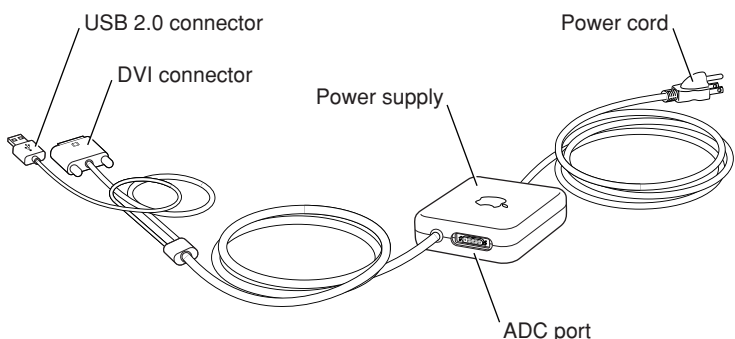
Figure 3-8 (page 49) shows the contact configuration for the DVI connector. Table 3-10 (page 49) lists the signals and pin assignments.

Figure 3-8   DVI connector
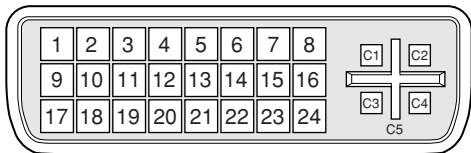
Table 3-10   Signals on the DVI connector

Pin	Signal name	Pin	Signal name
1	TMDS Data2–	13	TMDS Data3+
2	TMDS Data2+	14	+5V Power
3	TMDS Data2/4 Shield	15	Ground for +5V Power
4	TMDS Data4–	16	Hot Plug Detect
5	TMDS Data4+	17	TMDS Data0–
6	DDC Clock	18	TMDS Data0+
7	DDC Data	19	TMDS Data0/5 Shield
8	Analog vertical sync	20	TMDS Data5–

Pin	Signal name	Pin	Signal name
9	TMDS Data1–	21	TMDS Data5+
10	TMDS Data1+	22	TMDS Clock Shield
11	TMDS Data1/3 Shield	23	TMDS Clock+
12	TMDS Data3–	24	TMDS Clock–
C1	Analog red	C4	Analog horizontal sync
C2	Analog green	C5	Analog ground (analog R, G, B return)
C3	Analog blue		

The graphics data sent to the digital monitor use transition minimized differential signaling (TMDS). TMDS uses an encoding algorithm to convert bytes of graphics data into characters that are transition-minimized to reduce EMI with copper cables and DC balanced for transmission over fiber optic cables. The TMDS algorithm also provides robust clock recovery for greater skew tolerance with longer cables or low-cost short cables.

Note: The Nvidia GeForce 6800 Ultra DDL and Nvidia GeForce 6800 GT DDL have two dual-link DVI connectors. TMDS Data0, Data1, and Data2 support the first channel. TMDS Data3, Data4, and Data5 support the second channel.

For information about TMDS, see the reference listed in "Digital Visual Interface" (page 62).

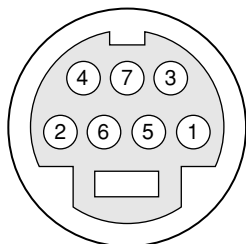**Note:** The single processor Power Mac G5 computer includes a DVI to VGA adapter.

DVI to Video Adapter

The single processor Power Mac G5 supports an optional DVI to S-video/composite adapter that provides S-video and composite output to a PAL or NTSC video monitor or VCR. When a display is connected by way of the video adapter, the computer detects the type of adapter and enables the composite and S-video outputs. The settings for the resolutions and standards (NTSC or PAL) are then selectable in the System Preferences Displays pane.

Note: The DVI to video adapter does not come packaged with the single processor Power Mac G5 computer and must be purchased separately.

The video output connector is a 7-pin S-video connector. (page 42) shows the arrangement of the pins and Table 3-11 (page 51) shows the pin assignments on the composite out and S-video connector.

Figure 3-9   S-video connector

Table 3-11   Pin assignments for the S-video output connector

Pin number	S-video output connector
1	Analog GND
2	Analog GND
3	Video Y (luminance)
4	Video C (chroma)
5	composite video
6	Unused
7	Unused

The single processor Power Mac G5 provides video output at picture sizes and frame rates compatible with the NTSC and PAL standards; the picture sizes are listed in Table 3-12 (page 51). Those picture sizes produce under-scanned displays on standard monitors.

Table 3-12   Picture sizes for S-video output

Picture size	Pixel depth
800 by 600	24 bpp
832 by 624	24 bpp
1024 by 768	24 bpp

Dual Display Extended and Mirror Modes

Some configurations of the single processor Power Mac G5 are equipped with a DVI port and an ADC port for connecting to an older generation Apple display. Configurations that use the build-to-order Nvidia GeForce 6800 Ultra DDL or Nvidia GeForce 6800 GT DDL graphics card have two dual-link DVI ports. A DVI to ADC adapter can be used to attach an ADC display to any DVI port.

Through the two graphics ports, the single processor Power Mac G5 supports dual displays in both extended desktop and video mirroring modes. Extended desktop mode lets users distribute work across two displays, increasing the amount of visible desktop space. Video mirroring mode displays the same information on both monitors, enabling the control of a presentation on one display, while allowing an audience to watch the presentation on a second display or projector. To power the new 30" Apple Cinema HD Display, a Nvidia GeForce 6800 GT DDL or Nvidia GeForce 6800 Ultra DDL is required. Both graphics cards are capable of supporting two 30" Apple Cinema HD Displays.

To switch between extended desktop and video mirroring modes, enable the "Mirror Displays" option on the Arrangement tab in the Displays pane of System Preferences.

The scaling function is available when both monitors (one via DVI and a second via ADC or DVI) are operating and the mirror mode is selected. Either monitor could have black borders during mirroring, depending on the supported timings between the two displays and on the monitor's selection algorithm. Both displays show full-sized images only when the resolutions match. Both displays can operate with other resolution settings, but in mirror mode, one of them has a display that is smaller than the full screen and has a black border around it.

Expansion

This chapter describes the expansion features of the single processor Power Mac G5 computer: the DDR SDRAM expansion slots and the PCI expansion slots.

> **Note:** For detailed instructions on accessing the expansion slots, refer to the *Power Mac G5 Setup Guide* that shipped with the computer.

RAM Expansion

The main logic board of the single processor Power Mac G5 computer has two pairs of DDR SDRAM expansion slots for unbuffered DDR400 (PC3200) dual in-line memory modules (DIMMs) for a maximum memory of 4 GB.

Each DIMM can contain 256 and 512 MB (also is capable of 128 MB and 1 GB) of double data rate synchronous dynamic RAM (DDR SDRAM). At least one pair of the RAM expansion slots contains factory installed DIMMs. The single processor Power Mac G5 supports CAS latencies of 2, 2.5, 3, 4, and 5.

Additional DIMMs must be installed in pairs of the same size. The slot pairs are numbered as indicated in Figure 4-1 (page 54). When installing additional memory, populate starting with the lowest slot pair numbers. The combined memory of all of the DIMMs installed is configured as a contiguous array of memory.

Figure 4-1   DIMM installation for single processor 1.8 GHz
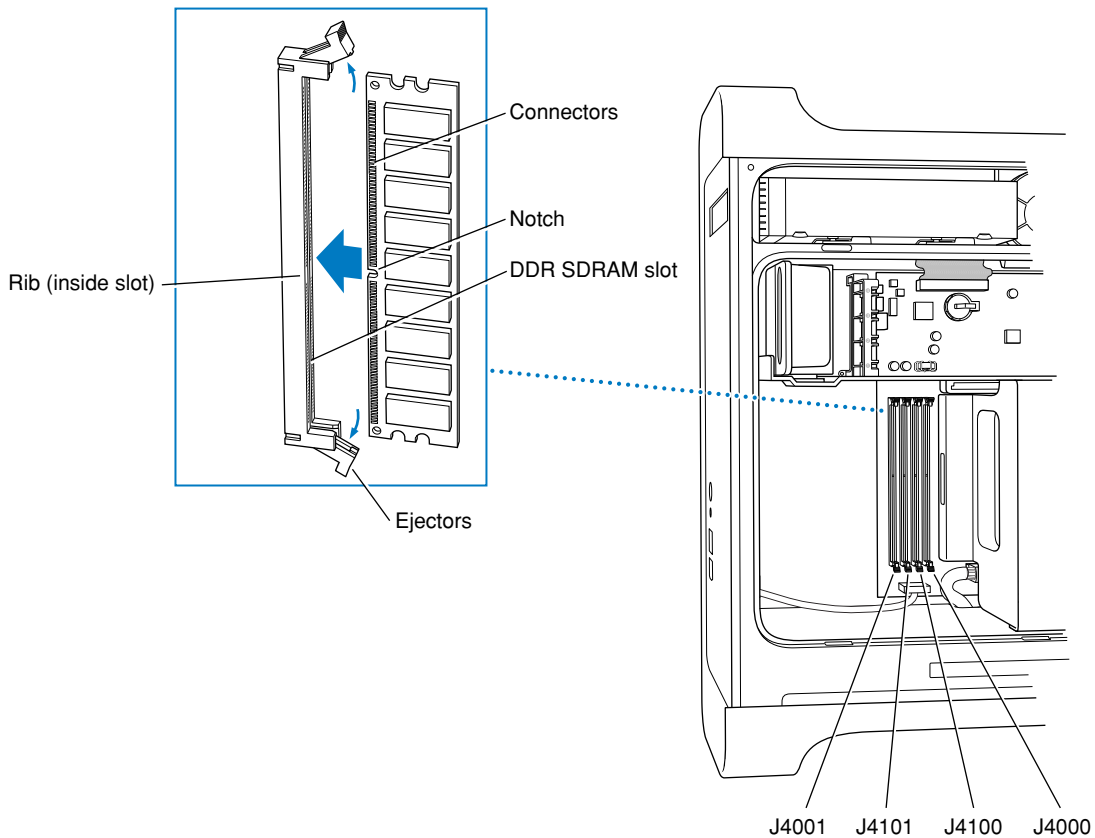
DIMM Specifications

The RAM expansion slots accept 184-pin DDR SDRAM DIMMs that are 2.5 volt, unbuffered, 8-byte, nonparity, and DDR400-compliant (PC3200).

> **Important:** DDR333 (PC2700) or slower DIMMs do not work in the single processor Power Mac G5 computer.

> **Important:** DIMMs with any of the following features are not supported in the single processor computer: registers or buffers, PLLs, ECC, parity, or EDO RAM.

Mechanical Specifications

The mechanical design of the DDR SDRAM DIMM is defined by the JEDEC specification JESD21-C, MODULES4_20_4, Release 11b. To find this specification on the World Wide Web, refer to "RAM Expansion Modules" (page 61).

The maximum height of DIMMs for use in the single processor Power Mac G5 is 2 inches.

Electrical Specifications

The electrical design of the SDRAM DIMM is defined by the JEDEC specification JESD21-C, MODULES4_20_4, Release 11b. To find this specification on the World Wide Web, refer to "RAM Expansion Modules" (page 61).

The Serial Presence Detect (SPD) EEPROM specified in the JEDEC standard is required and must be set to properly define the DIMM configuration. The EEPROM is powered on 3.3 V. Details about the required values for each byte on the SPD EEPROM can be found on pages 68–70 of the JEDEC specification.

Important: For a DIMM to be recognized by the startup software, the SPD feature must be programmed properly to indicate the timing modes supported by the DIMM.

DIMM Configurations

The largest DIMM supported is a two-bank DIMM of 1 GB using 512 Mbit DDR SDRAM devices. The maximum number of devices per DIMM is 16.

Important: Power is delivered to the single processor Power Mac G5 during sleep mode, so do not remove DIMMs while in sleep mode. A red light adjacent to the DIMM is illuminated when power is present.

Table 4-1 (page 55) shows information about the different sizes of DDR SDRAM devices used in the memory modules. The memory controller supports 64 Mbit, 128 Mbit, 256 Mbit, and 512 Mbit DDR SDRAM devices. The device configurations include three specifications: address range, word size, and number of banks. For example, a 1 M by 16 by 4 device addresses 1 M, stores 16 bits at a time, and has 4 banks.

The first column in Table 4-1 (page 55) shows the memory size of the largest DIMM with that device size that the computer can accommodate. The third column specifies the number of devices needed to make up the 8-byte width of the data bus. The fourth column in the table shows the size of each bank of devices, which is based on the number of internal banks in each device and the number of devices per bank.

Table 4-1   Sizes of DDR SDRAM expansion DIMMS and devices

Size of DIMM	SDRAM device size	Device configuration	Devices per bank	Size of each bank
128 MB	128 Mbits	4 M x 8 x 4	8	64 MB
128 MB	256 Mbits	2 M x 16 x 4	4	64 MB
256 MB	128 Mbits	4 M x 8 x 4	8	128 MB
256 MB	256 Mbits	4 M x 16 x 4	4	128 MB
512 MB	256 Mbits	8 M x 8 x 4	8	256 MB
1 GB	512 Mbits	16 M x 8 x 4	8	512 MB
1 GB	1 Gbits	16 M x 16x 4	4	512 MB
1 GB	1 Gbits	32 M x 8x 4	8	1 GB

RAM Addressing

Signals A[0–12] on each SDRAM DIMM make up a 13-bit multiplexed address bus that can support several different sizes of SDRAM devices. Table 4-2 (page 56) shows the address multiplexing modes used with the devices.

Table 4-2   Address multiplexing modes for SDRAM devices

Device size	Device configuration	Size of row address	Size of column address
128 Mbits	4 M x 8 x 4	12	10
128 Mbits	2 M x 16 x 4	12	9
256 Mbits	8 M x 8 x 4	13	10
256 Mbits	4 M x 16 x 4	13	9
512 Mbits	8 M x 8 x 4	14	10
1 Gb	16 M x 16x 4	14	10
1 Gb	32 M x 8x 4	14	11

PCI Expansion Slots

The single processor Power Mac G5 supports three 64-bit, 33 MHz PCI slots that interface to the Shasta I/O. The PCI configuration conforms to the PCI Specification 2.3.

To optimize performance of both PCI and main memory transfers, your PCI card should use DMA. The point-to-point G5 architecture is designed to provide optimal performance to each subsystem using DMA. If your card does not support DMA, it should use vector data types for read/write transfers. This approach results in 4 dword burst transfers on the PCI bus.

> **Important:** The single processor Power Mac G5 does not support marking memory space on PCI cards as cacheable. This technique was used in the past to improve read/write performance for cards without an on-board DMA engine. Cards using this technique will not function on the single processor Power Mac G5.

The computer's case has four openings in the back for access to I/O connectors on cards in the three expansion slots and the AGP slot. Each slot has room for a full size 12.335-inch or short 6.926-inch card. The numbering on the casing is 1 through 4 and corresponds to the label on the PCB. Number one is the AGP slot and numbers two through four are the PCI slots.

> **Note:** The single processor Power Mac G5's build-to-order ATI Radeon 9800 XT, Nvidia GeForce 6800 GT DDL, and Nvidia GeForce 6800 Ultra DDL graphics cards occupy both the AGP slot and the adjacent PCI slot. As a result, you can install PCI expansion cards in the slots labeled 3 and 4 but not in slot 2. To learn more about installing PCI cards, refer to "Chapter 3, Working Inside Your Computer" in the *Power Mac G5 User's Guide* that came with your computer.

The expansion slots accept PCI cards with either 32-bit or 64-bit address and data buses. The expansion slots support universal and +3.3 V cards, but not 5 V signalling. The cards are required to use the standard PCI fence described in the specification.

> **Note:** 5 V keyed or signalling cards do not work in the Power Mac G5 computer.

The expansion slots support all the required PCI signals and certain optional PCI signals. The PCI slots support the optional 64-bit bus extension signals and cache support signals.

The PCI slots and the AGP Pro 8x slot carry the 3.3 V_AUX power and PME signals to allow an expansion card to wake the computer from sleep mode.

A total of 90 W is allocated to the three PCI slots and the AGP Pro slot. Graphics cards that conform to the AGP 3.0 spec can consume up to 30W. AGP Pro cards may consume more power. AGP Pro cards may also block adjacent PCI slots, in order to provide thermal relief.

> ⚠ **Warning:** Before removing or installing PCI expansion cards, shut down the computer and unplug the AC power cord. The single processor Power Mac G5 computer does not support PCI hot-plugging functionality. When the computer is powered-on or asleep, a red light on the main logic board is illuminated when the duct door is removed and power is present.

In order to use the new PCI card, a driver must be installed. The driver installation procedure is documented by the manufacturer of the PCI card.

For more information on PCI, visit the worldwide web at

http://www.pci-sig.org

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APPENDIX A

Supplemental Reference Documents

For more information about the technologies mentioned in this developer note, you may wish to consult some of the references listed in the following sections.

For information about older models of Macintosh computers, refer to the developer notes archive at

http://developer.apple.com/documentation/Hardware/hardware2.html

Apple Technical Notes

Apple Technical Notes answer many specific questions about the operation of Macintosh computers and the Mac OS. The notes are available on the Technical Note website at

http://developer.apple.com/technotes/

PowerPC G5 Microprocessor

Information about the PowerPC G5 microprocessor is available on the World Wide Web at

http://www-306.ibm.com/chips/techlib/techlib.nsf/techdocs/AE818B5D1DBB02EC87256DDE00007821

Velocity Engine

Velocity Engine is Apple's vector processor in the PowerPC G5 microprocessor. Apple provides support for developers who are starting to use the Velocity Engine in their applications. Documentation, development tools, and sample code are available on the Apple website, at

http://developer.apple.com/hardwaredrivers/ve/index.html

Freescale's implementation of Apple's Velocity Engine is named AltiVec. The *AltiVec Technology Programming Environments Manual* (AltiVec PEM) is a reference guide for programmers. It contains a description for each instruction and information to help in understanding how the instruction works. Obtain a copy of the AltiVec PEM through the Freescale documentation site on the World Wide Web by searching for ALTIVECPEM/D.

http://www.freescale.com/

3D Graphics

Developers of 3D graphics for games should know about OpenGL for Macintosh, a version of SGI's application programming interface (API) and software library for 3D graphics.

Developer support and documentation is available at

http://developer.apple.com/opengl/

Mac OS X

The single processor Power Mac G5 computer ships with Mac OS X 10.3.5 installed as the default operating system. To run Mac OS 9 applications, install the Classic application from the "Additional Software and Apple Hardware Test" CD shipped with your computer. For access to Apple's developer documentation for Mac OS X, see the Apple Developer Connection (ADC) website at

http://developer.apple.com/documentation/MacOSX/MacOSX.html

O'Reilly & Associates publishes a series of books about Mac OS X development. The books in this series have been technically reviewed by Apple engineers and are recommended by the Apple Developer Connection.

Open Firmware

The software architecture implemented on current Macintosh computers follows the standard defined by the Open Firmware IEEE 1274-1994 specification. Three Technotes provide an introduction to Open Firmware on the Macintosh platform. They are

TN 1061: *Open Firmware, Part I*, available on the Technote web site at

http://developer.apple.com/technotes/tn/tn1061.html

TN 1062: *Open Firmware, Part II*, at

http://developer.apple.com/technotes/tn/tn1062.html

TN 1044: *Open Firmware, Part III*, at

http://developer.apple.com/technotes/tn/tn1044.html

Other Technotes provide additional information about Open Firmware on the Macintosh.

TN 2000: *PCI Expansion ROMs and You*, at

http://developer.apple.com/technotes/tn/tn2000.html

TN 2001: *Running Files from a Hard Drive in Open Firmware*, at

http://developer.apple.com/technotes/tn/tn2001.html

TN 2004: *Debugging Open Firmware Using Telnet*, at

http://developer.apple.com/technotes/tn/tn2004.html

RAM Expansion Modules

The single processor Power Mac G5 computer uses DDR400 PC3200-compliant, 184-pin DDR SDRAM DIMMs.

The electrical and mechanical characteristics of the DIMM are given in JEDEC Standard 21-C. The specification can be found by using the search string JESD21-C on the Electronics Industry Association's website at

http://www.jedec.org/DOWNLOAD/default.cfm

ATA Devices

ATA (AT Attachment), also referred to as integrated drive electronics (IDE), is a standard interface used with storage devices such as hard disk drives. For more information on ATA, refer to the following Apple website at

http://developer.apple.com/documentation/Hardware/DeviceManagers/ata/ata.html

ATA Manager 4.0 supports driver software for internal IDE drives and includes DMA support. For the latest information about ATA Manager 4.0, see Technical Note TN1098, ATA Device Software Guide Additions and Corrections, available on the world wide web at

http://developer.apple.com/technotes/tn/tn1098.html

The web page for Technical Note TN1098 includes a link to a downloadable copy of *ATA Device Software Guide*.

Information about the ATA standards is available at the Technical Committee T13 AT Attachment website, at

http://www.t13.org/

Ethernet

For information on Ethernet specifications and design guides, go to the World Wide Web at

http://standards.ieee.org/

Serial ATA

For information on Serial ATA specifications and design guides, go to the World Wide Web at

http://www.serialata.org

USB Interface

For more information about USB on the Macintosh computer, refer to Apple Computer's *Accessing Hardware from Applications* at

http://developer.apple.com/hardwaredrivers/usb/index.html

Mac OS X includes support for a wide variety of USB classes. Detailed specifications of these classes are available at

http://www.usb.org/developers/devclass/

For full specifications of the Universal Serial Bus, refer to the USB Implementation Forum on the World Wide Web, at

http://www.usb.org/developers/docs

FireWire Interface

For additional information about the FireWire IEEE 1394a and 1394b interfaces and the Apple API for FireWire software, refer to the resources available on the Apple FireWire website at

http://developer.apple.com/hardwaredrivers/firewire/index.html

The IEEE 1394a and 1394b standards are available from the IEEE; you can order these documents electronically from the IEEE Standards Department website at

http://shop.ieee.org/store

You may also find useful information at the 1394 trade association's website at

http://www.1394ta.org/

Digital Visual Interface

For information about transition minimized differential signaling (TMDS) used with digital video monitors, see the specification, Digital Visual Interface DVI Revision 1.0, available on the website of the Digital Display Working Group (DDWG) at

http://developer.intel.com/technology/memory/

Wireless Networks

More information about Wi-Fi and wireless networks using the IEEE 802.11 standard is available on the website of the WiFi Alliance, at

http://www.wi-fi.org

Bluetooth

For more information regarding Bluetooth technology, refer to the following locations on the World Wide Web.

Bluetooth specification:

http://www.bluetooth.org/spec/

Bluetooth SIG:

http://www.bluetooth.org

Bluetooth developer tools on the Apple web site at:

http://developer.apple.com/hardwaredrivers/bluetooth/

Conventions and Abbreviations

This developer note uses the following typographical conventions and abbreviations.

Typographical Conventions

> **Note:** A note like this contains information that is of interest but is not essential for an understanding of the text.

> **Important:** A note like this contains important information that you should read before proceeding.

> ⚠ **Warning:** A note like this contains a warning about a situation that could cause a system crash or loss of data.

Abbreviations

When unusual abbreviations appear in this developer note, the corresponding terms are also spelled out. Standard units of measure and other widely used abbreviations are not spelled out.

Here are the standard units of measure used in developer notes:

A	amperes		mA	milliamperes
dB	decibels		A	microamperes
Gb	gigabits		Mb	megabits
GB	gigabytes		MB	megabytes
Hz	hertz		MHz	megahertz
in.	inches		mm	millimeters
k	1000		ms	milliseconds
K	1024		s	microseconds
KB	kilobytes		ns	nanoseconds

kg	kilograms			ohms
kHz	kilohertz		sec.	seconds
k	kilohms		V	volts
lb.	pounds		W	watts

Other abbreviations used in developer notes include these:

ADC	Apple Display Connector
AGP	accelerated graphics port
ATA	advanced technology attachment
ATAPI	advanced technology attachment, packet interface
AV	audio visual
CAS	column address strobe
CAV	constant angular velocity
CD-ROM	compact disc read-only memory
CLV	constant linear velocity
DB-DMA	descriptor-based direct memory access
DDC	display data channel
DDR	double data rate, a type of SDRAM where data is clocked on rising and falling clock edges
DIMM	dual inline memory module
DIN	Deutsche Industrie Norm
DMA	direct memory access
DRAM	dynamic random-access memory
DVD	12 cm optical storage system with 4 GB capacity
DVD-ROM	DVD read-only memory
DVD-RAM	DVD that is both readable and writable
DVI	Digital Visual Interface
EDO	extended data out DRAM device type
EHCI	Enhanced Host Controller Interface, USB 2.0 specification
EIDE	extended IDE

EMI	electromagnetic interference
G5	Generation 5, the fifth generation of PowerPC microprocessors, with Velocity Engine technology
GBps	data throughput rate — giga bytes per second
Gbps	data throughput rate — giga bits per second
HID	human interface device, a class of USB devices
I2C	same as IIC
I2S	same as IIS
IC	integrated circuit
IDE	integrated device electronics
IEEE	Institute of Electrical and Electronics Engineers
IEEE 1275	the official specification for Open Firmware
IEEE 1394a	the official specification for FireWire 400 (400 Mbps)
IEEE 1394b	the official specification for FireWire 800 (800 Mbps)
IIC	inter-IC (an internal control bus)
IIS	inter-IC sound bus
I/O	input/output
ISO	International Organization for Standardization
JEDEC	Joint Electronics Devices Engineering Council
L2	level 2 (refers to level of cache)
L3	level 3 (refers to level of cache)
LAN	local area network
MAC	media access controller
Mac OS	Macintosh Operating System
MBps	data throughput rate — mega bytes per second
Mbps	data throughput rate — mega bits per second
OFDM	orthogonal frequency-division multiplexing
OHCI	Open Host Controller Interface, USB 1.1 specification
PCI	Peripheral Component Interconnect

PHY	physical layer
PIO	programmed input/output
RADIUS	Remote Authentication Dial-In User Service
RAM	random-access memory
RAS	row address strobe
RBC	reduced block commands
RGB	a video signal format with separate red, green, and blue components
RISC	reduced instruction set computing
RMS	root mean square
ROM	read-only memory
SATA	serial ATA bus for hard disk drives
SBP	Serial Bus Protocol
SPD	Serial Presence Detect
SCSI	Small Computer System Interface
SCC	serial communications controller
SDR	single data rate
SDRAM	synchronous dynamic random access memory
Shasta	I/O device and disk controller
SMU	system management unit
SO-DIMM	small outline - dual inline memory module
SRAM	static random access memory
U3L	memory controller lite version
USB	Universal Serial Bus
TMDS	transition minimized differential signaling
VESA	Video Electronics Standards Association
Vesta	Ethernet and Firewire PHY
VRAM	video RAM; used for display buffers
Wi-Fi	Wireless Fidelity. Used by the Wireless Ethernet Compatibility Alliance for certification of interoperability of 802.11 products

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