When reading the Instruction tables, please regard the BLG212_B5 Edu_CPU
ENG-to-TUR.pdf file for Turkish -> English equivalents

The notation used in the Instruction table:
Ki : 8-bit register, first operand
Kj : 8-bit register, second operand
Kii : 16-bit register, first operand
Kjj : 16-bit register, second operand
V   : 8-bit data
VV : 16-bit data
Veri : Data
Adr (Yuk) : Memory address (H)
Adr (Dus) : Memory address (L)
S   : Index for IX or SP addressing
R   : Increment or decrement for IX addressing