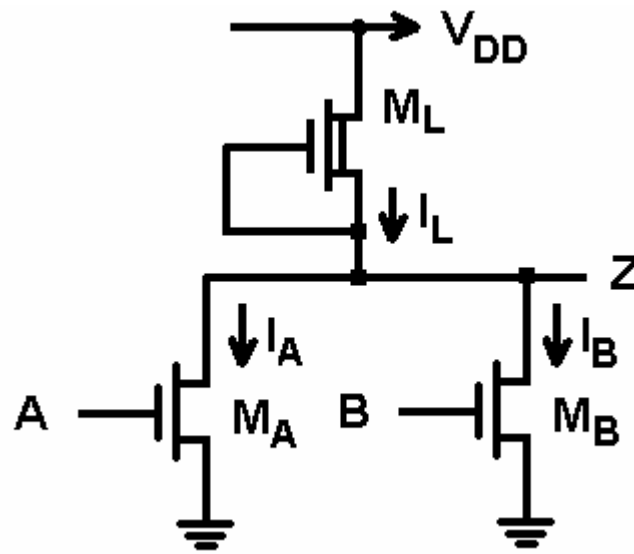
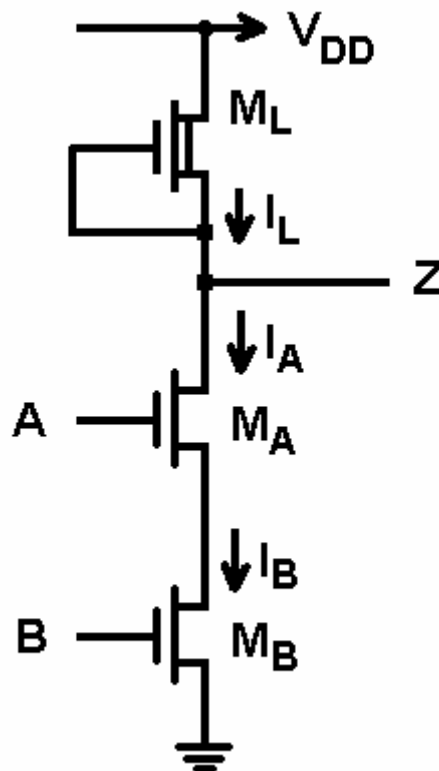


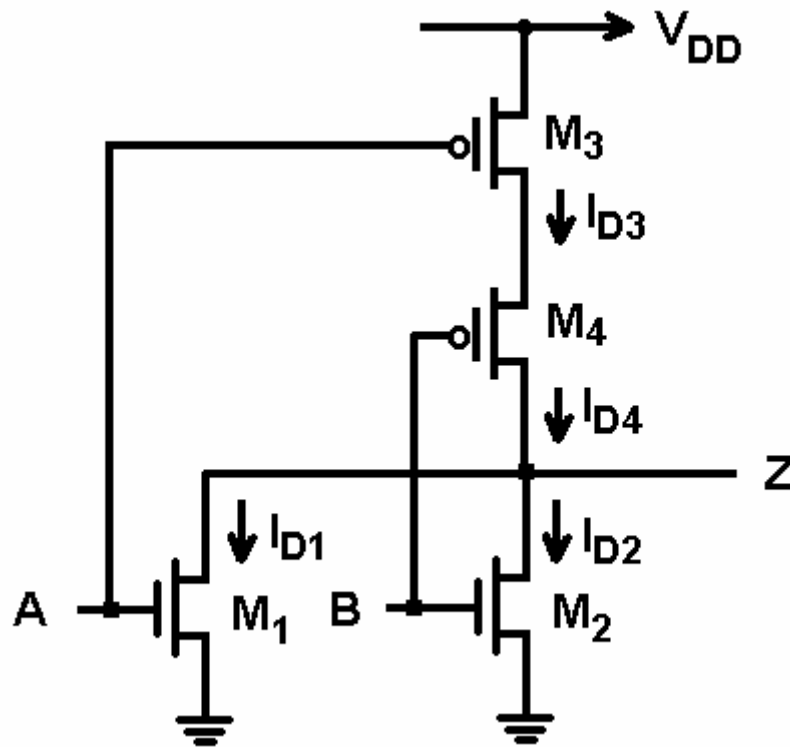
2-input NMOS NOR gate



2-input NMOS NAND gate



2-input CMOS NOR gate



2-input CMOS NAND gate

