

Positive and Negative Logic

Boolean values zero and one represent physical quantities such as voltage or state of an entity (door is open, light is off).

Assigning "1" to high value, and "0" to low value is called positive logic, and assigning "0" to high value, and "1" to low value is called negative logic.

Example:

Digital Circuits

Function table of a physical device with 2 inputs and one output is shown below.

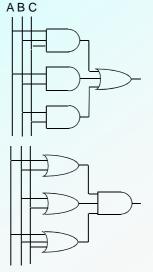
If we use the positive logic the device can be implemented with an AND gate.

In negative logic system the device is implemented with an OR gate.

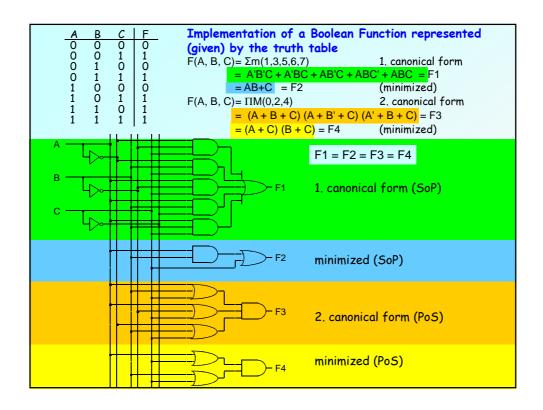
	Physical Device				Positive Logic			Negative Logic				
	Inputs:		Output:	Inp	uts:	Output	:	Inputs:		Output:		
	x1	x2	Z	×1	x2	Z		×1	x2	Z		
	L	L	L	0	0	0		1	1	1		
	L	Н	L	0	1	0		1	0	1		
	Н	L	L	1	0	0		0	1	1		
	Н	Н	Н	1	1	1		0	0	0		
	http://www.faculty.itu.edu.tr/buzluca © 2011 Dr. Feza BUZLUCA 3.5											

Implementation of Boolean Functions Using Logic Gates ABC Sum of Products (SoP) AND gates implement the products.

- OR gate implements the sum.
- Product of Sums (PoS)
 - OR gates implement the sums.
 - AND gate implements the product.



NOT gates can be also used, where they are necessary.

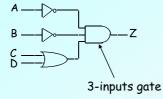


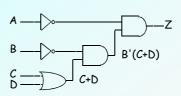


License: http://creativecommons.org/licenses/by-nc-nd/3.0/

As a Boolean function has many different expressions it can be implemented in different ways using different logic gates.

Example:
$$Z = A' \cdot B' \cdot (C + D) = (A' \cdot (B' \cdot (C + D)))$$





Only 2-input gates

Sometimes it would be necessary to manipulate logical expressions of functions according to currently available gates.

Functional completeness and Universal Gates

A functionally complete set of logical operators is one, which can be used to express all possible truth tables by combining members of the set.

From the definition of the Boolean algebra AND, OR, NOT is a compete set.

- The set consisting only of the binary operator NAND is also functionally complete.
- NAND gates alone can be used to reproduce the functions of all the other logic gates.
- By using only NAND gates all logic functions can be implemented.
- Similarly, the set consisting only of the binary operator NOR is also functionally complete.
- NOR gates alone can be used to reproduce the functions of all the other logic gates.

NAND (and also OR) gates are also called universal logic gates.

http://www.faculty.itu.edu.tr/buzluca http://www.buzluca.info

© 2011 Dr. Feza BUZLUCA

3.9

Digital Circuits

Proof of completeness

To prove that NAND and NOR operators are functionally complete, we have to show that AND, OR, NOT operations can be implemented by using only NAND (or alternatively NOR) gates.

NAND is denoted by symbol |

NOR is denoted by symbol \downarrow

•NOT: x'=x | x = (x·x)' = x' • AND:

 $x \cdot y = (x \mid y)'$ $x \cdot y = (x' \downarrow y')$ de Morgan $x + y = (x' \mid y')$ de Morgan $x + y = (x \downarrow y)'$

http://www.faculty.itu.edu.tr/buzluca

http://www.buzluca.info

• OR:

© 2011 Dr. Feza BUZLUCA

NOR

3.10

Relation between NAND and NOR

- NAND NOR Conversions
 - de Morgan:
- 1. $(A + B)' = A' \cdot B'$
- 2. (A B)' = A' + B'
- 3. $(A' \bullet B')' = A + B$
- 4. $(A' + B')' = (A \cdot B)$
- These expressions show that:
- 1. An AND gate with inverted inputs is the equivalent of the NOR gate.
- 2. An OR gate with inverted inputs is the equivalent of the NAND gate.
- 3. A NAND gate with inverted inputs is the equivalent of the OR gate.
- 4. A NOR gate with inverted inputs is the equivalent of the AND gate.









http://www.faculty.itu.edu.tr/buzluca

ttp://www.buzluca.info

© 2011 Dr. Feza BUZLUCA 3.11

Digital Circuits

Implementation of Boolean functions using only NAND (NOR) gates

As the binary operator NAND is functionally complete all Boolean functions can be implemented by using only NAND gates.

The same property is also valid for NOR gates.

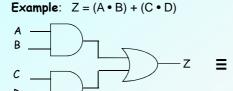
Implementation of Boolean functions in the SOP form using only NAND gates

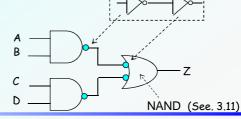
Shortcut: In circuits in the SOP form all AND gates and OR gates can be replaced with NAND gates. These changes do not affect the output.

Details:

If we put NOT gates to the outputs of AND gates, and to the inputs of the OR gates we obtain NAND gates. (See 3.11)

If we connect two NOT gates one after the other the truth table of the function does not change. (a')'=a (Involution)

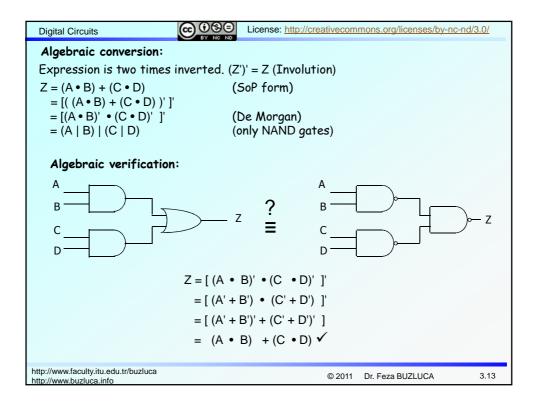


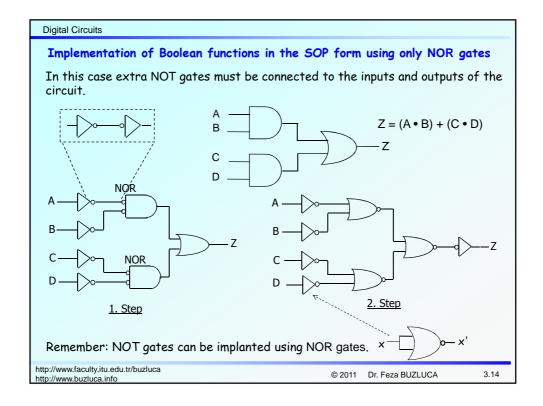


http://www.faculty.itu.edu.tr/buzluca

http://www.buzluca.info

© 2011 Dr. Feza BUZLUCA





Implementation of Boolean functions in the POS form using only NOR gates

Shortcut: In circuits in the POS form all AND gate and OR gates can be replaced with NOR gates.

Details:

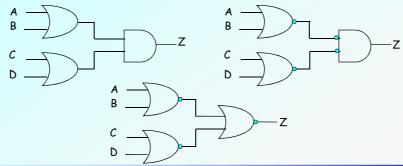
If we put NOT gates to the outputs of OR gates, and to the inputs of the AND gates we obtain NOR gates. (See 3.11)

Remember: If we connect two NOT gates one after the other, the truth table of the function does not change. (a')'=a (Involution)

Example: $Z = (A + B) \cdot (C + D)$

http://www.faculty.itu.edu.tr/buzluca

http://www.buzluca.info



© 2011 Dr. Feza BUZLUCA

3.15