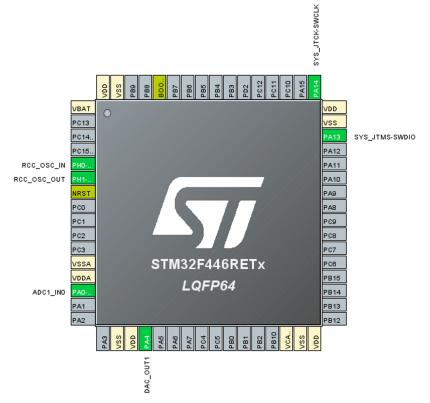
ADC Değeri İle DAC Kontrolü

25 Aralık 2021 Cumartesi 00:57

ADC Değeri İle DAC Kontrolü

> HAL

Konfigürasyon Kısmı



Pin Na 🌲	Signal on	GPIO outp	GPIO mode	GPIO Pull	<mark>Maxim</mark>	um l	Jser La	abel M	odified
PA0-WK	ADC1_IN0	n/a	Analog m	No pull-up	n/a				
Pin Na 🌻	Signal	on Pin G	PIO out G	PIO mode	GPIO P	Maxim	num l	Jser L	Modified
PA4	DAC_OUT1		/a A	nalog m	No pull	n/a			

• ADC1 için INO, DAC için OUT1 seçilir.

ADC_Settings

Clock Prescaler PCLK2 divided by 2

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment

Scan Conversion Mode Disabled
Continuous Conversion Mode Enabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

DAC Out1 Settings

Output Buffer Enable
Trigger None

Kod Kısmı

```
23⊖ /* Private includes ------
24 /* USER CODE BEGIN Includes */
```

25 uint16_t adc_value, dac_value;

26 /* USER CODE END Includes */

• 16 bitlik okuma yapan fonksiyon yazdık.

```
65
         if(HAL_ADC_PollForConversion(&hadc1, 100000) == HAL_OK)
66
67
             adc_value = HAL_ADC_GetValue(&hadc1);
68
69
70
         HAL_ADC_Stop(&hadc1);
71
72
73
         return adc_value;
74 }
75 /* USER CODE END 0 */
      /* USER CODE BEGIN 2 */
108
      HAL_DAC_Start(&hdac, DAC_CHANNEL_1);
      /* USER CODE END 2 */
109
     /* Infinite loop */
111
      /* USER CODE BEGIN WHILE */
112
113
     while (1)
114
115
       /* USER CODE END WHILE */
116
117
       /* USER CODE BEGIN 3 */
118
          dac_value=Read_ADC();
119
          HAL_DAC_SetValue(&hdac, DAC_CHANNEL_1, DAC_ALIGN_12B_R, dac_value);
120
121 /* USER CODE END 3 */
122 }
```